



# **GOVERNMENT COLLEGE OF TECHNOLOGY**

*(An Autonomous Institution Affiliated to Anna University)*

**Coimbatore – 641 013**

**2023**

**CURRICULAM & SYLLABI**

**DEPARTMENT OF ELECTRONICS AND  
COMMUNICATION ENGINEERING**

**M.E. APPLIED ELECTRONICS – FULL TIME**

# **GOVERNMENT COLLEGE OF TECHNOLOGY**

**(An Autonomous Institution affiliated to Anna University)**

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## **VISION AND MISSION OF THE INSTITUTION**

### ***VISION***

To emerge as a centre of excellence and eminence by imparting futuristic technical education in keeping with global standards, making our students technologically competent and ethically strong so that they can readily contribute to the rapid advancement of society and mankind.

### ***MISSION***

- To achieve academic excellence through innovative teaching and learning practices.
- To enhance employability and entrepreneurship.
- To improve the research competence to address societal needs.
- To inculcate a culture that supports and reinforces ethical, professional behaviours for a harmonious and prosperous society.



## **DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

### **VISION AND MISSION OF THE ELECTRONICS AND COMMUNICATION ENGINEERING DEPARTMENT**

#### **VISION**

The vision of ECE department is to become pioneer in higher learning and research and to produce creative solution to societal needs.



#### **MISSION**

- To provide excellence in education, research and public service
- To provide quality education and to make the students entrepreneur and employable.
- Continuous upgradation of techniques for reaching heights of excellence in a Global Perspective.

**CHOICE BASED CREDIT SYSTEM**  
**BRANCH: M.E. APPLIED ELECTRONICS- FULL TIME**

**PROGRAMME EDUCATIONAL OBJECTIVES :**

PEO1: To critically evaluate the design and provide optimal solution to the problems in Advanced Signal Processing, Communications, Digital system Design, Embedded Systems and VLSI Design.

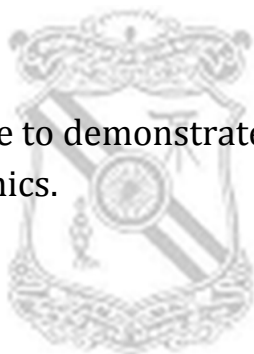
PEO2: To develop electronic systems using Modern engineering tools.

PEO3: To work professionally and ethically in Applied electronics and allied areas.



## **PROGRAM OUTCOMES**

- PO1: Ability to apply the knowledge of mathematics and engineering principles for developing problem solving attitude in field of Applied electronics for analysis and synthesis.
- PO2: Ability to design and Interpret the data using modern tools in the domain of Applied electronics.
- PO3: To work professionally and ethically in Applied electronics and allied areas for societal needs.
- PO4: An ability to carry out research to solve the practical problems.
- PO5: Ability to present a substantial technical report in the field of Applied electronics.
- PO6: Students should be able to demonstrate a degree of mastery in the field of Applied electronics.



**CHOICE BASED CREDIT SYSTEM**  
**CURRICULUM FOR CANDIDATES ADMITTED DURING 2023 ONWARDS**  
**FIRST SEMESTER**

Sl. No.	Course Code	Course Title	Category	CA Marks	End Sem Marks	Total Marks	Hours/Week /Credits			
							L	T	P	C
THEORY										
1	23AEFCZ1	RESEARCH METHODOLOGY AND IPR <i>(Common to all Branches)</i>	FC	40	60	100	3	0	0	3
2	23AEFC02	ADVANCED APPLIED MATHEMATICS <i>(Common to Applied Electronics &amp; VLSI Design)</i>	FC	40	60	100	3	1	0	4
3	23AEPC01	ADVANCED DIGITAL SYSTEM DESIGN <i>(Common to Applied Electronics &amp; VLSI Design)</i>	PC	40	60	100	3	0	0	3
4	23AEPC02	DSP ARCHITECTURES AND ALGORITHMS	PC	40	60	100	3	0	0	3
5	23AEPC03	STATISTICAL SIGNAL PROCESSING	PC	40	60	100	3	0	0	3
6	23AEPEXX	PROFESSIONAL ELECTIVE- I	PE	40	60	100	3	0	0	3
7	23AEACXX	AUDIT COURSE -I	AC	40	60	100	2	0	0	0
PRACTICAL										
8	23AEPC04	ADVANCED DIGITAL SYSTEM DESIGN LABORATORY	PC	60	40	100	0	0	4	2
		Total		340	460	800	20	1	4	21

**SECOND SEMESTER**

Sl. No.	Course Code	Course Title	Category	CA Marks	End Sem Marks	Total Marks	Hours/Week			
							L	T	P	C
THEORY										
1	23AEPC05	INDUSTRIAL IOT	PC	40	60	100	3	0	0	3
2	23AEPC06	EMBEDDED SYSTEM DESIGN	PC	40	60	100	3	0	0	3
3	23AEPEXX	PROFESSIONAL ELECTIVE-II	PE	40	60	100	3	0	0	3
4	23AEPEXX	PROFESSIONAL ELECTIVE-III	PE	40	60	100	3	0	0	3
5	23AEACXX	AUDIT COURSE-II	AC	40	60	100	2	0	0	0
THEORY WITH PRACTICAL COMPONENT										
6	23AEPC07	DIGITAL IMAGE PROCESSING AND ITS APPLICATIONS	PC	50	50	100	3	0	2	4
PRACTICAL										
7	23AEPC08	EMBEDDED SYSTEM DESIGN LABORATORY	PC	60	40	100	0	0	4	2
8	23AEEE01	MINI PROJECT	EEC	60	40	100	0	0	4	2
		Total		370	430	800	17	0	10	20

### THIRD SEMESTER

Sl. No.	Course Code	CourseTitle	Cate gor y	CA Marks	End Sem Marks	Total Marks	Hours/Week			
							L	T	P	C
THEORY										
1	23AEPEXX	PROFESSIONAL ELECTIVE IV	PE	40	60	100	3	0	0	3
2	23AEOEXX	OPEN ELECTIVE	OE	40	60	100	3	0	0	3
PRACTICAL										
3	23AEEEE02	INTERNSHIP/INDUSTRIAL TRAINING	EEC	100	-	100	-	-	**	2
4	23AEEEE03	PROJECT-I	EEC	60	40	100	0	0	24	12
		Total		240	160	400	6	0	24	20

\*\*4 Weeks Internship/Industrial Training

### FOURTH SEMESTER

Sl. No.	Course Code	CourseTitle	Category	CA Marks	End Sem Marks	Total Marks	Hours/Week			
							L	T	P	C
PRACTICAL										
1	23AEEEE04	PROJECT-II	EEC	60	40	100	-	-	*	24
		Total		60	40	100	-	-	*	24

**Note :** \* Maximum number of periods 720 to earn 24 credits shall be scheduled during the maximum period of 6 months.

**Total Credits:85**

### PROFESSIONALELECTIVE (PE)

Sl. No.	Course Code	Course Title	Category	CA Marks	End Sem Marks	Total Marks	Hours/Week /Credits			
							L	T	P	C
PROFESSIONAL ELECTIVE I										
1	23AEPE01	DIGITAL IC DESIGN <i>(Common to Applied Electronics &amp; VLSI Design)</i>	PE	40	60	100	3	0	0	3
2	23AEPE02	ANALYSIS AND DESIGN OF ANALOG INTEGRATED CIRCUITS <i>(Common to Applied Electronics &amp; VLSI Design)</i>	PE	40	60	100	3	0	0	3
3	23AEPE03	SEMICONDUCTOR DEVICE MODELING	PE	40	60	100	3	0	0	3
4	23AEPE04	SMART SENSORS	PE	40	60	100	3	0	0	3
5	23AEPE05	MULTIMEDIA COMPRESSION TECHNIQUES	PE	40	60	100	3	0	0	3
PROFESSIONAL ELECTIVE II										
6	23AEPE06	ANALOG IC DESIGN <i>(Common to Applied Electronics &amp; VLSI Design)</i>	PE	40	60	100	3	0	0	3
7	23AEPE07	EMI AND COMPATIBILITY	PE	40	60	100	3	0	0	3
8	23AEPE08	ADVANCED COMMUNICATION SYSTEMS	PE	40	60	100	3	0	0	3
9	23AEPE09	MEMS AND NEMS	PE	40	60	100	3	0	0	3
10	23AEPE10	SOFT COMPUTING AND OPTIMIZATION TECHNIQUES	PE	40	60	100	3	0	0	3



PROFESSIONAL ELECTIVE III										
11	23AEPE11	MODELING AND SYNTHESIS WITH HDL	PE	40	60	100	3	0	0	3
12	23AEPE12	RF SYSTEM DESIGN	PE	40	60	100	3	0	0	3
13	23AEPE13	ADVANCED COMPUTER ARCHITECTURE AND PARALLEL PROCESSING	PE	40	60	100	3	0	0	3
14	23AEPE14	EMBEDDED PROCESSORS	PE	40	60	100	3	0	0	3
15	23AEPE15	BIO-MEDICAL IMAGE PROCESSING	PE	40	60	100	3	0	0	3
PROFESSIONAL ELECTIVE IV										
16	23AEPE16	LOW POWER IC DESIGN (Common to Applied Electronics & VLSI Design)	PE	40	60	100	3	0	0	3
17	23AEPE17	VLSI SIGNAL PROCESSING (Common to Applied Electronics & VLSI Design)	PE	40	60	100	3	0	0	3
18	23AEPE18	ASIC DESIGN (Common to Applied Electronics & VLSI Design)	PE	40	60	100	3	0	0	3
19	23AEPE19	REAL TIME OPERATING SYSTEM	PE	40	60	100	3	0	0	3
20	23AEPE20	ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING	PE	40	60	100	3	0	0	3

### LIST OF OPEN ELECTIVES

Sl. No	Course Code	Course Title	Category	CA Marks	End Sem Marks	Total Marks	Hours/Week			
							L	T	P	C
1	23SEOE01	BUILDING BYE-LAW AND CODES OF PRACTICE	OE	40	60	100	3	0	0	3
2	23SEOE02	PLANNING OF SMART CITIES	OE	40	60	100	3	0	0	3
3	23SEOE03	GREEN BUILDING	OE	40	60	100	3	0	0	3
4	23EEOE04	ENVIRONMENT HEALTH AND SAFETY MANAGEMENT	OE	40	60	100	3	0	0	3
5	23EEOE05	CLIMATE CHANGE AND ADAPTATION	OE	40	60	100	3	0	0	3
6	23EEOE06	WASTE TO ENERGY	OE	40	60	100	3	0	0	3
7	23GEOE07	ENERGY IN BUILT ENVIRONMENT	OE	40	60	100	3	0	0	3
8	23GEOE08	EARTH AND ITS ENVIRONMENT	OE	40	60	100	3	0	0	3
9	23GEOE09	NATURAL HAZARD AND MITIGATION	OE	40	60	100	3	0	0	3
10	23EDOE10	BUSINESS ANALYTICS	OE	40	60	100	3	0	0	3
11	23EDOE11	INTRODUCTION TO INDUSTRIAL SAFETY	OE	40	60	100	3	0	0	3
12	23EDOE12	OPERATIONS RESEARCH	OE	40	60	100	3	0	0	3
13	23MFOE13	OCCUPATIONAL HEALTH AND SAFETY	OE	40	60	100	3	0	0	3
14	23MFOE14	COST MANAGEMENT OF ENGINEERING PROJECTS	OE	40	60	100	3	0	0	3
15	23MFOE15	COMPOSITE MATERIALS	OE	40	60	100	3	0	0	3
16	23TEOE16	GLOBAL WARMING SCIENCE	OE	40	60	100	3	0	0	3
17	23TEOE17	INTRODUCTION TO NANO ELECTRONICS	OE	40	60	100	3	0	0	3
18	23TEOE18	GREEN SUPPLY CHAIN MANAGEMENT	OE	40	60	100	3	0	0	3

Sl. No	Course Code	Course Title	Category	CA Marks	End Sem Marks	Total Marks	Hours/Week			
							L	T	P	C
19	23PSOE19	DISTRIBUTION AUTOMATION SYSTEM	OE	40	60	100	3	0	0	3
20	23PSOE20	ELECTRICITY TRADING AND ELECTRICITY ACTS	OE	40	60	100	3	0	0	3
21	23PSOE21	MODERN AUTOMOTIVE SYSTEMS	OE	40	60	100	3	0	0	3
22	23PEOE22	VIRTUAL INSTRUMENTATION	OE	40	60	100	3	0	0	3
23	23PEOE23	ENERGY MANAGEMENT SYSTEMS	OE	40	60	100	3	0	0	3
24	23PEOE24	ADVANCED ENERGY STORAGE TECHNOLOGY	OE	40	60	100	3	0	0	3
25	23AEOE25	DESIGN OF DIGITAL SYSTEMS	OE	40	60	100	3	0	0	3
26	23AEOE26	BASICS OF NANO ELECTRONICS	OE	40	60	100	3	0	0	3
27	23AEOE27	ADVANCED PROCESSOR	OE	40	60	100	3	0	0	3
28	23VLOE28	HDL PROGRAMMING LANGUAGES	OE	40	60	100	3	0	0	3
29	23VLOE29	CMOS VLSI DESIGN	OE	40	60	100	3	0	0	3
30	23VLOE30	HIGH LEVEL SYNTHESIS	OE	40	60	100	3	0	0	3
31	23CSOE31	ARTIFICIAL INTELLIGENCE	OE	40	60	100	3	0	0	3
32	23CSOE32	COMPUTER NETWORK MANAGEMENT	OE	40	60	100	3	0	0	3
33	23CSOE33	BLOCKCHAIN TECHNOLOGIES	OE	40	60	100	3	0	0	3

### LIST OF AUDIT COURSES

*(Common to all Branches)*

Sl. No.	Course Code	Course Title	Category	CA Marks	End Sem Marks	Total Marks	Hours/Week /Credits			
							L	T	P	C
1	23AEACZ1	ENGLISH FOR RESEARCH PAPER WRITING	AC	40	60	100	2	0	0	0
2	23AEACZ2	DISASTER MANAGEMENT	AC	40	60	100	2	0	0	0
3	23AEACZ3	VALUE EDUCATION	AC	40	60	100	2	0	0	0
4	23AEACZ4	CONSTITUTION OF INDIA	AC	40	60	100	2	0	0	0
5	23AEACZ5	PEDAGOGY STUDIES	AC	40	60	100	2	0	0	0
6	23AEACZ6	STRESS MANAGEMENT BY YOGA	AC	40	60	100	2	0	0	0
7	23AEACZ7	PERSONALITY DEVELOPMENT THROUGH LIFE ENLIGHTENMENT SKILLS	AC	40	60	100	2	0	0	0
8	23AEACZ8	SANSKRIT FOR TECHNICAL KNOWLEDGE	AC	40	60	100	2	0	0	0

### SUMMARY OF CREDIT DISTRIBUTION

S.No.	Course Category	Credits per Semester				Total Credits	Total Credits in %
		I	II	III	IV		
1	FC	7				7	8.23
2	PC	11	12			23	27.05
3	PE	3	6	3		12	14.11
4	OE			3		3	3.52
5	AC	0	0			0	
6	EEC		2	14	24	40	47.05
<b>Total</b>		<b>21</b>	<b>20</b>	<b>20</b>	<b>24</b>	<b>85</b>	<b>100</b>

## CATEGORYWISE CREDIT DISTRIBUTION

### FUNDAMENTAL COURSE (FC)

Sl. No.	Course Code	Course Title	Category	CA Marks	End Sem Marks	Total Marks	Hours/Week /Credits			
							L	T	P	C
THEORY										
1	23AEFCZ1	RESEARCH METHODOLOGY AND IPR <i>(Common to all Branches)</i>	FC	40	60	100	3	0	0	3
2	23AEFC02	ADVANCED APPLIED MATHEMATICS <i>(Common to Applied Electronics &amp; VLSI Design)</i>	FC	40	60	100	3	1	0	4
			Total	80	120	200	6	1	0	7

### PROFESSIONAL CORE (PC)

Sl. No.	Course Code	Course Title	Category	CA Marks	End Sem Marks	Total Marks	Hours/Week /Credits			
							L	T	P	C
THEORY										
1	23AEPC01	ADVANCED DIGITAL SYSTEM DESIGN <i>(Common to Applied Electronics &amp; VLSI Design)</i>	PC	40	60	100	3	0	0	3
2	23AEPC02	DSP ARCHITECTURES AND ALGORITHMS	PC	40	60	100	3	0	0	3
3	23AEPC03	STATISTICAL SIGNAL PROCESSING	PC	40	60	100	3	0	0	3
4	23AEPC04	ADVANCED DIGITAL SYSTEM DESIGN LABORATORY <i>(Common to Applied Electronics &amp; VLSI Design)</i>	PC	60	40	100	0	0	4	2
5	23AEPC05	INDUSTRIAL IOT	PC	40	60	100	3	0	0	3
6	23AEPC06	EMBEDDED SYSTEM DESIGN <i>(Common to Applied Electronics &amp; VLSI Design)</i>	PC	40	60	100	3	0	0	3
7	23AEPC07	DIGITAL IMAGE PROCESSING AND ITS APPLICATIONS	PC	50	50	100	3	0	2	4
8	23AEPC08	EMBEDDED SYSTEM DESIGN LAB	PC	60	40	100	0	0	4	2
			Total	370	430	800	18	0	10	23

**PROFESSIONAL ELECTIVE (PE)**

Sl. No.	Course Code	Course Title	Category	CA Marks	End Sem Marks	Total Marks	Hours/Week /Credits			
							L	T	P	C
THEORY										
1	23AEPEXX	PROFESSIONAL ELECTIVE - I	PE	40	60	100	3	0	0	3
2	23AEPEXX	PROFESSIONAL ELECTIVE - II	PE	40	60	100	3	0	0	3
3	23AEPEXX	PROFESSIONAL ELECTIVE - III	PE	40	60	100	3	0	0	3
4	23AEPEXX	PROFESSIONAL ELECTIVE - IV	PE	40	60	100	3	0	0	3
		Total		160	240	400	12	0	0	12

**OPEN ELECTIVE (OE)**

S.No	Course Code	Course Title	Category	CA	End Sem Marks	Total Marks	Hours/Week			
							L	T	P	C
THEORY										
1	23AEOEXX	OPEN ELECTIVE	OE	40	60	100	3	0	0	3
		Total		40	60	100	3	0	0	3

**AUDIT COURSE (AC)**

S.No	Course Code	Course Title	Category	CA	End Sem Marks	Total Marks	Hours/Week			
							L	T	P	C
THEORY										
1	23AEACXX	AUDIT COURSE - I	AC	40	60	100	2	0	0	0
2	23AEACXX	AUDIT COURSE- II	AC	40	60	100	2	0	0	0
		Total		80	120	200	4	0	0	0

**EMPLOYABILITY ENHANCEMENT COURSE (EEC)**

S.No	Course Code	CourseTitle	Category	CA	End Sem Marks	Total Marks	Hours/Week			
							L	T	P	C
THEORY										
1	23AEEEE01	MINIPROJECT	EEC	60	40	100	0	0	4	2
2	23AEEEE02	INTERNSHIP/ INDUSTRIALTRAINING	EEC	100	-	100	-	-	**	2
3	23AEEEE03	PROJECT-I	EEC	60	40	100	0	0	24	12
4	23AEEEE04	PROJECT-II	EEC	60	40	100	-	-	*	24
		Total		280	120	400	0	0	28	40

\*\*4 Weeks Internship/Industrial Training



<b>23AEFCZ1</b>	<b>RESEARCH METHODOLOGY AND IPR</b> ( Common to all branches)	<b>SEMESTER I</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	FC	3	0	0	3

<b>Course Objectives</b>	<ul style="list-style-type: none"><li>To impart knowledge on research methodology, Quantitative methods for problem solving, data interpretation and report writing</li><li>To know the importance of IPR and patent rights</li></ul>	
<b>UNIT-I</b>	<b>INTRODUCTION</b>	<b>9 Periods</b>
Definition and objectives of Research – Types of research, Various Steps in Research process, Mathematical tools for analysis, Developing a research question - Choice of a problem Literature review, Surveying, SYNTHESIZING: critical analysis, reading materials, reviewing, rethinking, critical evaluation, interpretation, Research Purposes, Ethics in research–APA Ethics code.		
<b>UNIT-II</b>	<b>QUANTITATIVE METHODS FOR PROBLEM SOLVING</b>	<b>9 Periods</b>
Statistical Modeling and Analysis, Time Series Analysis Probability Distributions, Fundamentals of Statistical Analysis and Inference, Multivariate methods, Concepts of Correlation and Regression, Fundamentals of Time Series Analysis and Spectral Analysis, Error Analysis, Applications of Spectral Analysis.		
<b>UNIT-III</b>	<b>DATA DESCRIPTION ANDREPORT WRITING</b>	<b>9 Periods</b>
Tabular and graphical description of data: Tables and graphs of frequency data of one variable, Tables and graphs that show the relationship between two variables , Relation between frequency distributions and other graphs, preparing data for analysis. Structure and Components of Research Report, Types of Report, Layout of Research Report, Mechanism of writing are search report, referencing in academic writing.		
<b>UNIT-IV</b>	<b>INTELLECTUAL PROPERTY</b>	<b>9 Periods</b>
Nature of Intellectual Property: Patents, Designs, Trade and Copyright. Process of Patenting and Development: technological research, innovation, patenting, development. International Scenario: International cooperation on Intellectual Property. Procedure for grants of patents, Patenting under PCT.		
<b>UNIT-V</b>	<b>PATENT RIGHTS</b>	<b>9 Periods</b>
Patent Rights: Scope of Patent Rights. Licensing and transfer of technology. Patent information and databases Geographical Indications.		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods      Tutorial:0 Periods      Practical:0 Periods      Total:45 Periods</b>		

#### REFERENCES:

1	Stuart Melville and Wayne Goddard, <b>“Research methodology: anintroduction”</b> ,Juta Academic, 2 <sup>nd</sup> edition, 2014.
2	Donald H.McBurney and Theresa White, <b>“Research Methods”</b> , 9 <sup>th</sup> Edition, Cengage Learning, 2013
3	RanjitKumar, <b>“Research Methodology: A Step by Step Guide for Beginners”</b> , 5th Edition ,2019
4	Dr.C.R.Kothari and GauravGarg, <b>“Research Methodology: Methods and Trends”</b> , Newage international publishers, 4th Edition ,2018



<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon the completion of the course, the students will be able to:		
C01	Formulate research question for conducting research.	K3
C02	Analyze the quantitative data.	K4
C03	Interpret research findings and give appropriate conclusions.	K2
C04	Develop a structured content to write technical report.	K3
C05	Summarize the importance of IPR and protect their research work through intellectual property.	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
C01	2	1	3	3	1	3
C02	2	3	1	3	3	3
C03	2	3	3	3	3	3
C04	2	3	1	3	3	3
C05	-	-	2	-	1	3
<b>23AEFCZ1</b>	<b>2</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	40	40	20	-	-	-	100
CAT2	40	40	20	-	-	-	100
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1	-	50	30	20	-	-	100
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2	-	50	30	20	-	-	100
ESE	30	30	20	20	-	-	100

<b>23AEFC02</b>	<b>ADVANCED APPLIED MATHEMATICS</b> (Common to Applied Electronics and VLSI Design)	<b>SEMESTER I</b>
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PREREQUISITES	CATEGORY	L	T	P	C
<b>NIL</b>	<b>FC</b>	<b>3</b>	<b>1</b>	<b>0</b>	<b>4</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To acquire knowledge with the foundations of vector space, inner product space, linear transformation, graph theory and linear programming problems mostly used in various applications in engineering and science.</li></ul>	
<b>UNIT-I</b>	<b>VECTOR SPACE</b>	<b>9+3 Periods</b>
Vector spaces– Subspaces – Linear combinations- Linear Span – Linear dependence – Linear independence – Basis and Dimensions.		
<b>UNIT-II</b>	<b>INNER PRODUCT SPACE</b>	<b>9+3 Periods</b>
Inner Products Space: Norms-Orthonormal basis, Gram Schmidt Orthogonalization Process-Orthogonal complement and Least square Approximations for linear system of equations. Hilbert spaces: Riesz Bases.		
<b>UNIT-III</b>	<b>LINEAR TRANSFORMATIONS</b>	<b>9+3 Periods</b>
Linear Transformation–Nullspace, Rangespace-dimension theorem-Matrix and representation of Linear transformation–Eigen values Eigen vectors of linear transformation Diagonalization by orthogonal transformation.		
<b>UNIT-IV</b>	<b>GRAPH THEORY</b>	<b>9+3 Periods</b>
Graphs and simple graphs, Incidence and Adjacency Matrices, Sub graphs-Vertex degrees and graphical sequences, walks, trails, paths, cycles-Trees: Characterizations of trees, Cayley’s formula, Shortest path algorithms and problems.		
<b>UNIT-V</b>	<b>LINEAR PROGRAMMING PROBLEM</b>	<b>9+3 Periods</b>
Formulation–Graphical solution–Simplex method–Big-M method-Transportation and Assignment Models.		
<b>Contact Periods:</b> <b>Lecture:45Periods      Tutorial:15Periods      Practical:0Periods      Total:60Periods</b>		

#### REFERENCES:

1	Bronson, R., <b>"Matrix Operation"</b> , Schaum' soutlineseries, McGrawHill, Newyork, 2011.
2	T.Veerarajan, <b>"Discrete Mathematics"</b> , McGrawHillEducation(India)Pvt.Ltd.,2019.
3	TahaH.A., <b>"Operations Research: Anintroduction"</b> , Ninth Edition, Pearson Education ,Asia, NewDelhi, 2012.
4	Andrews, L.C. and Philips.R.L., <b>"Mathematical Techniques for engineering and scientists"</b> , PrenticeHall of India, 2006.
5	O'NeilP.V., <b>"Advanced Engineering Mathematics"</b> , Cengage learning India private limited,(Thomson Asiaptltd , Singapore) 2007.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon the completion of the course, the students will be able to:		
C01	Obtain the knowledge of vector spaces and matrices	K3
C02	Explain the fallouts of inner product space for linear system of equations	K3
C03	Understand the concept of linear transformation	K3
C04	Understand the basic concept of graph theory and algorithm to solve network problems	K3
C05	Develop the knowledge of finding solutions of Linear Programming problems	K3

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	2	1	-	1	-	-
C02	2	1	-	1	-	-
C03	2	1	-	1	-	-
C04	2	1	-	1	-	-
C05	2	1	-	1	-	-
<b>23AEFC02</b>	2	1	-	1	-	-
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20%	50%	30%	-	-	-	100%
CAT2	20%	50%	30%	-	-	-	100%
Individual /Assignment 1/case study/seminar 1/Project 1	20%	50%	30%	-	-	-	100%
Individual /Assignment 2/case study/seminar 2/Project 2	20%	50%	30%	-	-	-	100%
ESE	20%	50%	30%	-	-	-	100%

<b>23AEPC01</b>	<b>ADVANCED DIGITAL SYSTEM DESIGN</b> (Common to Applied Electronics and VLSI Design)	<b>SEMESTER I</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>PC</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To understand the design and modeling of digital circuits, design and analyse of synchronous and asynchronous sequential circuits and architectures of programmable devices and communication controllers.</li></ul>	
<b>UNIT – I</b>	<b>SYSTEM DESIGN USING VERILOG HDL</b>	<b>9 Periods</b>
Overview of Digital Design with Verilog HDL - Hierarchical Modeling Concepts - Basic Concepts - Modules and Ports - Language Constructs and Conventions - Gate Level Modeling - Dataflow Modeling - Behavioral Modeling –Switch Level Modeling - System Tasks –Functions and Compiler Directives - Realization of combinational circuits using Verilog.		
<b>UNIT – II</b>	<b>MODELING AND DESIGN</b>	<b>9 Periods</b>
Sequential Models - Feedback Model, Capacitive Model, Implicit Model, Basic Memory Components, Functional Register, Static Machine Coding, Sequential Synthesis. Design of memories - ROM, single and dual port RAM - synchronous and asynchronous read - arithmetic circuit design - serial/parallel adder, subtractor, floating point adder/subtractor multiplier - sequential multiplier, array multiplier, signed Multiplier – ALU – Hardwired Control Design – Micro programmed Control Design.		
<b>UNIT – III</b>	<b>SYNCHRONOUS SEQUENTIAL CIRCUIT DESIGN</b>	<b>9 Periods</b>
Analysis of clocked synchronous sequential circuits and modeling - State diagram, state table, state assignment and reduction - Design of synchronous sequential circuits - Design of Iterative circuits - ASM Chart and realization using ASM - Realization of synchronous sequential circuits using Verilog.		
<b>UNIT – IV</b>	<b>ASYNCHRONOUS SEQUENTIAL CIRCUIT DESIGN</b>	<b>9 Periods</b>
Analysis of asynchronous sequential circuit – flow table reduction – Races - state assignment- transition table and problems in transition table- Design of asynchronous sequential circuit - Static, dynamic and Essential Hazards – Data synchronizers – Mixed operating mode asynchronous circuits - Realization of asynchronous sequential circuits using Verilog.		
<b>UNIT – V</b>	<b>PROGRAMMABLE DEVICES AND CONTROLLER</b>	<b>9 Periods</b>
Programming logic device families – Designing a synchronous sequential circuit using PLA/PAL –Realization of finite state machine using PLD – FPGA –Memory controller – Processor control unit –Communication controllers: UART-I <sup>2</sup> C – VGA Controllers – USB.		
<b>Contact Periods:</b>		
<b>Lecture:45 Periods      Tutorial: 0 Periods    Practical: 0 Periods    Total: 45 Periods</b>		

## REFERENCES:

1	Charles H. Roth Jr, <b>“Fundamentals of Logic Design”</b> , Thomson Learning, 7 <sup>th</sup> edition, 2014.
2	Nripendra N Biswas, <b>“Logic Design Theory”</b> , Prentice Hall of India, 2010.
3	Parag K. Lala, <b>“Digital system Design using PLD”</b> , B S Publications, 2003.
4	Morris Mano M, Charles R Kime, <b>“Logic and Computer Design Fundamentals”</b> , Pearson Education, 2015.
5	M. Morris R. Mano and Michael D. Ciletti, <b>“Digital Design: With an Introduction to the Verilog HDL”</b> , 5 <sup>th</sup> edition, Pearson Education, 2013.
6	Samir Palnitkar, <b>“Verilog HDL – A Guide to Digital Design and Synthesis”</b> , Pearson, 2003.

<b>COURSE OUTCOMES:</b> Upon the completion of the course, the students will be able to:		<b>Bloom's Taxonomy Mapped</b>
CO1	Explain the design of digital circuits in various abstraction level using Verilog HDL programming.	K2
CO2	Gain knowledge on sequential modeling and design of digital systems.	K2
CO3	Design and analyse of synchronous sequential Circuits	K4
CO4	Design and analyse of asynchronous sequential Circuits	K4
CO5	Understand the architectures of programmable devices and communication controllers	K4

COURSE ARTICULATION MATRIX						
COs/POs	P01	P02	P03	P04	P05	P06
C01	3	3	-	1	-	2
C02	3	3	-	1	-	2
C03	3	3	-	2	-	2
C04	3	3	-	2	-	2
C05	3	3	-	1	-	2
<b>23AEPC01</b>	3	3	-	1	-	2

1 – Slight, 2 – Moderate, 3 – Substantial

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	40%	40%	20%				100%
CAT2	40%	40%	20%				100%
Individual /Assignment 1/case study/seminar 1/Project 1		50%	30%	20%			100%
Individual /Assignment 2/case study/seminar 2/Project 2		50%	30%	20%			100%
ESE	30%	30%	20%	20%			100%



23AEPC02	DSP ARCHITECTURES AND ALGORITHMS	SEMESTER I
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	PC	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"> <li>To Understand the Fundamental blocks of TMS32007x Architecture and to implement various DSP Algorithms</li> </ul>
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<b>UNIT – I</b>	<b>FUNDAMENTALS OF PROGRAMMABLE DSPs</b>	<b>9 Periods</b>
Von Neumann ,Harvard Architecture, Modified Harvard and VLIW Architecture - Modified Bus Structures and Memory access in P-DSPs- Multiple access memory , Multi-ported memory, Pipelining -Special Addressing modes in P- DSPs - On chip Peripherals- Computational accuracy in DSP processor- MAC		
<b>UNIT – II</b>	<b>TMS320C67x DSP ARCHITECTURE</b>	<b>9 Periods</b>
TMS320 DSP Family Overview- TMS320C6000 DSP Family Overview- TMS320C67x DSP Features- TMS320C67x DSP Architecture - Central Processing Unit (CPU),Internal Memory ,Memory and Peripheral		
<b>UNIT – III</b>	<b>TMS320C67x CPU DATA PATHS AND CONTROL</b>	<b>9 Periods</b>
General-Purpose Register Files -Functional Units - Register File Cross -Memory, Load, and Store Paths- Data Address Paths -Control Register File- Instruction Operation and Execution- Parallel Operations- Conditional Operations- Resource Constraints- Addressing Modes- Instruction Compatibility		
<b>UNIT – IV</b>	<b>TMS320C67x PIPELINE AND INTERRUPTS</b>	<b>9 Periods</b>
Pipeline Operation- Pipeline Execution of Instruction Types- Functional Unit Constraints- Performance Considerations- Interrupts - Overview- Globally Enabling and Disabling Interrupts- Individual Interrupt Control- Interrupt Detection and Processing- Performance Considerations- Programming Considerations		
<b>UNIT – V</b>	<b>IMPLEMENTATION OF BASIC DSP ALGORITHMS</b>	<b>9 Periods</b>
Study of time complexity of DFT and FFT algorithm, Use of FFT for filtering long data sequence, IIR and FIR Filters, Interpolation, Decimation , Wavelet filter		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods    Tutorial: 0 Periods    Practical: 0 Periods    Total: 45 Periods</b>		

#### REFERENCES:

1	<i>Digital Signal Processors, "Architecture, Programming and Applications" – B. Venkata Ramani and M. Bhaskar, TMH, 2004.</i>
2	<i>"Digital Signal Processing" – Jonatham Stein, John Wiley, 2005</i>
3	<i>Avtar Singh and S. Srinivasan" Digital Signal Processing – Implementations using DSP Microprocessors", cengage Learning India Private Limited, Delhi 2012</i>
4	<i>Avtar Singh and S. Srinivasan "Digital Signal Processing", Thomson Publications, 2004.</i>
5	<i>Lapsley et al. S. Chand &amp; Co "DSP Processor Fundamentals, Architectures &amp; Features ", 2000.</i>

<b>COURSE OUTCOMES:</b> Upon completion of the course, students will be able to/have:		<b>Bloom's Taxonomy Mapped</b>
C01	Understand the Fundamentals of Programmable DSPs	K2
C02	Understand various components of DSP Architecture	K2
C03	In depth knowledge on CPU Data Paths and Control	K2
C04	Understand various concepts Pipeline and Interrupts	K2
C05	Implement various DSP Algorithms	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	3	2	1	1	1
C02	3	3	2	1	1	1
C03	3	3	2	1	1	1
C04	3	3	2	1	1	1
C05	3	3	2	1	1	1
<b>23AEPC02</b>	<b>3</b>	<b>3</b>	<b>2</b>	<b>1</b>	<b>1</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Rememberi ng (K1) %</b>	<b>Understandi ng (K2) %</b>	<b>Applyin g (K3) %</b>	<b>Analyzi ng (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	50%	50%					100%
CAT2	50%	50%					100%
Individual /Assignme nt 1/case study/sem inar 1/Project 1		50%	50%				100%
Individual /Assignme nt 2/case study/sem inar 2/Project 2		50%	50%				100%
ESE	25%	50%	25%				100%



<b>23AEP03</b>	<b>STATISTICAL SIGNAL PROCESSING</b>	<b>SEMESTER I</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>Digital Signal Processing</b>	<b>PC</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To introduce the concepts of Random Signal Processing, Signal Modeling, Spectral and Linear Estimation, Adaptive filtering and Linear estimation.</li></ul>	
<b>UNIT – I</b>	<b>INTRODUCTION TO RANDOM SIGNAL PROCESSING</b>	<b>9 Periods</b>
Discrete Random Processes- Ensemble Averages, Stationary processes, Bias and Estimation, Auto covariance, Autocorrelation, Parseval's theorem, Wiener-Khintchine relation, White noise, Power Spectral Density, Spectral factorization, Filtering Random Processes.		
<b>UNIT – II</b>	<b>SIGNAL MODELING</b>	<b>9 Periods</b>
Special types of Random Processes – ARMA, AR, MA – Yule-Walker equations- Linear Prediction of Signals-Forward and Backward Predictions, Solution to Prony's normal equation, Levinson Durbin Algorithm.		
<b>UNIT – III</b>	<b>SPECTRAL ESTIMATION</b>	<b>9 Periods</b>
Estimation of spectra from finite duration signals, Nonparametric methods - Periodogram, Modified periodogram, Bartlett, Welch and Blackman-Tukey methods, Parametric method, AR(p)spectral estimation and detection of Harmonic signals, MUSIC algorithm		
<b>UNIT – IV</b>	<b>LINEAR ESTIMATION</b>	<b>9 Periods</b>
Linear Minimum Mean-Square Error (LMMSE) Filtering: Wiener Hopf Equation, FIR Wiener filter, Noise Cancellation, Causal IIR Wiener filter, Noncausal IIR Wiener filter, Discrete Kalman filter.		
<b>UNIT – V</b>	<b>ADAPTIVE FILTERS</b>	<b>9 Periods</b>
FIR adaptive filters – Adaptive filter based on steepest descent method- Widrow-Hopf LMS algorithm, Normalized LMS algorithm, Adaptive channel Equalization, Adaptive echo cancellation, Adaptive noise cancellation, RLS adaptive algorithm.		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods      Tutorial: 0 Periods    Practical: 0 Periods    Total: 45 Periods</b>		

#### REFERENCES:

1	Monson H. Hayes, " <b>Statistical Digital Signal Processing and Modeling</b> ", John Wiley and Sons, Inc, Singapore, 2002.
2	Dimitris G. Manolakis and Vinay K .Ingle, " <b>Applied Digital Signal Processing</b> ", Cambridge University Press, 2011.
3	T. Chonavel, " <b>Statistical Signal Processing Modelling and Estimation</b> ", Springer London, 2012.
4	Umberto Spagnolini, " <b>Statistical Signal Processing in Engineering</b> ", Wiley, 2018.

<b>COURSE OUTCOMES:</b> Upon the completion of the course, the students will be able to		<b>Bloom's Taxonomy Mapped</b>
C01	Understand the Basics of random signal processing and Estimation of the spectra of finite duration signal	K2
C02	Design different Minimum Mean Square Error filters and model for prediction and Estimation	K2
C03	Analyze different speech signal Processing technique	K2
C04	Design LMSE Filters	K2
C05	Designing adaptive filters for different applications	K2

<b>Course Articulation Matrix</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	2	-	-	-	1
C02	3	2	-	-	-	1
C03	3	1	-	-	-	1
C04	3	2	-	-	-	1
C05	3	2	-	-	-	1
<b>23AEPC03</b>	<b>3</b>	<b>2</b>	<b>-</b>	<b>-</b>	<b>-</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	50%	50%					100%
CAT2	50%	50%					100%
Individual /Assignment 1/case study/seminar 1/Project 1	50%	50%					100%
Individual /Assignment 2/case study/seminar 2/Project 2	50%	50%					100%
ESE	50%	50%					100%

<b>23AEPC04</b>	<b>ADVANCED DIGITAL SYSTEM DESIGN LABORATORY</b>	<b>SEMESTER I</b>
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<b>PREREQUISITES :</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>PC</b>	<b>0</b>	<b>0</b>	<b>4</b>	<b>2</b>

<b>Course Objective</b>	<ul style="list-style-type: none"> <li>To model digital systems in HDL at different abstraction levels, identify, formulate, solve and analyze problems using digital logics and to familiarize with the implementation of design on FPGAs and ASIC.</li> </ul>
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#### **LIST OF EXPERIMENTS:**

Design, simulation and implementation on FPGAs:

1. Combinational and Sequential logic circuits based on Mealy and Moore's Machine Modelling.
2. Arithmetic circuits like serial/parallel adder/subtractor and multiplier - with and without pipelining
3. ALU architecture with suitable data path and control path circuits.
4. Vending machine/Traffic Light controller/ATM/Elevator control.

System Design on FPGAs:

5. LCD Interfacing / Keypad Interfacing.
6. Design MIPS 32-bit RISC processor and implement on FPGA.
7. Design a reconfigurable filter and verify its functionality on FPGA.
8. Design and implement the CORDIC algorithm on FPGA.

ASIC Design:

9. Perform digital design on combinational and sequential logic circuits from RTL to GDS

#### **Contact Periods:**

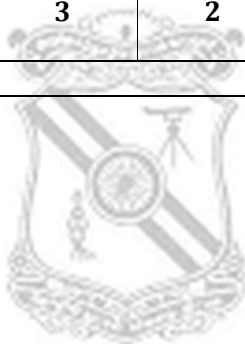
**Lecture: 0 Periods      Tutorial: 0 Periods      Practical: 60 Periods      Total: 60 Periods**

#### **REFERENCES:**

1	Charles Roth Jr.H., "Fundamentals of Logic Design", Australia cengage learning, 7 <sup>th</sup> edition, 2014.
2	Samir Palnitkar, "Verilog HDL-A guide to Digital Design and synthesis" 2 <sup>nd</sup> edition Pearson, Education in South Asia 2013.
3	Clive Maxfield, "The design warrior guide to FPGA's, devices, Tools and flows", Elsevier, 2011.
4	Altera Corporation- "Standard Cell ASIC to FPGA Design Methodology and Guidelines", April 2009.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	An ability to Model digital systems in HDL at different abstraction levels	K2
C02	An ability to design with and without pipelining circuits.	K3
C03	An ability to Identify and design Interfacing circuits.	K4
C04	An ability to Solve and analyze problems using CORDIC algorithm.	K4
C05	Familiarize with the implementation of design on FPGAs and ASIC circuits.	K3

<b>Course Articulation Matrix</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	3	2	3	1	1
C02	3	3	3	3	1	2
C03	3	3	2	3	1	2
C04	3	3	2	3	1	2
C05	3	3	2	3	1	2
<b>23AEPC04</b>	<b>3</b>	<b>3</b>	<b>2</b>	<b>3</b>	<b>1</b>	<b>2</b>
1 – Slight, 2 – Moderate, 3 – Substantial						



23AEPC05	INDUSTRIAL IOT	SEMESTER II
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	PE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To get knowledge on recent trends of industry 4.0 and cloud computing and to design IOT systems for various applications.</li></ul>	
<b>UNIT – I</b>	<b>INTRODUCTION TO INDUSTRIAL 4.0</b>	<b>9 Periods</b>
Overview of Internet of Things and IIOT- Introduction to Industry 4.0 –Evolution - Design requirements, Drivers, Impacts and applications - Sustainability assessment of industries – Cyber security -Industrial Internet Systems - Cyber Physical Systems- Characteristics -Industrial Processes - Functional & Operational Viewpoint.		
<b>UNIT – II</b>	<b>INDUSTRIAL INTERNET OF THINGS</b>	<b>9 Periods</b>
IIOT Architecture – IIOT Requirements – IIOT Business Model: Categorization- Business opportunities-Reference Architecture of IIOT - key Technologies: Augmented Reality - Virtual Reality - Artificial Intelligence - Introduction to Sensors- Characteristics- Categories- Smart Sensor-Actuators.		
<b>UNIT – III</b>	<b>INDUSTRIAL DATA TRANSMISSION</b>	<b>9 Periods</b>
Introduction to Industrial Data Transmission- Field bus, Profi bus, Inter bus, Bit bus, Mod bus, Digital STROM- Communication protocols-Types:802.15.4, Zigbee, 6LoWPAN, HART,Z wave, Wi-Fi, RFID, NFC-Industrial Data Acquisition-PLC-SCADA		
<b>UNIT – IV</b>	<b>IoT ANALYTICS</b>	<b>9 Periods</b>
Introduction to IIoT -IIoT Analytics - Big Data Analytics - Software Defined Networks- Machine Learning and Data Science in Industries - Cloud & FOG Computing- Industrial IoT: Security.		
<b>UNIT – V</b>	<b>IIoT APPLICATION</b>	<b>9 Periods</b>
Industrial IoT- Application Domains: Healthcare Applications in Industries - Inventory Management and Quality Control -Plant Safety and Security - Smart factories and Smart Cities -Applications of UAVs in Industries.		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>		

#### REFERENCES:

1	SudipMisra, Chandana Roy, Anandarup Mukherjee,“ <b>Introduction to Industrial Internet of Tings and Industry 4.0</b> ”, CRC Press, 1st edition, 2021
2	Alasdair Gilchrist, “ <b>Industry 4.0: The Industrial Internet of Things</b> ”,Apress, 2017.
3	ArshdeepBahga, Vijay Madiseti, “ <b>Internet of Things-A hands-on approach</b> ”, Universities Press,2015.
4	Olivier Hersent, David Boswarthick, and Omar Elloumi, “ <b>The Internet of Things: Key Applications and Protocols</b> ”, Wiley Publications-2011

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy</b>
Upon the completion of the course, the students will be able to:		<b>Mapped</b>
CO1	Acquire knowledge about advanced Industrial IOT systems	K1
CO2	Explain various protocols and data analytics used for Industrial IOT.	K2
CO3	Understand Industrial Data Transmission	K2
CO4	Apply IOT in big data analytics and in Cloud & FOG Computing	K3
CO5	Ability to analyze and apply industrial IoT to real.	K4

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
CO1	3	1	-	-	1	2
CO2	3	1	-	-	1	2
CO3	3	1	-	-	1	2
CO4	3	1	-	-	1	2
CO5	3	1	-	-	1	2
<b>23AEPC05</b>	3	1			1	2
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	30%	30%		40%			100%
CAT2	40%	50%		10%			100%
Individual /Assignment 1/case study/seminar 1/Project 1	30%	60%	10%				100%
Individual /Assignment 2/case study/seminar 2/Project 2	30%	30%		40%			100%
ESE	30%	30%	10%	30%			100%

23AEPC06	EMBEDDED SYSTEM DESIGN	SEMESTER II
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PREREQUISITES	CATEGORY	L	T	P	C
MICROPROCESSORS AND MICROCONTROLLERS	PC	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To learn the basic concepts of embedded systems, program design and networks and exploit the architecture and applications of ARM CORTEX and PIC processor.</li></ul>	
<b>UNIT – I</b>	<b>EMBEDDED PROCESSOR</b>	<b>9 Periods</b>
Embedded Computers, Characteristics and Challenges of Embedded Computing System, Embedded system design process- Requirements, Specification, Architectural Design, Designing Hardware and Software Components, System Integration, Formalism for System Design- Structural Description, Behavioural Description, Design Example: Model Train Controller, Alarm Controller, Elevator Controller.		
<b>UNIT – II</b>	<b>PIC CONTROLLER</b>	<b>9 Periods</b>
PIC 16F877- architecture, memory technologies, timing circuits, power-up and reset, parallel ports, ADC, interrupt, PWM, counters and timers, instruction set and assembly language programming.		
<b>UNIT – III</b>	<b>INTERFACING WITH PIC</b>	<b>9 Periods</b>
Human and physical interfaces- switches to keyboard, LED display, liquid crystal display, Actuators and sensors, PWM, serial communication protocols (UART, I2C, SPI), programming interrupt, timers and counter.		
<b>UNIT – IV</b>	<b>ARM CORTEX M4</b>	<b>9 Periods</b>
Introduction to Cortex -M Processor family – Cortex M4 – Features - Architecture – Block Diagram – Operation modes and states – Registers - Memory System – Exceptions and Interrupts – Instruction Set – Low power		
<b>UNIT – V</b>	<b>INTERFACING WITH ARM CORTEX</b>	<b>9 Periods</b>
ARM Cortex STM32F controller – Configuring GPIO Ports – Switches and LEDs - LCD display Seven Segment LED Display – Matrix Keypad – ADC – DAC – Pulse Width Modulation – DMA - Serial Communication USART.		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods Tutorial: 0 Periods Practical: 0 Periods Total: 45 Periods</b>		

#### REFERENCES:

1	Marilyn Wolf, <i>"Computers as Components - Principles of Embedded Computing System Design"</i> , Third Edition "Morgan Kaufmann Publisher, 2012.
2	Tim Wilmshurst, <i>"Designing Embedded Systems with PIC microcontrollers-Principles and Applications"</i> , Newnes Publications, 2007.
3	Muhammad Ali Mazidi, Rolin McKinlay, Danny Causey, <i>"PIC Microcontroller and Embedded Systems: Using Assembly and C for PIC18"</i> , Prentice hall publications, 2007.
4	Martin Bates, <i>"Interfacing PIC microcontrollers-Embedded Design by Interactive Simulation"</i> , Newnes Publication, 2006.
5	Joseph Yiu, <i>"The Definitive Guide to ARM® Cortex®-M3 and Cortex®-M4 Processors"</i> , Newnes Third Edition, 2013.
6	Dr. Mark Fisher, <i>ARM Cortex M4 Cook Book</i> , Packt Publishing, 2016.
7	Andrew N. Sloss Dominic Symes Chris Wright, <i>"ARM System Developer's Guide Designing and Optimizing System Software"</i> , 1st edition Elsevier Inc 2010.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Exploit the basic concepts of embedded system.	K2
C02	Interpret the Architecture and features of PIC Controller.	K2
C03	Apply programming skill for interfacing with PIC Controller.	K2
C04	Interpret the Architecture and features of ARM CORTEX controller.	K3
C05	Apply programming skill for interfacing with ARM CORTEX processor.	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	1	2	2	1	-
C02	3	1	2	2	1	-
C03	3	1	2	2	1	-
C04	3	1	2	2	1	-
C05	3	1	2	2	1	-
<b>23AEPC06</b>	3	1	2	2	1	-
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Rememberin g (K1) %</b>	<b>Understandin g (K2) %</b>	<b>Applyin g (K3) %</b>	<b>Analyzin g (K4) %</b>	<b>Evaluatin g (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	30%	30%	40%				100%
CAT2	30%	30%	40%				100%
Individual /Assignment 1/case study/seminar 1/Project 1	30%	30%	40%				100%
Individual /Assignment 2/case study/seminar 2/Project 2	20%	30%	50%				100%
ESE	30%	30%	40%				100%



<b>23AEPC07</b>	<b>DIGITAL IMAGE PROCESSING AND ITS APPLICATIONS</b>	<b>SEMESTER II</b>
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<b>PREREQUISITES :</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>PC</b>	<b>3</b>	<b>0</b>	<b>2</b>	<b>4</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To Gain knowledge in Digital Image Fundamentals, Image Enhancement, Image Segmentation, Representation, Description, Image compression and in certain area of image processing Applications.</li></ul>		
<b>UNIT - I</b>	<b>DIGITAL IMAGE FUNDAMENTALS</b>	<b>9 Periods</b>	
Representing Digital Images: Binary images, Gray scale images: Sampling and quantization, Spatial and Gray level resolution , Color images –Color models, Basics of color image processing-Basic relationship between Pixels: Neighbours of a Pixel, Adjacency, Connectivity, Regions and Boundaries, Distance measures. Image transforms: Discrete Fourier transforms, Discrete Wavelet transforms.			
<b>UNIT – II</b>	<b>IMAGE ENHANCEMENT IN SPATIAL AND FREQUENCY DOMAIN</b>	<b>9 Periods</b>	
Spatial domain image Enhancement: Basic gray level transformations, Histogram processing, Enhancement using arithmetic & logical operators, Basics of spatial filtering Smoothing spatial filters, Sharpening spatial filters. Frequency domain image Enhancement: Smoothing frequency domain filters, Sharpening frequency domain filters, Homomorphic filtering- Noise models.			
<b>UNIT – III</b>	<b>IMAGE SEGMENTATION, REPRESENTATION AND DESCRIPTION</b>	<b>9 Periods</b>	
Image segmentation: Detection of Discontinuities, Edge linking and boundary detection, Thresholding, Region based segmentation, Segmentation by Morphological watersheds. Representation and Description: Representation, Boundary Descriptors, Regional Descriptors.			
<b>UNIT – IV</b>	<b>IMAGE COMPRESSION</b>	<b>9 Periods</b>	
Fundamentals of image compression: Coding redundancy, Inter Pixel redundancy, Psychovisual redundancy, Fidelity Criteria-Image compression models-Elements of Information theory-Error free compression-Lossy compression-Image compression standards.			
<b>UNIT – V</b>	<b>IMAGE PROCESSING APPLICATIONS</b>	<b>9 Periods</b>	
Object Recognition: Pattern and Pattern classes –Recognition based on Decision –theoretic methods – Structural methods –Medical imaging processing: Pattern classification and diagnostic decision – Measures of diagnostic accuracy – Applications: Contrast enhancement of mammograms – Detection of calcifications by region growing – Shape and texture analysis of tumours.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical:30 Periods      Total: 75 Periods</b>			

**List of Experiments – 30 Periods**

1. **Point-to-point transformation**-Thresholding an image and the evaluation of its histogram.
2. **Histogram Equalization.**
3. **Geometric transformations**-Image rotation, scaling, and translation. Two-dimensional Fourier transform I.
4. **Two-dimensional Fourier Transform.** Harmonic content of an image using the discrete Fourier transform (DFT) and masking with DFT.
5. **Linear filtering using convolution.**
6. **Ideal filters in the frequency domain**-Effects of filtering low and high frequencies in an image.
7. **Non-Linear filtering using convolutional masks**-Effects of a median filter on an image corrupted with impulsive noise.
8. **Morphological operations I.** Erosion and dilation
9. **Entropy as a compression measure**-Entropy as a compression measurement to the DPCM compression measure.
10. **Edge detection**-Edge detectors and their operation in noisy images.

**REFERENCES:**

1	Gonzalez R.C., Woods R.E., <b>"Digital Image Processing"</b> , Fourth Edition, Pearson, 2017.
2	Sinha G. R, Patel, B. C., <b>"Medical Image Processing: Concepts And Applications"</b> , Prentice Hall, 2014.
3	Anil K. Jain, <b>'Fundamentals of Digital Image Processing'</b> , Pearson, 2002.

COURSE OUTCOMES:		Bloom's Taxonomy Mapped
Upon completion of the course, the students will have the ability to :		
C01	Understand the fundamentals of Digital Image Processing and concepts in Image Enhancement.	K3
C02	Analyze the image using descriptors and representation schemes and segmentation algorithms.	K4
C03	Apply the algorithms for object recognition, and Medical Image Processing.	K2
C04	Understand the Image Compression Techniques	K3
C05	Analyse the image Processing Applications	K4

<b>COURSE ARTICULATION MATRIX</b>									
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>	<b>PS01</b>	<b>PS02</b>	<b>PS03</b>
C01	3	2	1	-	-	1	3	-	1
C02	3	2	1	-	-	1	3	-	1
C03	3	1	1	-	-	1	3	-	1
C04	3	1	1	-	-	1	3	-	1
C05	3	2	1	-	-	1	3	-	1
<b>23AEPC07</b>	<b>3</b>	<b>2</b>	<b>1</b>	<b>-</b>	<b>-</b>	<b>1</b>	<b>3</b>	<b>-</b>	<b>1</b>
<b>1 - Slight, 2 - Moderate, 3 - Substantial</b>									

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20	20	30	30			100
CAT2	20	20	30	30			100
CAT3	30	40	10	20			100
Assignment 1	25	25	25	25			100
Assignment 2	25	25	25	25			100
Assignment 3	25	25	25	25			100
Quiz1	33	33	34				100
Quiz 2	33	33	34				100
Quiz 3	33	33	34				100
Other mode of internal assessments, if any							
ESE	20	20	30	30			100

<b>23AEPC08</b>	<b>EMBEDDED SYSTEM DESIGN LABORATORY</b>	<b>SEMESTER II</b>
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<b>PREREQUISITES :</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>PC</b>	<b>0</b>	<b>0</b>	<b>4</b>	<b>2</b>

<b>Course Objective</b>	<ul style="list-style-type: none"> <li>This course enables to students to learn above the programming PIC controller and ARM Processor.</li> </ul>
<p><b>LIST OF EXPERIMENTS:</b></p> <p><b>PIC controller:</b></p> <ol style="list-style-type: none"> <li>1. Configuration of ports</li> <li>2. Timer</li> <li>3. Seven Segment display</li> <li>4. I2C</li> <li>5. LCD interface</li> <li>6. Stepper Motor control.</li> </ol> <p><b>ARM Processor:</b></p> <ol style="list-style-type: none"> <li>1. GPIO Configuration</li> <li>2. Timer</li> <li>3. LCD interface</li> <li>4. ADC and DAC</li> <li>5. PWM Generation</li> <li>6. Real Time Clock</li> <li>7. Serial data transfer</li> </ol> <p>Study of sensing elements using IOT.</p> <p><b>Mini Project using PIC Controller or ARM processor</b></p> <p><b>Contact Periods:</b>  <b>Lecture: 0 Periods      Tutorial: 0 Periods      Practical: 60 Periods      Total:60 Periods</b></p>	

#### REFERENCES:

1	Andrew N.Sloss Dominic Symes Chris Wright, " <b>ARM System Developer's Guide Designing and Optimizing System Software</b> ", Elsevier Inc 2010.
2	Joseph Yiu , " <b>The Definitive Guide to the ARM Cortex-M</b> ", Elsevier- Newness, 2014.
3	Kirk Zurell, " <b>C Programming for Embedded Systems</b> ", CRC Press, 2000.
4	Dogan Ibrahim, " <b>Advanced PIC microcontroller projects in C</b> ", Newnes publication, 2012.
5	Muhammad Ali Mazidi, Rolin McKinlay, Danny Causey, " <b>PIC Microcontroller and Embedded Systems</b> "

<b>COURSE OUTCOMES:</b> Upon completion of the course, the students will be able to:		<b>Bloom's Taxonomy Mapped</b>
C01	An ability to apply programming skills in PIC Controller.	K2
C02	An exposure to interfacing concepts of PIC controller.	K4
C03	An ability to apply programming skills in ARM processor	K3
C04	An exposure to interfacing concepts of ARM processor.	K3
C05	An ability to apply controllers for real time applications.	K4

COURSE ARTICULATION MATRIX						
C0s/P0s	P01	P02	P03	P04	P05	P06
C01	3	3	-	2	-	1
C02	3	3	-	2	-	1
C03	3	3	-	2	-	1
C04	3	3	-	2	-	1
C05	3	3	-	2	-	1
<b>23AEP08</b>	3	3	-	2	-	1

1 – Slight, 2 – Moderate, 3 – Substantial

<b>23AEEE01</b>	<b>MINI PROJECT</b>	<b>SEMESTER II</b>
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<b>PREREQUISITES :</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>EEC</b>	<b>0</b>	<b>0</b>	<b>4</b>	<b>2</b>

#### **COURSE OBJECTIVES:**

- To identify, analyze, formulate and handle programming projects with a comprehensive and systematic approach. In particular acquire practical knowledge within the chosen area of technology for technical project development.

**Lecture : 0 Periods   Tutorial : 0 Periods   Practical : 60 Periods   Total: 60 Periods**

#### **COURSE OUTCOMES:**

Upon completion of the course, students will be able to/have:		<b>Bloom's Taxonomy Mapped</b>
C01	An exposure to take up real time problems and challenges.	K6
C02	Hands-on experience on the technical topics	K4
C03	Confidence to work on projects independently.	K4
C04	Better presentation and communication skills	K5
C05	An understanding of technical dissertation presentation and writing.	K5

#### **COURSE ARTICULATION MATRIX**

<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
C01	3	3	2	3	1	2
C02	3	3	2	3	2	3
C03	1	3	2	3	3	3
C04	1	3	2	3	3	3
C05	1	3	2	3	3	3
<b>23AEEE01</b>	3	3	2	3	3	3

1 – Slight, 2 – Moderate, 3 – Substantial

23AEEE02	INTERNSHIP/INDUSTRIAL TRAINING	SEMESTER III
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PREREQUISITES :	CATEGORY	L	T	P	C
NIL	EEC	0	0	-	2

**COURSE OBJECTIVES:**

- To expose the students to work on real time challenges independently in industry and present their technical dissertation and writing.

**Lecture : 0 Periods    Tutorial : 0 Periods    Practical : 160 Periods    Total: 160 Periods**

**COURSE OUTCOMES:**

Upon completion of the course, the students will have:

**Bloom's  
Taxonomy  
Mapped**

C01	An exposure to the processes of advanced electronics or other related industries	K6
C02	An ability to take up real time challenges.	K4
C03	Confidence to work on the project independently.	K4
C04	Team work experience	K3
C05	An understanding of technical dissertation presentation and writing.	K5

**COURSE ARTICULATION MATRIX**

COs/POs	PO1	PO2	PO3	PO4	PO5	PO6
C01	3	3	2	3	1	2
C02	1	3	2	3	3	3
C03	3	3	2	3	2	3
C04	2	2	3	3	1	2
C05	3	3	2	3	2	3
<b>23AEEE02</b>	3	3	2	3	2	3

1 – Slight, 2 – Moderate, 3 – Substantial





<b>23AEEE04</b>	<b>PROJECT-II</b>	<b>SEMESTER IV</b>
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<b>PREREQUISITES:</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>EEC</b>	<b>-</b>	<b>-</b>	<b>*</b>	<b>24</b>

**COURSEOBJECTIVE:**

- To expose the students to work on real time challenges independently to provide solution and present the technical dissertation and writing.

**Lecture:0 Periods Tutorial:0 Periods Practical:720 Periods Total:720 Periods**

**COURSEOUTCOMES:**

Upon completion of the course, the students will have:

		<b>Bloom's Taxonomy Mapped</b>
C01	An exposure to take up real time problems and challenges and provide solution.	K6
C02	Hands-on experience on the technical topics	K4
C03	Confidence to work on projects independently.	K4
C04	Better presentation and communication skills	K5
C05	An understanding of technical dissertation presentation and writing.	K5

**COURSE ARTICULATION MATRIX**

<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
C01	3	3	2	3	1	2
C02	1	3	2	3	3	3
C03	3	3	2	3	2	3
C04	1	1	1	1	3	3
C05	3	3	2	3	2	3
<b>23AEEE04</b>	3	3	2	3	2	3

1-Slight,2-Moderate,3-Substantial

<b>23AEPE01</b>	<b>DIGITAL IC DESIGN</b>	<b>SEMESTER I</b>
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<b>PREREQUISITES :</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>PE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To learn VLSI design methodology, MOS transistor principles, combinational and sequential logic circuit design with FET devices, arithmetic building blocks and memory architectures.</li></ul>		
<b>UNIT - I</b>	<b>OVERVIEW OF VLSI DESIGN METHODOLOGY</b>		<b>9 Periods</b>
VLSI Design Process - Architectural design - Logical design-Physical design - Layout styles - Full custom, Semicustom approaches, layout design rules: Need for design rules – Layer representations - CMOS nwell / pwell design rules – Design rule background-Layer assignments-SOI rules.			
<b>UNIT – II</b>	<b>MOS TRANSISTOR PRINCIPLES AND ADVANCED FET DEVICES</b>		<b>9 Periods</b>
MOSFET Transistor Characteristic under Static and Dynamic Conditions, MOS Transistor Secondary Effects, CMOS Inverter - Static Characteristic, Dynamic Characteristic, Power, Energy, and Energy Delay parameters. FinFETs – VI Characteristics – SuperFin Technology.			
<b>UNIT – III</b>	<b>COMBINATIONAL LOGIC CIRCUITS</b>		<b>9 Periods</b>
Static CMOS Design – Complementary CMOS, Ratioed Logic, Pass-Transistor Logic. Dynamic CMOS Design – Dynamic Logic: Basic Principles, Speed and Power Dissipation of Dynamic Logic, Issues in Dynamic Design, Cascading Dynamic Gates.			
<b>UNIT – IV</b>	<b>SEQUENTIAL LOGIC CIRCUITS</b>		<b>9 Periods</b>
Timing metrics for sequential circuits, Static Latches and Registers, Dynamic Latches and Registers, Clock tree synthesis, Pipelines, Pulse and sense amplifier based Registers, Non-Bistable Sequential Circuits.			
<b>UNIT – V</b>	<b>ARITHMETIC BUILDING BLOCKS AND MEMORY ARCHITECTURES</b>		<b>9 Periods</b>
Data path circuits, Architectures for Adders, Multipliers, Shifters, Speed and Area Tradeoffs, Array Subsystems based on CMOS and FinFET design: SRAM, DRAM, ROM.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>			

#### REFERENCES:

1	Jan M Rabaey, Anantha Chandrakasan, B Nikolic, <b>"Digital Integrated Circuits: A Design Perspective"</b> , 2nd Edition, Prentice Hall of India, 2016.
2	Niel H.E. Weste, David Harris, Ayan Banerjee, <b>"CMOS VLSI Design- A circuits and Systems Perspective"</b> , 3rd Edition, Pearson education, 2015.
3	Niraj K. Jha I Deming Chen , <b>"Nano electronic Circuit Design"</b> , Springers, 2021.
4	Wayne Wolf, <b>"Modern VLSI Design"</b> , PHI Learning Private Limited, New Delhi, 2011.
5	Sung-Mo Kang and Yusuf Leblebici, <b>"CMOS Digital Integrated Circuits"</b> , McGraw Hill, 3rd Edition, 2016.

COURSE ARTICULATION MATRIX						
COs/POs	PO1	PO2	PO3	PO4	PO5	PO6
C01	3	2	-	1	-	1
C02	3	2	-	1	-	1
C03	3	2	-	1	-	2
C04	3	2	-	1	-	2
C05	3	2	-	1	-	2
<b>23AEPE01</b>	<b>3</b>	<b>2</b>	<b>-</b>	<b>1</b>	<b>-</b>	<b>2</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

ASSESSMENT PATTERN – THEORY							
Test / Bloom's Category*	Remembering (K1) %	Understanding (K2) %	Applying (K3) %	Analyzing (K4) %	Evaluating (K5) %	Creating (K6) %	Total %
CAT1	40%	40%	20%				100%
CAT2	40%	40%	20%				100%
Individual /Assignment 1/case study/seminar 1/Project 1		50%	30%	20%			100%
Individual /Assignment 2/case study/seminar 2/Project 2		50%	30%	20%			100%
ESE	30%	30%	20%	20%			100%

<b>23AEPE02</b>	<b>ANALYSIS AND DESIGN OF ANALOG INTEGRATED CIRCUITS</b> (Common to Applied Electronics & VLSI Design)	<b>SEMESTER I</b>
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<b>PREREQUISITES :</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>PE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	• To explain, analyse and construct various analog integrated circuits.		
<b>UNIT - I</b>	<b>CIRCUIT CONFIGURATION FOR BIPOLAR IC</b>	<b>9 Periods</b>	
Bipolar Current Mirrors-General Properties-Simple Current Mirror with beta helper-Simple current mirror with degeneration-Cascode Current Mirror-Wilson Current mirror-Bipolar Widlar Current Source-Bipolar Peaking Current Source-Supply Insensitive Biasing- Band-Gap-Referenced Bias Circuits in Bipolar Technology. Output Stages: Transfer Characteristics, Power Output and Efficiency of Emitter Follower and Class B Push-Pull stage.			
<b>UNIT – II</b>	<b>CIRCUIT CONFIGURATION FOR MOS IC</b>	<b>9 Periods</b>	
MOS Current Mirrors-General Properties-Simple Current Mirror with beta helper-Simple current mirror with degeneration-Cascode Current Mirror-Wilson Current mirror-MOS Widlar Current Source-MOS Peaking Current Source- Band-Gap-Referenced Bias Circuits in CMOS Technology. Output Stages: Transfer Characteristics of Source Follower-CMOS Class AB Output Stage			
<b>UNIT – III</b>	<b>TWO STAGE OPERATIONAL AMPLIFIERS</b>	<b>9 Periods</b>	
Basic Two-Stage MOS Operational Amplifiers: Common-Mode Rejection Ratio-Power-Supply Rejection Ratio-Effect of Overdrive Voltages-Layout Considerations - Two-Stage MOS Operational Amplifiers with Cascodes - MOS Telescopic-Cascode Operational Amplifiers - MOS Folded-Cascode Operational Amplifiers - MOS Active-Cascode Operational Amplifiers - Bipolar Operational Amplifiers- Frequency Response of Operational amplifiers.			
<b>UNIT – IV</b>	<b>PHASE LOCKED LOOPS</b>	<b>9 Periods</b>	
Simple PLL: Phase detector- Basic PLL Topology-Dynamics of Simple PLL - Charge-Pump PLLs: Problem of Lock Acquisition-Charge Pump-Basic Charge-Pump PLL – Non-ideal Effects in PLLs - Jitter in PLLs - Delay-Locked Loops – Applications of PLL.			
<b>UNIT – V</b>	<b>NONLINEAR ANALOG CIRCUITS</b>	<b>9 Periods</b>	
Analog Multiplier: Emitter Coupled pair as Multiplier-Gilbert Cell as Multiplier-Complete Analog Multiplier-Gilbert Multiplier Cell as Balanced Modulator and Phase Shifter. Noise: Sources of Noise-Noise Models of IC Components-Circuit Noise Calculations-Equivalent Input Noise Generator-Effect of Feedback on Noise Performance-Noise in Operation Amplifier-Noise Bandwidth-Noise Figure and Noise Temperature.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>			

#### REFERENCE:

1	Paul R. Gray, Paul J.Hurst, Stephen H.Lewis, and Robert G. Meyer, “ <b>Analysis and Design of Analog Integrated circuits</b> ”, Wiley, 5 <sup>th</sup> Edition, 2009.
2	Behzad Razavi, “ <b>Design of Analog CMOS Integrated circuits</b> ”, McGraw Hill Education, 2 <sup>nd</sup> Edition, 2016.
3	David Johns, Ken Martin, “ <b>Analog Integrated circuit design</b> ”, Wiley, 2 <sup>nd</sup> Edition, 2013.

4	<i>Sergio Franco, "Design with Operational Amplifiers and Analog Integrated Circuits" McGraw Hill Education, 4<sup>th</sup> Edition, 2015.</i>
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<b>COURSE OUTCOMES:</b> Upon completion of the course, the students will be able to:		<b>Bloom's Taxonomy Mapped</b>
CO1	Analyse the basic circuits required to build up Bipolar IC	K4
CO2	Analyse the basic circuits required to build up MOS IC	K4
CO3	Design and describe the characteristics of two stage Bipolar and MOS Operation amplifiers	K3
CO4	Analyse the various types of PLL circuit and explain their applications	K4
CO5	Discuss the construction and working of non-linear analog circuits and describe noise characteristics in analog circuits	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
CO1	3	1	-	1	-	1
CO2	3	1	-	1	-	1
CO3	3	1	-	1	-	1
CO4	3	1	-	1	-	1
CO5	3	1	-	1	-	1
<b>23AEPE02</b>	3	1	-	1	-	1
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	40%	40%	20%				100%
CAT2	40%	40%	20%				100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1		50%	25%	25%			100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2		50%	25%	25%			100%
ESE	30%	40%	20%	10%			100%

23AEPE03	SEMICONDUCTOR DEVICE MODELING	SEMESTER I
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	PE	3	0	0	3

#### Course Objective

- To acquire the fundamental knowledge on semiconductor theory, device modeling aspects of various semiconductor devices for electronic applications.

#### UNIT – I MOS CAPACITORS 9 Periods

Surface Potential: Accumulation, Depletion, and Inversion, Electrostatic Potential and Charge Distribution in Silicon, Capacitances in an MOS Structure, Polysilicon-Gate Work Function and Depletion Effects, MOS under Non equilibrium and Gated Diodes, Charge in Silicon Dioxide and at the Silicon–Oxide Interface, Effect of Interface Traps and Oxide Charge on Device Characteristics, High-Field Effects, Impact Ionization and Avalanche Breakdown, Band-to-Band Tunneling, Tunneling into and through Silicon Dioxide, Injection of Hot Carriers from Silicon into Silicon Dioxide, High-Field Effects in Gated Diodes, Dielectric Breakdown.

#### UNIT – II MOSFET DEVICES 9 Periods

Long-Channel MOSFETs, Drain-Current Model, MOSFET I–V Characteristics, Subthreshold Characteristics, Substrate Bias and Temperature Dependence of Threshold Voltage, MOSFET Channel Mobility, MOSFET Capacitances and Inversion-Layer Capacitance Effect, Short-Channel MOSFETs, Short-Channel Effect, Velocity Saturation and High-Field Transport Channel Length Modulation, Source–Drain Series Resistance, MOSFET Degradation and Breakdown at High Fields.

#### UNIT – III CMOS DEVICE DESIGN 9 Periods

CMOS Scaling, Constant-Field Scaling, Generalized Scaling, Non scaling Effects, Threshold Voltage, Threshold-Voltage Requirement, Channel Profile Design, Non uniform Doping, Quantum Effect on Threshold Voltage, Discrete Dopant Effects on Threshold Voltage, MOSFET Channel Length, Various Definitions of Channel Length, Extraction of the Effective Channel Length, Physical Meaning of Effective Channel Length, Extraction of Channel Length by C–V Measurements.

#### UNIT – IV BIPOLAR DEVICES 9 Periods

n–p–n Transistors, Basic Operation of a Bipolar Transistor, Modifying the Simple Diode Theory for Describing Bipolar Transistors, Ideal Current–Voltage Characteristics, Collector Current, Base Current, Current Gains, Ideal IC–VCE Characteristics, Characteristics of a Typical n–p–n Transistor, Effect of Emitter and Base Series Resistances, Effect of Base–Collector Voltage on Collector Current, Collector Current Falloff at High Currents, Non ideal Base Current at Low Currents, Bipolar Device Models for Circuit and Time- Dependent Analyses Basic dc Model, Basic ac Model, Small-Signal Equivalent-Circuit Model, Emitter Diffusion Capacitance, Charge-Control Analysis, Breakdown Voltages, Common-Base Current Gain in the Presence of Base–Collector Junction Avalanche, Saturation Currents in a Transistor.

#### UNIT – V MATHEMATICAL TECHNIQUES FOR DEVICE SIMULATIONS 9 Periods

Poisson equation, continuity equation, drift-diffusion equation, Schrodinger equation, hydrodynamic equations, trap rate, finite difference solutions to these equations in 1D and 2D space, grid generation.

#### Contact Periods:

**Lecture: 45 Periods    Tutorial: 0 Periods    Practical: 0 Periods    Total: 45 Periods**

#### REFERENCES:

1	Yuan Taur and Tak H.Ning, " <i>Fundamentals of Modern VLSI Devices</i> ", Cambridge. University Press, 2016.
2	A.B. Bhattacharyya " <i>Compact MOSFET Models for VLSI Design</i> ", John Wiley & Sons Ltd, 2009.

3	Ansgar Jungel, <b>"Transport Equations for Semiconductors"</b> , Springer, 2009.
4	Trond Ytterdal, Yuhua Cheng and Tor A. Fjeldly Wayne Wolf, <b>"Device Modeling for Analog and RFCMOS Circuit Design"</b> , John Wiley & Sons Ltd, 2004.
5	Selberherr, S., <b>"Analysis and Simulation of Semiconductor Devices"</b> , Springer-Verlag., 1984.
6	Behzad Razavi, <b>"Fundamentals of Microelectronics"</b> Wiley Student Edition, 2nd Edition, 2014 7.
7.	J P Collinge, C A Collinge, <b>"Physics of Semiconductor devices"</b> Springer, 2002.
8.	8. S.M.Sze, Kwok.K.NG, <b>"Physics of Semiconductor devices"</b> , Springer, 2006.

<b>COURSE OUTCOMES:</b> Upon completion of the course, students will be able to/have:		<b>Bloom's Taxonomy Mapped</b>
CO1	Explore the properties of MOS capacitors.	K2
CO2	Analyze the various characteristics of MOSFET devices.	K2
CO3	Describe the various CMOS design parameters and their impact on performance of the device.	K2
CO4	Discuss the device level characteristics of BJT transistors.	K2
CO5	Identify the suitable mathematical technique for simulation.	K3

#### COURSE ARTICULATION MATRIX:

COs/POs	PO1	PO2	PO3	PO4	PO5	PO6
CO1	3	2	-	-	-	1
CO2	3	2	-	-	-	1
CO3	3	1	-	-	-	1
CO4	2	2	-	-	-	1
CO5	3	2	-	-	-	1
<b>23AEPE03</b>	3	2	-	-	-	1
1 – Slight, 2 – Moderate, 3 – Substantial						

#### ASSESSMENT PATTERN – THEORY

Test / Bloom's Category*	Remembering (K1) %	Understanding (K2) %	Applying (K3) %	Analyzing (K4) %	Evaluating (K5) %	Creating (K6) %	Total %
CAT1	40%	40%	20%				100%
CAT2	40%	40%	20%				100%
Individual Assessment 1 / Case Study 1 / Seminar 1 / Project 1							100%
Individual Assessment 2 / Case Study 2 / Seminar 2 / Project 2	40%	40%	20%				100%
ESE	40%	40%	20%				100%

<b>23AEPE04</b>	<b>SMART SENSORS</b>	<b>SEMESTER I</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>PE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>This course enables the students to learn the different types of sensors, smart sensors, interfacing sensors with MCU and their applications.</li></ul>		
<b>UNIT – I</b>	<b>DISPLACEMENT, FORCE AND PRESSURE SENSORS</b>	<b>9 Periods</b>	
Definition, Classification and selection of sensors – Measurement of displacement using Potentiometer, LVDT and Optical Encoder – Measurement of force using strain gauge – Measurement of pressure using LVDT based diaphragm and piezoelectric sensor.			
<b>UNIT – II</b>	<b>TEMPERATURE, POSITION, FLOW AND LEVEL SENSORS</b>	<b>9 Periods</b>	
Thermocouple and RTD – Concept of thermal imaging– Measurement of position using Hall effect sensors, Proximity sensors: Inductive and Capacitive – Use of proximity sensor as accelerometer and vibration sensor – Flow Sensors: Ultrasonic and Laser – Level Sensors: Ultrasonic and Capacitive.			
<b>UNIT –III</b>	<b>SMART SENSORS</b>	<b>9 Periods</b>	
General Structure of smart sensors and its components – Characteristic of smart sensors: Self calibration, Self-testing and self-communicating – Application of smart sensors: Automatic robot control and automobile engine control.			
<b>UNIT –IV</b>	<b>INTERFACING SENSOR INFORMATION AND MCU</b>	<b>9 Periods</b>	
Amplification and Signal Conditioning – Integrated Signal Conditioning – Digital conversion,MCU Control – MCUs for Sensor Interface, Techniques and System Consideration – Sensor Integration.			
<b>UNIT – V</b>	<b>APPLICATION FOR SMART SENSORS</b>	<b>9 Periods</b>	
Automotive Protocols – Industrial Networks – Home Automation – MCU Protocols – Wireless Data Communications – RF Sensing, Telemetry. Standards: IEEE 1451, STIM, Smart Plug- and-Play.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods    Tutorial: 0 Periods    Practical: 0 Periods    Total: 45Periods</b>			

#### REFERENCES:

1	<i>D.Patranabis, -Sensors and Transducers, Second Edition, Prentice Hall of India, 2005.</i>
2	<i>Randy Frank, -Understanding Smart Sensors, Third Edition, Artech House Publishers, 2013.</i>
3	<i>Jacob Fraden, “Hand Book of Modern Sensors: physics, Designs and Applications”, 2015, 3<sup>rd</sup> edition, Springer, New York.</i>
4	<i>Jon. S. Wilson, “Sensor Technology Hand Book”, 2011, 1st edition, Elsevier, Netherland.</i>
5	<i>Sabrie Solomon, “Sensors Handbook,” 2nd edition McGraw Hill, 1998.</i>
6	<i>Y.L. Lin, “Smart Sensors and Systems”, Springer, 2017.</i>



<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Understand the displacement, force and pressure sensors.	K2
C02	Exploit the temperature, position, flow and level sensors.	K2
C03	Gain knowledge on smart sensors and their applications.	K2
C04	Interface sensor information and MCU.	K3
C05	Gain knowledge about communication for smart sensors.	K2

<b>COURSE ARTICULATION MATRIX:</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	1	-	2	-	-
C02	3	1	-	2	-	-
C03	3	1	-	2	-	-
C04	3	1	-	2	-	-
C05	3	1	-	2	-	-
<b>23AEPE04</b>	3	1	-	2	-	-

1 – Slight, 2 – Moderate, 3 – Substantial

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20%	30%	50%				100%
CAT2	20%	30%	50%				100%
Individual /Assignment 1/case study/seminar 1/Project 1	20%	30%	50%				100%
Individual /Assignment 2/case study/seminar 2/Project 2	20%	30%	50%				100%
ESE	20%	30%	50%				100%

<b>23AEPE05</b>	<b>MULTIMEDIA COMPRESSION TECHNIQUES</b>	<b>SEMESTER I</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
NIL	PE	3	0	0	3

<b>Course Objective</b>	This course enables its students to learn above the <ul style="list-style-type: none"><li>Various data coding techniques, audio compression techniques, image and video compression techniques.</li></ul>		
<b>UNIT - I</b>	<b>INTRODUCTION</b>	<b>9 Periods</b>	
Compression Techniques – Overview of information theory - Lossless and Lossy coding– Modeling and Coding - Taxonomy of compression techniques – Rate distortion theory - Huffman coding – Non-Binary Huffman Codes –Adaptive Huffman Coding – Applications of Huffman Coding.			
<b>UNIT - II</b>	<b>ARITHMETIC CODING AND DICTIONARY TECHNIQUES</b>	<b>9 Periods</b>	
Introduction - Coding a sequence – Generating deciphering the tag – Generating a binary code – Uniqueness of arithmetic code – Algorithm, Integer implementation – Comparison of Huffman and Arithmetic coding – Applications -Static and Adaptive dictionary – LZ77, LZ78, LZW approach – Applications - Facsimile encoding – Run length coding – Comparison of MH, MR, MMR and JBIG - Scalar and Vector Quantization.			
<b>UNIT - III</b>	<b>AUDIO COMPRESSION</b>	<b>9 Periods</b>	
Audio compression techniques - Frequency domain and filtering - Basic sub-band coding - Application to speech coding - G.722 - Application to audio coding - MPEG audio - Silence suppression - Speech compression techniques – Vocoders.			
<b>UNIT - IV</b>	<b>IMAGE COMPRESSION</b>	<b>9 Periods</b>	
Predictive techniques - DPCM, DM - KL transform – Discrete cosine, Walsh, Hadamardtransform - JPEG, Wavelet based Compression – Quad Trees – EZW, SPIHT, JPEG 2000.			
<b>UNIT - V</b>	<b>VIDEO COMPRESSION</b>	<b>9 Periods</b>	
Video signal representation – Motion compensation – MPEG standards - Motion estimationtechniques - H.261 family of standards - Motion video compression.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods   Tutorial: 0 Periods   Practical: 0 Periods   Total:45 Periods</b>			

## REFERENCES

1	Khalid Sayood, <i>"Introduction to Data Compression"</i> , Morgan Kaufman, 2017.
2	Salomon D, <i>"Data Compression The Complete Reference"</i> , Springer, 2015.
3	Jan Vozer, <i>"Video Compression for Multimedia"</i> , AP Press, New York, 1995.
4	AlistarMoffat, <i>"Compression and Coding Algorithms"</i> , Kluwer Academic Publishers, 2002
5	Salomon D, <i>"A Guide to Data Compression Methods"</i> , Springer, 2002.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Code information using various Lossy and Lossless methods.	K2
C02	Apply the concepts dictionary based coding techniques.	K3
C03	Do various analysis on audio compression.	K3
C04	Implement image and video compression	K2
C05	Describe various video compression techniques	K2

<b>COURSE ARTICULATION MATRIX:</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	-	2	1	-	1
C02	3	-	2	1	-	1
C03	3	-	2	1	-	1
C04	3	-	2	1	-	1
C05	3	-	2	1	-	1
<b>23AEPE05</b>	3	-	2	1	-	1
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	40%	40%	20%				100 %
CAT2	40%	40%	20%				100 %
Individual /Assignment 1/case study/seminar 1/Project 1		50%	50%				100 %
Individual /Assignment 2/case study/seminar 2/Project 2		50%	50%				100 %
ESE	40%	40%	20%				100%

<b>23AEPE06</b>	<b>ANALOG IC DESIGN</b> (Common to Applied Electronics and VLSI Design)	<b>SEMESTER II</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
NIL	PE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To develop the skills to design analog VLSI circuits for a given specification.</li></ul>		
<b>UNIT – I</b>	<b>MOS DEVICE PHYSICS</b>	<b>9 Periods</b>	
General Considerations, MOS I/V Characteristics, Second Order effects, MOS Device models- Long channel versus short channel devices. Single Stage Amplifiers – General considerations, Common Source Stage: CS stage with resistive load, CS stage with diode connected load, CS stage with current source load, Source Follower stage, Common Gate Stage, Cascode Stage.			
<b>UNIT – II</b>	<b>MOS AMPLIFIERS AND CURRENT MIRRORS</b>	<b>9 Periods</b>	
Differential Amplifiers –single Ended and Differential Operation, Basic Differential Pair, Common mode response, Differential Pair with MOS loads, Gilbert Cell. Basic Current Mirrors, Cascode Current Mirrors, Active Current Mirrors.			
<b>UNIT – III</b>	<b>FREQUENCY AND NOISE CHARACTERISTICS OF MOS AMPLIFIERS</b>	<b>9 Periods</b>	
Frequency Response of Amplifiers: Miller’s effect, Common Source Stage, Source Followers, Common Gate Stage, Cascode Stage.-Noise: Types of Noise, Representation of Noise in circuits, Noise in single stage amplifiers, Noise in cascade stage, Noise in current mirrors, Noise power trade-off, Noise bandwidth.			
<b>UNIT – IV</b>	<b>CMOS OPERATIONAL AMPLIFIERS</b>	<b>9 Periods</b>	
Properties of feedback circuits – Effect of feedback on noise -Operational Amplifiers – General Considerations, One Stage Op Amps- design procedure, Two Stage Op Amps, Common-Mode Feedback, Input Range limitations, Slew Rate, Power Supply Rejection, Noise in Op Amps. Concept of Stability and Frequency Compensation in Op. Amps- Basic PLL Topology- Dynamics of Simple PLL - Problem of Lock Acquisition- Charge Pump- Basic Charge-Pump PLL.			
<b>UNIT – V</b>	<b>D/A AND A/D CONVERTERS</b>	<b>9 Periods</b>	
Ideal A/D and D/A converters, Quantization noise, Signed codes, Performance limitations. Nyquist Rate D/A converters: Decoder based Binary scaled, Current mode and hybrid D /A converters – Nyquist A/D Converters: Integrating type, Successive approximation type, Algorithmic type, Interpolating, Pipelined, Time interleaved A/D converters, High performance A/D converters.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>			

#### REFERENCES:

1	Behzad Razavi, " <b>Design of Analog CMOS Integrated circuits</b> ", McGraw Hill Education, 2 <sup>nd</sup> edition, 2016.
2	David Johns, Ken Martin, " <b>Analog Integrated circuit design</b> ", Wiley, 2 <sup>nd</sup> edition, 2013.
3	Paul R. Gray, Paul J.Hurst, Stephen H.Lewis, and Robert G. Meyer, " <b>Analysis and Design of Analog Integrated circuits</b> ", Wiley, 5 <sup>th</sup> edition, 2009.
4	R. Jacob Baker, " <b>CMOS Circuit Design, Layout, and Simulation</b> ", Wiley, 3 <sup>rd</sup> edition, 2010.

<b>COURSE OUTCOMES:</b> Upon completion of the course, the students will be able to:		<b>Bloom's Taxonomy Mapped</b>
CO1	Explain and analyze the MOS device models for different configurations.	K3
CO2	Design various MOS amplifiers and Current mirror circuits,	K4
CO3	Discuss the effects of frequency on MOS amplifier characteristics	K3
CO4	Discuss the effects of feedback and noise in CMOS Operational amplifiers and explain the operation of PLL	K2
CO5	Reproduce and explain the operation of various Nyquist rate data converters	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
CO1	3	1	-	1	-	1
CO2	3	1	-	1	-	1
CO3	3	1	-	1	-	1
CO4	3	1	-	1	-	1
CO5	3	1	-	1	-	1
<b>23AEPE06</b>	3	1	-	1	-	1
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remember ing (K1) %</b>	<b>Understandin g (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluatin g (K5) %</b>	<b>Creatin g (K6) %</b>	<b>Total %</b>
CAT1	40%	40%	20%				100%
CAT2	40%	40%	20%				100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1		50%	30%	20%			100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2		50%	30%	20%			100%
ESE	30%	30%	20%	20%			100%

<b>23AEPE07</b>	<b>EMI AND COMPATIBILITY</b>	<b>SEMESTER II</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
NIL	<b>PE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>The students will be able to gain broad conceptual understanding of the various aspects of electromagnetic (EM) interference and compatibility</li></ul>		
<b>UNIT – I</b>	<b>INTRODUCTION &amp; SOURCES OF EM INTERFERENCE</b>	<b>9 Periods</b>	
Introduction - Classification of sources - Natural sources - Man-made sources - Survey of the electromagnetic environment.			
<b>UNIT – II</b>	<b>EM SHIELDING</b>	<b>9 Periods</b>	
Introduction - Shielding effectiveness - Far-field sources - Near-field sources - Low-frequency, magnetic field shielding - Effects of apertures			
<b>UNIT – III</b>	<b>INTERFERENCE CONTROL TECHNIQUES</b>	<b>9 Periods</b>	
Equipment screening - Cable screening - grounding - Power-line filters - Isolation - Balancing -Signal-line filters - Nonlinear protective devices.			
<b>UNIT – IV</b>	<b>EMC STANDARDS, MEASUREMENTS AND TESTING</b>	<b>9 Periods</b>	
Need for standards - The international framework - Human exposure limits to EM fields –EMC measurement techniques - Measurement tools - Test environments.			
<b>UNIT – V</b>	<b>EMC CONSIDERATIONS IN WIRELESS AND BROADBAND TECHNOLOGIES</b>	<b>9 Periods</b>	
Efficient use of frequency spectrum - EMC, interoperability and coexistence - Specifications and alliances - Transmission of high-frequency signals over telephone and power networks – EMC and digital subscriber lines - EMC and power line telecommunications.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>			

#### REFERENCES:

1	<i>Christopoulos C, <b>Principles and Techniques of Electromagnetic Compatibility</b>, CRC Press, Second Edition, Indian Edition, 2013.</i>
2	<i>Paul C R, <b>Introduction to Electromagnetic Compatibility</b>, Wiley India, Second Edition, 2008.</i>
3	<i>Kodali V P, <b>Engineering Electromagnetic Compatibility</b>, Wiley India, Second Edition, 2010.</i>
4	<i>Henry W Ott, <b>Electromagnetic Compatibility Engineering</b>, John Wiley &amp; Sons Inc, New York, 2009.</i>
5	<i>Scott Bennett W, <b>Control and Measurement of Unintentional Electromagnetic Radiation</b>, John Wiley &amp; Sons Inc., Wiley Interscience Series, 2007.</i>

<b>COURSE OUTCOMES:</b> Upon completion of the course, students will be able to/have:		<b>Bloom's Taxonomy Mapped</b>
C01	Discuss the various sources of electromagnetic interference	K2
C02	Explain the EMI mitigation techniques of shielding and grounding	K2
C03	Recall the controlling mechanism of electro-magnetic interference.	K2
C04	Explain the need for standards and EMC measurement methods	K2
C05	Discuss about the EM compliance considerations in wireless systems.	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	1	-	1	-	1
C02	3	1	-	1	-	1
C03	3	1	-	1	-	1
C04	3	1	-	1	-	1
C05	3	1	-	1	-	1
<b>23AEPE07</b>	<b>3</b>	<b>1</b>	<b>-</b>	<b>1</b>	<b>-</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Rememb ering (K1) %</b>	<b>Understan ding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	50%	50%					100%
CAT2	50%	50%					100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1	50%	50%					100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2	50%	50%					100%
ESE	50%	50%					100%

<b>23AEPE08</b>	<b>ADVANCED COMMUNICATION SYSTEMS</b>	<b>SEMESTER II</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<ul style="list-style-type: none"> <li>Digital Communication, Wireless Communication</li> </ul>	<b>PE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To acquire knowledge about the wireless channel characteristics, Diversity techniques, Massive MIMO, Millimeter Wave Communication, D2D and 5G.</li></ul>		
<b>UNIT – I</b>	<b>WIRELESS CHANNELS</b>	<b>9 Periods</b>	
Radio wave propagation, Physical modeling for wireless channels, Path loss and Shadowing, outage probability under path loss and shadowing, time and frequency coherence, Statistical multipath channel models, narrowband fading models, wideband fading models, Discrete-time model, Space-time channel models.			
<b>UNIT – II</b>	<b>MIMO DIVERSITY AND SPATIAL MULTIPLEXING</b>	<b>9 Periods</b>	
Sources and types of diversity, analysis under Rayleigh fading, Diversity and channel knowledge. Alamouti space time code. MIMO spatial multiplexing: Space time receivers, ML, ZF, MMSE and Spheredecoding, BLAST receivers and Diversity multiplexing trade - off.			
<b>UNIT – III</b>	<b>MASSIVE MIMO SYSTEM</b>	<b>9 Periods</b>	
Introduction - MIMO for LTE, capacity of massive MIMO, Pilot Design for massive MIMO, Resource allocation and transceivers design, Base band and RF implementation, Channel Models.			
<b>UNIT – IV</b>	<b>MILLIMETER WAVE COMMUNICATION AND DEVICE-to-DEVICE COMMUNICATION</b>	<b>9 Periods</b>	
Millimeter-wave Communications – spectrum regulations, deployment scenarios, beam-forming, physical layer techniques, interference and mobility management. Device-to-device (D2D) and machine-to-machine (M2M) type communications – Extension of 4G D2D standardization to 5G, radio resource management for mobile broadband D2D, multi-hop and multi-operator D2D communications.			
<b>UNIT – V</b>	<b>TRANSMISSION AND DESIGN TECHNIQUES FOR 5G</b>	<b>9 Periods</b>	
Basic requirements of transmission over 5G, Modulation Techniques – Orthogonal frequency division multiplexing (OFDM), generalized frequency division multiplexing (GFDM), filter bank multi-carriers (FBMC) and universal filtered multi-carrier (UFMC), Multiple Accesses Techniques – orthogonal frequency division multiple accesses (OFDMA), generalized frequency division multiple accesses (GFDMA), non-orthogonal multiple accesses (NOMA).			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>			

<b>REFERENCES:</b>	
1.	Theodore S.Rappaport, Robert W.Heath, Robert C.Danials, James N.Murdock “ <b>Millimeter Wave Wireless Communications</b> ”, Prentice Hall Communications.
2.	Hamid Jafarkhani, “ <b>Space - Time Coding: Theory and Practices</b> ”, Cambridge University Press 2005.
3.	Wei Xiang, Kan Zheng, Xuemin (Sherman) Shen, - “ <b>5G Mobile Communications</b> ”, Springer, 2017.





**ASSESSMENT PATTERN - THEORY**

<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	50%	50%					100%
CAT2	50%	50%					
Individual /Assignment 1/case study/seminar 1/Project 1	50%	50%					
Individual /Assignment 2/case study/seminar 2/Project 2	50%		50%				100%
ESE	50%	50%					100%



<b>23AEPE09</b>	<b>MEMS AND NEMS</b>	<b>SEMESTER II</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>Nil</b>	<b>PE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b> <ul style="list-style-type: none"><li>• To familiarize the concepts of fabrication process of Microsystems, micro sensors, Micro actuators and quantum mechanics and Nano systems.</li></ul>		
<b>UNIT – I</b>	<b>OVERVIEW OF MEMS AND NEMS</b>	<b>9 Periods</b>
New trends in Engineering and Science: Micro and Nanoscale systems, Introduction to Design of MEMS and NEMS, MEMS and NEMS – Applications, Devices and structures. Materials for MEMS: Silicon, silicon compounds, polymers, metals.		
<b>UNIT – II</b>	<b>MEMS FABRICATION TECHNOLOGIES</b>	<b>9 Periods</b>
Microsystem fabrication processes: Photolithography, Ion Implantation, Diffusion, Oxidation. Thin film depositions: LPCVD, Sputtering, Evaporation, Electroplating; Etching techniques: Dry and wet etching, electrochemical etching; Micromachining: Bulk Micromachining, Surface Micromachining, High Aspect- Ratio (LIGA and LIGA-like) Technology; Packaging: Microsystems packaging, Essential packaging technologies, Selection of packaging materials		
<b>UNIT – III</b>	<b>MICRO SENSORS</b>	<b>9 Periods</b>
MEMS Sensors: Design of Acoustic wave sensors, resonant sensor, Vibratory gyroscope, Capacitive and Piezo Resistive Pressure sensors- engineering mechanics behind these Micro sensors. Case study: Piezo-resistive pressure sensor.		
<b>UNIT – IV</b>	<b>MICRO ACTUATORS</b>	<b>9 Periods</b>
Design of Actuators: Actuation using thermal forces, Actuation using shape memory Alloys, Actuation using piezoelectric crystals, Actuation using Electrostatic forces (Parallel plate, Torsion bar, Comb drive actuators), Micromechanical Motors and pumps. Case study: Comb drive actuators.		
<b>UNIT – V</b>	<b>NANOSYSTEMS AND QUANTUM MECHANICS</b>	<b>9 Periods</b>
Atomic Structures and Quantum Mechanics, Molecular and Nanostructure Dynamics: Schrodinger Equation and Wave function Theory, Density Functional Theory, Nanostructures and Molecular Dynamics, Electromagnetic Fields and their quantization, Molecular Wires and Molecular Circuits .		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>		

REFERENCES:	
1	Chang Liu, <i>“Foundations of MEMS”</i> , Pearson education India limited, 2006.
2	Marc Madou, <i>“Fundamentals of Microfabrication”</i> , CRC press 1997.
3	Sergey Edward Lyshevski, <i>“MEMS and NEMS: Systems, Devices, and Structures”</i> CRCPress, 2002.
4	Stephen D. Senturia, <i>“Micro system Design”</i> , Kluwer Academic Publishers, 2001
5	Tai Ran Hsu, <i>“MEMS and Microsystems Design and Manufacture”</i> , Tata Mcraw Hill, 2002.

<b>COURSE OUTCOMES:</b> Upon completion of the course, students will be able to/have:		<b>Bloom's Taxonomy Mapped</b>
C01	Explain MEMS, NEMS and its applications	K2
C02	Understand MEMS fabrication technologies	K2
C03	Acquire knowledge on micro sensors	K2
C04	Design micro actuators	K3
C05	Outline Nano systems and Quantum mechanics	K3

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	-	2	-	-	1
C02	3	-	2	-	-	1
C03	3	-	2	-	-	1
C04	3		2			1
C05	3		2			1
<b>23AEPE09</b>	3	-	2	-	-	1
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	50%	20%	30%				100%
CAT2	50%	20%	30%				100%
Individual /Assignment 1/case study/seminar 1/Project 1	50%	40%	10%				100%
Individual /Assignment 2/case study/seminar 2/Project 2	50%	25%	25%				100%
ESE	50%	20%	30%				100%

<b>23AEPE10</b>	<b>SOFT COMPUTING AND OPTIMIZATION TECHNIQUES</b>	<b>SEMESTER II</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>PE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b> <ul style="list-style-type: none"> <li>To gain knowledge in neural networks, fuzzy logic, genetic algorithm, neuro- fuzzy modeling and the various conventional optimization techniques</li> </ul>		
<b>UNIT-I</b>	<b>FUZZY LOGIC</b>	<b>9 Periods</b>
Introduction to Fuzzy logic –Fuzzy sets and membership functions – Operations on Fuzzy sets-Fuzzy relations, rules, propositions, implications, and inferences – Defuzzification techniques -Fuzzy logic controller design-Some applications of Fuzzy logic.		
<b>UNIT -II</b>	<b>ARTIFICIAL NEURAL NETWORKS</b>	<b>9 Periods</b>
Supervised Learning: Introduction and how brain works, Neuron as a simple computing element, The perceptron, Back propagation networks: architecture, multilayer perceptron, back propagation learning-input layer, accelerated learning in multilayer perceptron, The Hopfield network, Bidirectional associative memories(BAM),RBF Neural Network. Unsupervised Learning: Hebbian Learning, Generalized Hebbian learning algorithm, Competitive learning, Self-Organizing Computational Maps: Kohonen Network.		
<b>UNIT -III</b>	<b>GENETIC ALGORITHM</b>	<b>9 Periods</b>
Genetic algorithm – Introduction – biological background –traditional optimization and search techniques-Genetic basic concepts- operators-Encoding scheme-Fitness evaluation- crossover - mutation-Travelling Salesman Problem, Particle swarm optimization, Ant colony optimization.		
<b>UNIT -IV</b>	<b>NEURO-FUZZY MODELING</b>	<b>9 Periods</b>
Adaptive Neuro-Fuzzy Inference Systems (ANFIS) – architecture - Coactive Neuro-Fuzzy Modeling, framework, neuron functions for adaptive networks-Data Clustering Algorithms-Rule base Structure Identification –Neuro-Fuzzy Control –the inverted pendulum system.		
<b>UNIT -V</b>	<b>CONVENTIONAL OPTIMIZATION TECHNIQUES</b>	<b>9 Periods</b>
Introduction to optimization techniques, Statement of an optimization problem, classification, Unconstrained optimization- gradient search method - Gradient of a function, steepest gradient-conjugate gradient, Newton's Method, Marquardt Method, Constrained optimization-sequential linear programming, Interior penalty function method, external penalty function method.		
<b>Contact Periods:</b> <b>Lecture:45 Periods      Tutorial:0Periods      Practical:0Periods      Total:45 Periods</b>		

#### REFERENCES:

1.	George J. Klir and Bo Yuan, <b>"Fuzzy Sets and Fuzzy Logic-Theory and Applications"</b> , Prentice Hall, 1995.
2.	James A. Freeman and David M. Skapura, <b>"Neural Networks Algorithms, Applications and Programming Techniques"</b> , Pearson Edn., 2003.
3.	S. Rajasekaran, G. A. Vijayalakshmi Pai, <b>"Neural Networks, Fuzzy Logic and Genetic Algorithm, Synthesis and Applications"</b> , PHI Learning Pvt. Ltd., 2017.
4.	Timothy J. Ross, <b>"Fuzzy Logic with Engineering Applications"</b> , McGraw-Hill, 1997.

5.	Singiresu S. Rao, <b><i>“Engineering optimization Theory and practice”</i></b> , John Wiley & sons, inc, Fourth Edition, 2009
6.	J.S.R.Jang, C.T.Sunand, E.Mizutani, <b><i>“Neuro-Fuzzy and Soft Computing”</i></b> , PHI/Pearson Education, 2004.
7.	David. E.Goldberg, <b><i>“Genetic Algorithms in Search, Optimization and Machine Learning”</i></b> , Addison wesley, 2009.

COURSEOUTCOMES: Upon completion of the course, students will be able to/		Bloom's Taxonomy Mapped
C01	Understand Fuzzy logic concepts	K2
C02	Interpret the mathematical background of supervised learning and unsupervised learning in artificial neural networks	K3
C03	Understand the mathematical background of Genetic algorithm	K2
C04	Understand Neuro- fuzzy modeling	K2
C05	Solve Unconstrained optimization and constrained optimization problems	K3

COURSE ARTICULATION MATRIX :						
COs/POs	PO1	PO2	PO3	PO4	PO5	PO6
C01	3	-	2	-	-	1
C02	3	-	2	-	-	1
C03	3	-	2	-	-	1
C04	3	-	2	-	-	1
C05	3	-	2	-	-	1
<b>23AEPE10</b>	3	-	2	-	-	1
1 – Slight, 2 – Moderate, 3 – Substantial						

ASSESSMENT PATTERN - THEORY							
Test / Bloom's Category*	Remembering (K1) %	Understanding (K2) %	Applying (K3) %	Analyzing (K4) %	Evaluating (K5) %	Creating (K6) %	Total %
CAT1	40%	40%	20%				100%
CAT2	40%	40%	20%				100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1		50%	50%				100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2		50%	50%				100%
ESE	40%	40%	20%				100%

<b>23AEPE11</b>	<b>MODELING AND SYNTHESIS WITH HDL</b>	<b>SEMESTER III</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>PE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

### Course Objective

- To understand the language features of Verilog HDL, levels of modeling and synthesis of combinational and sequential circuits in digital logic design.

### UNIT – I INTRODUCTION TO LOGIC DESIGN WITH VERILOG 9 Periods

Overview of Digital Design with Verilog HDL - Hierarchical Modeling Concepts: Top-down and bottom-up design methodology, differences between modules and module instances, parts of a simulation, design block, stimulus block - Basic Concept- Modules and Ports: Module definition-port declaration-connecting ports-hierarchical name referencing. Tasks and Functions.

### UNIT – II LEVELS OF MODELING 9 Periods

Gate Level Modeling: Modeling using basic Verilog gate primitives, description of and/or and buf/not type gates, rise, fall and turn-off delays, min, max, and typical delays. Dataflow Modeling: Continuous assignments, delay specification, expressions, operators, operands, operator types. Behavioral Modeling: Structured procedures, initial and always, blocking and nonblocking statements, delay control, generate statement, event control, conditional statements, multiway branching, loops, sequential and parallel blocks.

### UNIT – III DESIGN OF DIGITAL LOGIC USING HDL 9 Periods

Design of combinational logic: adders-multiplexers, de-multiplexers, encoders and decoders-comparators-multipliers - Design of Sequential logic: Flip-flops-synchronous and Asynchronous counters-shift registers-Universal shift register-FSM and LFSR. (Using various Levels of Modeling).

### UNIT – IV LOGIC SYNTHESIS AND DESIGN FLOW 9 Periods

Logic Synthesis with verilog HDL-Synthesis Design flow, RTL and Test Bench Modeling Techniques and Timing and Path Delay Modeling, Timing Checks, Switch Level Modeling.

### UNIT – V PROGRAMMABLE LOGIC DEVICES 9 Periods

Programmable logic devices, storage devices, programmable logic array- programmable array logic-programmability of PLDs and CPLDs.

### Contact Periods:

**Lecture: 45 Periods    Tutorial: 0 Periods    Practical: 0 Periods    Total: 45 Periods**

### REFERENCES:

1	Samir Palnitkar - Verilog HDL, 2nd edition, Pearson Education, 2003.
2	Michael D Ciletti - Advanced Digital Design with the VERILOG HDL, 2ND Edition, PHI, 2009.
3	Z Navabi - Verilog Digital System Design, 2nd Edition, McGraw Hill, 2005.
4	Stephen Brown and Zvonko Vranesic - Fundamentals of Digital Logic with Verilog, 2nd Edition, TMH, 2008.
5	Charles H Roth, Larry L. Kinney - Fundamentals of Logic Design, 2015

<b>COURSE OUTCOMES:</b> Upon completion of the course, students will be able to/have:		<b>Bloom's Taxonomy Mapped</b>
C01	Knowledge on logic design with Verilog HDL	K2
C02	Explain different levels of modelling digital systems	K2
C03	Understand Design of combinational logic with HDL	K3
C04	Understand logic synthesis and design flow	K3
C05	Knowledge on programmable logic devices	K2

<b>COURSE ARTICULATION MATRIX:</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	2	2	-	-	-	-
C02	3	2	-	2	-	-
C03	3	2	-	2	-	-
C04	3	2	-	2	-	-
C05	2	2	-	-	-	-
<b>23AEPE11</b>	<b>2</b>	<b>2</b>	<b>-</b>	<b>2</b>	<b>-</b>	<b>-</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20%	40%	40%				100%
CAT2	20%	40%	40%				100%
Assignment 1	10%	40%	50%				100%
Assignment 2	10%	40%	50%				100%
Quiz1	30%	30%	40%				100%
Quiz 2	30%	30%	40%				100%
ESE	20%	40%	40%				100%



23AEPE12	RF SYSTEM DESIGN	SEMESTER III
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	PE	3	0	0	3

<b>Course Objective</b>	Upon completion of this course, the students will be familiar with: <ul style="list-style-type: none"><li>Issues with RF components and design of micro strip line, RF Filters, RF amplifiers, Low Noise Amplifiers, RF Oscillators, Mixers and RF Front end components.</li></ul>		
<b>UNIT – I</b>	<b>RF ISSUES, MICROSTRIP LINE DESIGN</b>	<b>9 Periods</b>	
Issues in RF system design - RF behavior of passive components: High Frequency Resistors – High Frequency Capacitors – High Frequency Inductors- Chip Resistors-Chip Capacitors-Surface Mounted Inductors- Micro strip Transmission line design –Short circuit transmission line – Open circuit transmission line-Quarter-wave Transmission Line-Sourced & Loaded Transmission Line-Scattering Parameters – Smith chart based Impedance Matching using Two-Discrete Components.			
<b>UNIT – II</b>	<b>RF FILTER DESIGN</b>	<b>9 Periods</b>	
Basic Resonator and Filter configurations - Filter characteristics - Filter design based on Insertion Loss Method - Butterworth and Chebyshev filters - Prototype filter design and normalization - LPF, HPF, BPF and BSF - Filter Implementation – Kuroda's Identities - Micro strip realization of filters			
<b>UNIT – III</b>	<b>RF AMPLIFIER DESIGN</b>	<b>9 Periods</b>	
Characteristics of RF Amplifiers – Transducer Power gain - Unilateral power gain - Available power gain - Stability - Stability Circles - Tests for unconditional stability - Single Stage Transistor Amplifier Design: Design for maximum gain, Design for constant gain & Low noise amplifier design.			
<b>UNIT – IV</b>	<b>RF OSCILLATOR DESIGN</b>	<b>9 Periods</b>	
Basic Oscillator Model - Negative Resistance oscillator - Feedback oscillator design: Hartley, Colpiit's and Clapp Oscillators for RF Systems-High Frequency oscillator: Fixed frequency Oscillator - Voltage controlled oscillator.			
<b>UNIT – V</b>	<b>MIXERS &amp; RF FRONT END DESIGN</b>	<b>9 Periods</b>	
Mixers: Basic Characteristics of Mixers - Single Ended mixer design, Single Balanced Mixer – Double Balanced Mixer - Image Reject Mixer. RF Front End and Tuner building blocks- RF directional couplers and hybrid couplers - Complete RF Tuner design considerations.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>			

REFERENCES:	
1	Reinhold Ludwig and Gene Bogdanov, "RF Circuit Design – Theory and Applications", Prentice Hall, 2009.
2	David M. Pozar, " <b>Microwave Engineering</b> ", John Wiley and Sons, 2009.
3	David M. Pozar, " <b>Microwave and RF Wireless Systems</b> ", Wiley, 2001.
4	Harvey Lehpamer, " <b>Transmission System Design Handbook for Wireless Networks</b> ", Artech House, 2002.
5	Stephan A Mass, " <b>Non-Linear Microwave and RF circuits</b> ", Artech House, Second Edition, 2003.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Explain issues of passive components at RF and analyze micro strip line	K2
C02	Design RF filters for the filter configurations	K3
C03	Design RF amplifiers for given specifications	K3
C04	Design and explain various types of mixer circuits.	K3
C05	Design and explain the RF front end components and mixers.	K3

<b>COURSE ARTICULATION MATRIX:</b>						
COs/POs	PO1	PO2	PO3	PO4	PO5	PO6
C01	2	2	1	1	-	1
C02	2	2	1	1	-	1
C03	2	2	1	1	-	1
C04	2	2	1	1	-	1
C05	2	2	1	1	-	1
<b>23AEPE12</b>	<b>2</b>	<b>2</b>	<b>1</b>	<b>1</b>	<b>-</b>	<b>1</b>

1 – Slight, 2 – Moderate, 3 – Substantial

<b>ASSESSMENT PATTERN – THEORY</b>							
Test / Bloom's Category *	Remembering (K1)%	Understanding (K2) %	Applying (K3) %	Analyzing (K4)%	Evaluating (K5) %	Creating (K6)%	Total %
CAT1	20%	50%	30%				100 %
CAT2	20%	50%	30%				100 %
Individual /Assignment 1/case study/seminar 1/Project 1	20%	50%	30%				100 %
Individual /Assignment 2/case study/seminar 2/Project 2	20%	50%	30%				100 %
ESE	20%	50%	30%				100 %

<b>23AEPE13</b>	<b>ADVANCED COMPUTER ARCHITECTURE AND PARALLEL PROCESSING</b>	<b>SEMESTER III</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>PE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To understand the difference between the pipeline and parallel concepts, various types of architectures and the importance of scalable architectures, memories and optimization of memory.</li></ul>		
<b>UNIT - I</b>	<b>COMPUTER DESIGN AND PERFORMANCE MEASURES</b>	<b>9 Periods</b>	
Fundamentals of Computer Design – Parallel and Scalable Architectures – Multiprocessors –Multivector and SIMD architectures – Multithreaded architectures – Data-flow architectures - Performance Measures			
<b>UNIT – II</b>	<b>PARALLEL PROCESSING, PIPELINING AND ILP</b>	<b>9 Periods</b>	
Instruction Level Parallelism and Its Exploitation - Concepts and Challenges - Overcoming DataHazards with Dynamic Scheduling – Dynamic Branch Prediction - Speculation - Multiple Issue Processors - Performance and Efficiency in Advanced Multiple Issue Processors			
<b>UNIT – III</b>	<b>MEMORY HIERARCHY DESIGN</b>	<b>9 Periods</b>	
Memory Hierarchy - Memory Technology and Optimizations – Cache memory – Optimizations of Cache Performance – Memory Protection and Virtual Memory - Design of Memory Hierarchies			
<b>UNIT – IV</b>	<b>MULTIPROCESSORS</b>	<b>9 Periods</b>	
Symmetric and distributed shared memory architectures – Cache coherence issues - Performance Issues – Synchronization issues – Models of Memory Consistency - Interconnection networks – Buses, crossbar and multi-stage switches.			
<b>UNIT – V</b>	<b>MULTI-CORE ARCHITECTURES</b>	<b>9 Periods</b>	
Software and hardware multithreading – SMT and CMP architectures – Design issues – Casestudies – Intel Multi-core architecture – SUN CMP architecture – IBM cell architecture – hp architecture.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>			

#### REFERENCES:

1	Kai Hwang, " <b>Advanced Computer Architecture</b> ", McGraw Hill International, 2001.
2	William Stallings, " <b>Computer Organization and Architecture – Designing for Performance</b> ", Pearson Education, Seventh Edition, 2006.
3	John P. Hayes, " <b>Computer Architecture and Organization</b> ", McGraw Hill.
4	John L. Hennessy and David A. Patterson, " <b>Computer Architecture – A quantitative approach</b> ", Morgan Kaufmann / Elsevier, 4th. edition, 2007.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Understand the Computer design and its performance measures.	K2
C02	Discuss the limitations and Applications of ILP.	K3
C03	Identify issues related to memory hierarchy and suggest solutions	K2
C04	Understand the different multiprocessor and its real time applications	K2
C05	Illustrate various techniques used in multicore architecture	K2

<b>COURSE ARTICULATION MATRIX:</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	1	-	3	-	1
C02	3	1	-	3	-	1
C03	3	1	-	3	-	2
C04	3	1	-	3	-	2
C05	3	1	-	3	-	2
<b>23AEPE13</b>	<b>3</b>	<b>1</b>	<b>-</b>	<b>3</b>	<b>-</b>	<b>2</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	40%	40%	20%				100%
CAT2	40%	40%	20%				100%
Individual /Assignment 1/case study/seminar 1/Project 1		50%	50%				100%
Individual /Assignment 2/case study/seminar 2/Project 2		50%	50%				100%
ESE	40%	40%	20%				100%

<b>23AEPE14</b>	<b>EMBEDDED PROCESSORS</b>	<b>SEMESTER III</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>PE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To learn the basic concepts of MSP430 microcontroller and interfacing concepts and also ARM processor and its instruction set.</li></ul>		
<b>UNIT – I</b>	<b>EMBEDDED SOFTWARE TOOLS</b>	<b>9 Periods</b>	
Software development tools- editor, assembler, compiler, cross-compiler and simulator, Hardware development tools- development board, device programmer, in-circuit emulator and debuggers. Embedded C Programming, data types and variables, data type modifiers, storage Class modifiers, C statements, structures and operations, pointers, libraries, in-line assembly programming, optimizing and testing embedded C programs.			
<b>UNIT – II</b>	<b>MSP430 CONTROLLER</b>	<b>9 Periods</b>	
MSP430 – Introduction to Architecture - Embedded C Programming in MSP430 – GPIO Pins & Configuration Timers, Capture, & PWM DAC- ADCs –Memory System-Flash Memory-DMA.			
<b>UNIT – III</b>	<b>INTERFACING WITH MSP430</b>	<b>9 Periods</b>	
USCI Port –SPI mode - I2C Mode-UART Mode & RS232 Low Power Mode Operation- Interfacing- Input Devices- Output Devices-DC Motor-Stepper Motor- Alarm interface- AC Devices.			
<b>UNIT – IV</b>	<b>ARM PROCESSOR FUNDAMENTALS</b>	<b>9 Periods</b>	
The RISC design philosophy-ARM design philosophy-Embedded system hardware- AMBA bus protocol, Embedded system software-Applications-ARM core data flow model-Registers- CPSR- Pipeline-Characteristics- ARM 3 stage Pipeline and 5 stage Pipeline-ARM instruction execution- Exceptions, Interrupts and Vector Table.			
<b>UNIT – V</b>	<b>ARM AND THUMB INSTRUCTION SET</b>	<b>9 Periods</b>	
ARM Instruction-Data processing instructions, Branch instructions, Load Store instructions, SWI instruction- Loading Instructions-Conditional Execution. Thumb Instruction-Thumb Registers-ARM Thumb Interworking- Branch instruction, Data processing instruction, Single/multiple load store instruction, Stack instruction, SWI instruction.			
<b>Contact Periods:</b> <b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>			

## REFERENCE

1	Steven Barrett, Daniel Pack, <b><i>“Microcontroller Programming and Interfacing TI MSP430, Part 1”</i></b> , Morgan and Claypool, 2011.
2	Brock J. LaMeres, <b><i>“Embedded Systems Design Using the MSP430FR2355 LaunchPadTM”</i></b> , Springer International Publishing, 2020.
3	John H. Davies, <b><i>“MSP430 Microcontrollers Basics”</i></b> , Elsevier Limited 2008.
4	Andrew N. Sloss Dominic Symes Chris Wright, <b><i>“ARM System Developer’s Guide Designing and Optimizing System Software”</i></b> , 1st edition Elsevier Inc 2010.
5	Steve Furber, <b><i>“ARM system on chip architecture”</i></b> , Second Edition, Pearson Education, 2015.

<b>COURSE OUTCOMES:</b> Upon completion of the course, the students will be able to:		<b>Bloom's Taxonomy Mapped</b>
C01	Interpret the various embedded software tools.	K2
C02	Exploit the fundamental blocks of MSP430 microcontroller.	K2
C03	Interface Peripherals with MSP430 microcontroller.	K3
C04	Exploit the basic concepts of ARM processor.	K2
C05	Summarize the ARM and THUMB instruction set.	K3

<b>COURSE ARTICULATION MATRIX:</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	1	2	2	1	
C02	3	1	2	2	1	
C03	3	1	2	2	1	
C04	3	1	2	2	1	
C05	3	1	2	2	1	
<b>23AEPE14</b>	3	1	2	2	1	
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understandi ng (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	30%	30%	40%				100%
CAT2	30%	30%	40%				100%
Individual /Assignment 1/case study/semina r 1/Project 1	30%	30%	40%				100%
Individual /Assignment 2/case study/semina r 2/Project 2	20%	30%	50%				100%
ESE	30%	30%	40%				100%

23AEPE15	BIO-MEDICAL IMAGE PROCESSING	SEMESTER III
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	PE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>This course aims to provide an insight to the Medical imaging modalities and reconstruction techniques.</li></ul>		
<b>UNIT - I</b>	<b>IMAGE CHARACTERISTICS AND QUALITY METRICS</b>	<b>9 Periods</b>	
Real and mental images -Reflected, transmitted and emitted light images-noise-Signal to NoiseRatio-Contrast Optimum contrast-Sharpness-Transfer functions-Resolution-line pairs and MTF. Image quality metrics for digital systems-Global parameter assessment, Spatial frequency assessment, Image processing assessment, Observer assessment.			
<b>UNIT - II</b>	<b>RADIOGRAPHIC IMAGE</b>	<b>9 Periods</b>	
Unsharpness-Geometric, photographic, motion-identifying the causes of Unsharpness-Over and under penetration-Radiographic contrast -fogging-Graininess-mottle-Image artefacts-Distortion- foreshortening-elongation- Double images Image subtraction techniques-Digital subtraction.			
<b>UNIT - III</b>	<b>TOMOGRAPHIC IMAGING</b>	<b>9 Periods</b>	
Over view of Computerized tomography as an image device-Scanner design-Reconstruction techniques-Reconstruction techniques-CT image quality-Other artefacts in CT-Multislice CT-CT Scanner Performance.			
<b>UNIT - IV</b>	<b>MAGNETIC RESONANCE IMAGING</b>	<b>9 Periods</b>	
Basic principles of Magnetic Resonance Imaging-Block diagram of MR Scanner components- Common artefacts-image reconstruction-imaging equations-image quality-Resolution-Noise-Signal to Noise Ratio-Artefacts-Functional MRI.			
<b>UNIT - V</b>	<b>3D ULTRASOUND IMAGING</b>	<b>9 Periods</b>	
Limitations of 3D Ultrasound imaging-3D Ultrasound scanning techniques-Reconstruction and 3D Ultrasound images-effects of errors in 3D image reconstruction-Viewing of 3D Ultrasound images- 3-D Ultrasound system performance.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>			

#### REFERENCES:

1	Richard L. Van Metter, Jacob Beutel, Harold L. Kundel, <b>"Handbook of Medical Imaging, Volume" 1. Physics and Psychophysics</b> , SPIE, 2000.
2	Chesney D. N., Chesney M. O. <b>"Radio graphic imaging"</b> , CBS Publications, New Delhi, 1989
3	Donald W. McRobbice, Elizabeth A. Moore, Martin J. Grave and Martin R. Prince <b>"MRI from Picture to proton"</b> , Cambridge University press, second edition, New York 2007.
4	Frederick W Kremkau, <b>"Diagnostic Ultrasound Principles &amp; Instruments"</b> , Saunders Elsevier, 2005.
5	Jerry L. Prince, Jnathan M. Links, <b>"Medical Imaging Signals and Systems"</b> - Pearson Education Inc. 2014.
6	Peggy, W., Roger D. Ferimarch, <b>"MRI for Technologists"</b> , McGraw Hill, New York, second edition, 2000.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will have the ability to :		
C01	Assess the characteristics and quality of the image.	K2
C02	Demonstrate Principles of Radiography.	K2
C03	Explain the image acquisition using CT.	K3
C04	Demonstrate the applications of magnetic field in the field of medicine.	K3
C05	Explain the principles of 3D Ultrasound imaging.	K3

<b>COURSE ARTICULATION MATRIX:</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
C01	3	1	2	2	1	-
C02	3	1	2	2	1	-
C03	3	1	2	2	1	-
C04	3	1	2	2	1	-
C05	3	1	2	2	1	-
<b>23AEPE15</b>	3	1	2	2	1	-
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	30%	50%	20%				100%
CAT2	30%	50%	20%				100%
Individual /Assignment 1/case study/seminar 1/Project 1	30%	50%	20%				100%
Individual /Assignment 2/case study/seminar 2/Project 2	30%	50%	20%				100%
ESE	30%	50%	20%				100%



<b>23AEPE16</b>	<b>LOW POWER IC DESIGN</b> (Common to Applied Electronics & VLSI Design)	<b>SEMESTER IV</b>
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	PE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To acquire knowledge in low power CMOS designs and optimization.</li></ul>		
<b>UNIT – I</b>	<b>INTRODUCTION TO LOW POWER DESIGN</b>	<b>9 Periods</b>	
Physics of Power Dissipation in CMOS FET Devices-Sources of power consumption- -Basic Principles of Low Power Design. Sources of Power dissipation in Ultra Deep Submicron CMOS Circuits – Static, Dynamic and Short circuit components Effects of scaling on power consumption- Low power design flow- Normalized Figure of Merit – PDP& EDP.			
<b>UNIT – II</b>	<b>POWER DISSIPATION IN CMOS</b>	<b>9 Periods</b>	
SPICE circuit simulation-Gate level Analysis, Architecture level Analysis, Data Correlation Analysis, Monte-Carlo Simulation, Probabilistic Power Analysis. Statistical Techniques - Estimation of Glitching Power - Sensitivity Analysis - Circuit Reliability - Power Estimation at the circuit level - High level Power Estimation - Information Theory based approaches - Estimation of maximum power.			
<b>UNIT – III</b>	<b>POWER OPTIMIZATION TECHNIQUES</b>	<b>9 Periods</b>	
Circuit Level – Transistor and Gate Sizing, Equivalent Pin Ordering, Network Restructuring and Reorganization, Special Latches and Flip Flops, Low Power Digital Cell Library, Adjustable Device Threshold Voltage. Leakage current in deep sub micrometer transistors.			
<b>UNIT – IV</b>	<b>SPECIAL TECHNIQUES</b>	<b>9 Periods</b>	
Gate Reorganization, Signal Gating, Logic Encoding, State Machine Encoding, Precomputational Logic. Architectural and System Level – Power and Performance Management, Switching Activity Reduction, Parallel Architecture with Voltage Reduction, Flow Graph Transformation. Advanced Techniques- Adiabatic Computation, Pass Transistor Logic Synthesis, Asynchronous Circuits, Low power bus – low swing bus, charge recycling bus, delay balancing.			
<b>UNIT – V</b>	<b>LOW POWER MEMORIES</b>	<b>9 Periods</b>	
Basics of ROM, Low power ROM Technology, Basics of SRAM-Memory Cell-Low Power SRAM Technology-Precharge and Equalization Circuit-Basics of DRAM-Low Power DRAM Technology. Conventional BiCMOS Logic- BiCMOS Logic Family-Low Voltage BiCMOS Logic family-Low Voltage BiCMOS Applications.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods    Tutorial: 0 Periods    Practical: 0 Periods    Total: 45 Periods</b>			

#### REFERENCES:

1	Kaushik Roy and Sharat C Prasad, " <b>Low Power CMOS VLSI circuit Design</b> ", John Wiley and Sons, 2010.
2	Soudris, Dimitrios, Christian Pignet, Goutis, Costas, " <b>Designing CMOS circuits for low power</b> ", Springer US, First Edition, 2011.
3	Gary B Yeap K, " <b>Practical Low Power Digital VLSI Design</b> ", Springer US, First Edition 2010.
4	AjitPal, " <b>Low Power VLSI circuits and Systems</b> ", Springer India, First Edition, 2014.
5	Jan M.Rabaey, Massoud Pedram, " <b>Low power Design methodologies</b> ", SpringerUS, First Edition, 2014.

<b>COURSE OUTCOMES:</b> Upon completion of the course, the students will be able to:		<b>Bloom's Taxonomy Mapped</b>
CO1	Understand low power design in CMOS	K2
CO2	Analyze various sources of power dissipation in CMOS circuits	K2
CO3	Reduce the power consumption by optimizing the circuit structures	K3
CO4	Design CMOS low power circuits using various special techniques.	K3
CO5	Understand low power memories	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
CO1	3	1	3	-	-	1
CO2	3	1	3	-	-	1
CO3	3	1	1	-	-	1
CO4	3	1	1	-	-	1
CO5	3	1	1	-	-	1
<b>23AEPE16</b>	3	1	3	-	-	1
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remember ing (K1) %</b>	<b>Understandin g (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creati ng (K6) %</b>	<b>Total %</b>
CAT1	40%	40%	20%				100%
CAT2	40%	40%	20%				100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1	40%	40%	20%				100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2	40%	40%	20%				100%
ESE	40%	40%	20%				100%

<b>23AEPE17</b>	<b>VLSI SIGNAL PROCESSING</b> (Common to Applied Electronics & VLSI Design)	<b>SEMESTER IV</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>PE</b>	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To increase the performance of the DSP systems in terms of power consumption, speed and area</li></ul>	
<b>UNIT – I</b>	<b>INTRODUCTION TO DSP SYSTEMS, PIPELINING AND PARALLEL PROCESSING OF FIR FILTERS</b>	<b>9 Periods</b>
Introduction to DSP systems - Typical DSP algorithms, Data flow and Dependence graphs - Critical path- Loop bound- Iteration bound, Longest path matrix algorithm, Pipelining and Parallel processing of FIR filters, Pipelining and Parallel processing for low power.		
<b>UNIT – II</b>	<b>RETIMING, ALGORITHMIC STRENGTH REDUCTION, RANK ORDER FILTERS</b>	<b>9 Periods</b>
Retiming – Definitions and properties, Unfolding – An algorithm for Unfolding- properties of unfolding- Sample period reduction and parallel processing application, Systolic Architecture Design-Algorithmic strength reduction in filters and transforms – 2-parallel FIR filter, 2-parallel fast FIR filter, DCT architecture, Rank-order filters, Odd- Even merge-sort architecture, Parallel rank-order filters		
<b>UNIT – III</b>	<b>FAST CONVOLUTION, PIPELINING AND PARALLEL PROCESSING OF IIR FILTERS.</b>	<b>9 Periods</b>
Fast convolution – Cook-Toom algorithm, modified Cook-Toom algorithm, Pipelined and parallel recursive filters, Look-Ahead pipelining in first-order IIR filters, Look-Ahead pipelining with power-of- 2 decomposition, Clustered look-ahead pipelining, Parallel processing of IIR filters, Combined pipelining and parallel processing of IIR filters- Low-power IIR Filter design using pipelining and parallel processing- Pipelined Adaptive digital filters.		
<b>UNIT – IV</b>	<b>BIT-LEVEL ARITHMETIC ARCHITECTURES</b>	<b>9 Periods</b>
Scaling and Round off Noise Computations -Bit-level arithmetic architectures – parallel multipliers with sign extension, parallel carry-ripple and carry-save multipliers, Design of Lyon’s bit-serial multipliers using Horner’s rule, bit-serial FIR filter, CSD representation, CSD multiplication using Horner’s rule for precision improvement, Distributed Arithmetic fundamentals and FIR filters.		
<b>UNIT – V</b>	<b>NUMERICAL STRENGTH REDUCTION, CLOCKING STYLES, SYNCHRONOUS, ASYNCHRONOUS AND WAVE PIPELINING</b>	<b>9 Periods</b>
Numerical strength reduction – subexpression elimination - multiple constant multiplication- iterative matching, synchronous pipelining and clocking styles - clock skew in edge-triggered single phase clocking, two-phase clocking, wave pipelining, Asynchronous pipelining, Programmable Digital signal processors.		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods      Tutorial: - 0 Periods      Practical: -0 Periods      Total: 45 Periods</b>		

#### REFERENCES:

1	Keshab K. Parhi, " <b>VLSI Digital Signal Processing Systems, Design and implementation</b> ", Wiley, Interscience, 2007.
2	U. Meyer – Baese, " <b>Digital Signal Processing with Field Programmable Gate Arrays</b> ", Springer, Second Edition, 2004.

3	Kung S. Y, H. J. While House, T. Kailath, <b>"VLSI and Modern Signal Processing"</b> , Prentice Hall, 1985.
4	Jose E. France, Yannis Tsividis <b>"Design of Analog – Digital VLSI Circuits for Telecommunications and Signal Processing"</b> , Prentice Hall, 1994.
5	Mediseti V. K, <b>"VLSI Digital Signal Processing"</b> , IEEE Press (NY), USA, 1995.

<b>COURSE OUTCOMES:</b> Upon completion of the course, the students will be able to:		<b>Bloom's Taxonomy Mapped</b>
CO1	Increase the performance of the FIR filter structures in terms of power consumption, speed and area.	K3
CO2	Reduce the complexity of DSP algorithms in VLSI hardware.	K3
CO3	Increase the performance of the IIR filter structures in terms of power consumption, speed and area.	K3
CO4	Improve the performance of bit level architectures in DSP systems.	K2
CO5	Understand clocking styles, wave pipelining and complexity reduction in computations.	K1

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
CO1	2	3	-	1	-	-
CO2	2	3	-	1	-	-
CO3	2	3	-	1	-	-
CO4	2	3	-	1	-	-
CO5	2	1	-	-	-	-
<b>23AEPE17</b>	2	3	-	1	-	-
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20%	40%	40%				100%
CAT2	20%	40%	40%				100%
Individual Assessment 1 / Case Study 1 / Seminar 1 / Project 1		50%	50%				100%
Individual Assessment 2 / Case Study 2 / Seminar 2 / Project 2		50%	50%				100%
ESE	20%	40%	40%				100%

<b>23AEPE18</b>	<b>ASIC DESIGN</b> (Common to Applied Electronics & VLSI Design)	<b>SEMESTER IV</b>
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	PE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To Design and acquire knowledge on principles of ASIC design flow, fundamentals of logic cells and concepts of various programming technology, high level ASIC design synthesis and ASIC Construction.</li></ul>	
<b>UNIT – I</b>	<b>FUNDAMENTALS OF ASICs, CMOS LOGIC AND ASIC LIBRARY DESIGN</b>	<b>9 Periods</b>
Types of ASICs - Design flow-CMOS Transistors CMOS Design Rules - Combinational Logic Cell - Sequential Logic cell - Data path Logic Cell -Transistors as Resistors -Transistor Parasitic Capacitance -Logical effort - Library Cell Design-Library Architecture.		
<b>UNIT – II</b>	<b>PROGRAMMABLE ASICs</b>	<b>9 Periods</b>
Anti fuse - Static RAM - EPROM and EEPROM technology - PREP benchmarks - Actel ACT - Xilinx LCA - Altera FLEX - Altera MAX DC and AC inputs and outputs - Clock and Power inputs - Xilinx I/O blocks.		
<b>UNIT – III</b>	<b>PROGRAMMABLE ASIC INTERCONNECT, DESIGN SOFTWARE AND LOW LEVEL DESIGN ENTRY</b>	<b>9 Periods</b>
Actel ACT - Xilinx LCA - Xilinx EPLD - Altera MAX 5000 and 7000 - Altera MAX 9000 - Altera FLEX - Design Systems - Logic Synthesis - Half gate ASIC - Schematic entry - Low level design language - PLA tools - EDIF-CFI design representation.		
<b>UNIT – IV</b>	<b>LOGIC SYNTHESIS - SIMULATION AND TESTING</b>	<b>9 Periods</b>
Verilog and Logic Synthesis -VHDL and Logic Synthesis - Types of Simulation - Boundary Scan Test - Fault simulation - Automatic Test Pattern Generation.		
<b>UNIT – V</b>	<b>ASIC CONSTRUCTION</b>	<b>9 Periods</b>
System partition - FPGA partitioning - Partitioning methods - Floor planning - placement - Physical Design Flow - Global Routing - Detailed Routing - Special Routing - Circuit extraction – DRC.		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods    Tutorial: 0 Periods    Practical: 0 Periods    Total: 45 Periods</b>		

#### REFERENCES:

1	Smith M.J.S., " <b>Application Specific Integrated Circuits</b> ", Addison, Wesley Longman Inc., 1997.
2	Farzad Nekoogar and Faranak Nekoogar, " <b>From ASICs to SoCs - A Practical Approach</b> ", Prentice Hall, 2003.
3	Wayne Wolf, " <b>FPGA-Based System Design</b> ", Prentice Hall, 2004.
4	Rajsuman R., " <b>System-on-a-Chip Design and Test</b> ", Santa Clara, CA, Artech House Publishers, 2000.
5	Nekoogar F., " <b>Timing Verification of Application-Specific Integrated Circuits</b> ", Prentice Hall, 1999

<b>COURSE OUTCOMES:</b> Upon completion of the course, the students will be able to:		<b>Bloom's Taxonomy Mapped</b>
C01	Design sequential and combinational logic cells and analyze Programmable ASICs	K2
C02	Explain the memory technologies and architecture of Programmable ASIC s.	K2
C03	Understand the ASIC interconnects and design entry.	K3
C04	Explain and execute the Logic synthesis of ASIC	K3
C05	Construct an ASIC using the described methods.	K3

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	3	-	1	-	2
C02	3	3	-	1	-	2
C03	3	3	-	2	-	2
C04	3	3	-	2	-	2
C05	3	3	-	1	-	2
<b>23AEPE18</b>	3	3	-	1	--	2
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remember ing (K1) %</b>	<b>Understandi ng (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	40%	40%	20%				100%
CAT2	40%	40%	20%				100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1		50%	50%				100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2		50%	50%				100%
ESE	40%	40%	20%				100%

23AEPE19	REAL TIME OPERATING SYSTEM	SEMESTER IV
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PREREQUISITES	CATEGORY	L	T	P	C
<ul style="list-style-type: none"> <li>Operating System.</li> </ul>	PE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>The students will be able to acquire knowledge about the basic concepts of Real time Operating System, various uniprocessor and multiprocessor scheduling mechanisms and Real time communication protocols and databases.</li></ul>		
<b>UNIT – I</b>	<b>INTRODUCTION TO REAL TIME OPERATING SYSTEM</b>	<b>9 Periods</b>	
Introduction to Real time computing Concepts, Example of real time applications-Structure of a real time system-Characterization of real time systems and tasks- Hard and Soft timing constraints -Design challenges- Performance metrics -Prediction of Execution time: Source code analysis, Micro - architecture level analysis, Cache and pipeline issues –Programming Languages for Real -Time System.			
<b>UNIT – II</b>	<b>REVIEW OF RTOS</b>	<b>9 Periods</b>	
Real time OS-Threads and Tasks-Structure of Microkernel-Time Services-Scheduling Mechanisms Communication and Synchronization-Event Notification and Software interrupt.			
<b>UNIT – III</b>	<b>TASK SCHEDULING AND ALGORITHMS</b>	<b>9 Periods</b>	
Task assignment and Scheduling -Task allocation algorithms- Single processor and Multiprocessor task scheduling- Clock driven and Priority based scheduling algorithms –Fault tolerant Scheduling.			
<b>UNIT – IV</b>	<b>REAL TIME PROTOCOLS</b>	<b>9 Periods</b>	
Real Time Communication Network-Topologies and architecture issues-protocols-contention based, token based, polled bus, deadline-based protocol, Fault tolerant routing.RTP and RTCP.			
<b>UNIT – V</b>	<b>REAL TIME DATABASES</b>	<b>9 Periods</b>	
Real time Databases-Transaction priorities-Concurrency control issues-Disk scheduling algorithms-Two phase approach to improve predictability			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods    Tutorial:0 Periods       Practical: 0Periods       Total: 45 Periods</b>			

#### REFERENCES:

1	Jane W.S. Liu, <i>“Real Time Operating Systems”, Pearson Education India, 2000.</i>
2	Philip A. Laplante and Seppo J. Ovaska, <i>“Real Time Operating Systems Design and Analysis: Tools for the Practitioner” IV Edition IEEE Press, Wiley. 2013.</i>
3	C.M. Krishna, Kang G. Shin – <i>“ Real Time Operating Systems”, International Edition, McGraw Hill Companies, Inc., New York, 2013</i>
4	Abraham Silberschatz, Peter B. Galvin, Greg Gagne, <i>“Operating System concepts”, 8th edition, Wiley, 2008.</i>

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Explain various real time operating system and its design challenges.	K2
C02	Reproduce and explain the operation of various threads and Tasks	K2
C03	Design and analyze various real time task scheduling algorithms.	K3
C04	Illustrate the different real time communication protocols.	K2
C05	Describe the concept of real time database.	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
C01	2	-	3	1	-	-
C02	2	-	3	1	-	-
C03	2	-	3	1	-	-
C04	2	-	3	1	-	-
C05	2	-	3	1	-	-
<b>23AEPE19</b>	2	-	3	1	-	-
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	40%	40%	20%				100%
CAT2	40%	40%	20%				100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1		50%	50%				100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2		50%	50%				100%
ESE	30%	40%	30%				100%



<b>23AEPE20</b>	<b>ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING</b>	<b>SEMESTER IV</b>
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>PE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>		
• Study heuristic search techniques, Machine Learning, supervised and deep learning algorithms		
<b>UNIT – I</b>	<b>PROBLEM SOLVING</b>	<b>9 Periods</b>
Introduction to AI - AI Applications - Problem solving agents – search algorithms – uninformed search strategies – Heuristic search strategies – Local search and optimization problems –adversarial search – constraint satisfaction problems (CSP).		
<b>UNIT – II</b>	<b>PROBABILISTIC REASONING</b>	<b>9 Periods</b>
Acting under uncertainty – Bayesian inference – naïve bayes models. Probabilistic reasoning – Bayesian networks – exact inference in BN – approximate inference in BN – causal networks.		
<b>UNIT – III</b>	<b>SUPERVISED LEARNING</b>	<b>9 Periods</b>
Introduction to machine learning – Linear Regression Models: Least squares, single & multiple variables, Bayesian linear regression, gradient descent, Linear Classification Models: Discriminant function – Probabilistic discriminative model - Logistic regression, Probabilistic generative model- Naive Bayes, Maximum margin classifier – Support vector machine, Decision Tree, Random forests.		
<b>UNIT – IV</b>	<b>ENSEMBLE TECHNIQUES AND UNSUPERVISED LEARNING</b>	<b>9 Periods</b>
Combining multiple learners: Model combination schemes, Voting, Ensemble Learning - bagging,boosting, stacking, Unsupervised learning: K-means, Instance Based Learning: KNN, Gaussian mixture models and Expectation maximization.		
<b>UNIT – V</b>	<b>NEURAL NETWORKS</b>	<b>9 Periods</b>
Perceptron - Multilayer perceptron, activation functions, network training – gradient descent optimization – stochastic gradient descent, error backpropagation, from shallow networks to deep networks –Unit saturation (aka the vanishing gradient problem) – ReLU, hyperparameter tuning, batch normalization, regularization, dropout.		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>		

#### REFERENCES:

1	Dan W. Patterson, <b>"Introduction to Artificial Intelligence and Expert Systems"</b> , Pearson Education,2007
2	Stuart Russell and Peter Norvig, <b>"Artificial Intelligence – A Modern Approach"</b> , Fourth Edition, Pearson Education, 2021.
3	Kevin Night, Elaine Rich, and Nair B., <b>"Artificial Intelligence"</b> , McGraw Hill, 2008
4	Patrick H. Winston, <b>"Artificial Intelligence"</b> , Third Edition, Pearson Education, 2006
5	Deepak Khemani, <b>"Artificial Intelligence"</b> , Tata McGraw Hill Education, 2013( <a href="http://nptel.ac.in/">http://nptel.ac.in/</a> )
6	Christopher M. Bishop, <b>"Pattern Recognition and Machine Learning"</b> , Springer, 2006.
7	Tom Mitchell, <b>"Machine Learning"</b> , McGraw Hill, 3rd Edition,1997.

8	Charu C. Aggarwal, " <b>Data Classification Algorithms and Applications</b> ", CRC Press, 2014
9	Mehryar Mohri, Afshin Rostamizadeh, Ameet Talwalkar, " <b>Foundations of Machine Learning</b> ", MIT Press, 2012.
10	Ian Goodfellow, Yoshua Bengio, Aaron Courville, " <b>Deep Learning</b> ", MIT Press, 2016

<b>COURSE OUTCOMES:</b> Upon completion of the course, students will be able to/have:		<b>Bloom's Taxonomy Mapped</b>
C01	Use appropriate search algorithms for problem solving	K2
C02	Apply reasoning under uncertainty	K2
C03	Build supervised learning models	K2
C04	Build ensembling and unsupervised models	K3
C05	Build deep learning neural network models	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	2	-	3	1	-	-
C02	2	-	3	1	-	-
C03	2	-	3	1	-	-
C04	2	-	3	1	-	-
C05	2	-	3	1	-	-
<b>23AEPE20</b>	<b>2</b>	<b>-</b>	<b>3</b>	<b>1</b>	<b>-</b>	<b>-</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remember ing (K1) %</b>	<b>Understandi ng (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	40%	40%	20%				100%
CAT2	40%	40%	20%				100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1		50%	50%				100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2		50%	50%				100%
ESE	30%	30%	40%				100%

<b>23SEOE01</b>	<b>BUILDING BYE-LAWS AND CODES OF PRACTICE</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
NIL	OE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To impart knowledge on the building bye –laws and to emphasize the significance of codes of practice in construction sector.</li></ul>	
<b>UNIT – I</b>	<b>INTRODUCTION TO BUILDING BYE-LAWS</b>	<b>9 Periods</b>
Introduction to Building Bye Laws and regulation, their need and relevance, General definitions such as building height, building line, FAR, Ground Coverage, set back line. Introduction to Master Plan and understanding various land uses like institutional, residential etc. - Terminologies of Building bye-laws.		
<b>UNIT – II</b>	<b>ROLE OF STATUTORY BODIES</b>	<b>9 Periods</b>
Role of various statutory bodies governing building works like development authorities, municipal corporations etc. Local Planning Authority, Town and Country planning organisation, Ministry of urban development.		
<b>UNIT – III</b>	<b>APPLICATION OF BUILDING BYE-LAWS</b>	<b>9 Periods</b>
Interpretation of information given in bye laws including ongoing changes as shown in various annexure and appendices. Application of Bye-laws like structural safety, fire safety, earthquake safety, basement, electricity, water, and communication lines in various building types.		
<b>UNIT – IV</b>	<b>INTRODUCTION TO CODES OF PRACTICE</b>	<b>9 Periods</b>
Introduction to various building codes in professional practice - Codes, regulations to protect public health, safety and welfare - Codes , regulations to ensure compliance with the local authority.		
<b>UNIT – V</b>	<b>APPLICATION OF CODES OF PRACTICE</b>	<b>9 Periods</b>
Applications of various codes as per various building types. Bureau of Indian Standards, Eurocode – Introduction to other international codes.		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods</b>	<b>Tutorial: 0 Periods</b>	<b>Practical: 0 Periods</b>
		<b>Total: 45 Periods</b>

#### REFERENCES :

1	<i>“National Building Code of India 2016 – SP 7”, NBC 2016, Bureau of Indian Standards.</i>
2	<i>“Model Building Bye-Laws (MBBL) – 2016”, Town and Country Planning Organization, Ministry of Housing and Urban Affairs, Government of India.</i>
3	<i>“Unified Building Bye-laws for Delhi 2016”, Nabhi Publications, 2017.</i>
4	<i>Mukesh Mittal, “Building Bye Laws”, Graphicart publishers, Jaipur, 2013.</i>

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Apply the building bye-laws in planning, design and construction works.	K3
C02	Familiarize with the role of various statutory bodies.	K2
C03	Execute safety related work practices in the construction sector.	K3
C04	Ensure compliance with the rules and regulations in design and construction practices.	K3
C05	Perform design and construction practices based on national and international codal provisions.	K3

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	1	3	1	1	2	3
C02	1	3	1	1	2	3
C03	1	3	1	1	2	3
C04	2	3	1	1	2	3
C05	2	3	1	1	2	3
<b>23SEOE01</b>	<b>2</b>	<b>3</b>	<b>1</b>	<b>1</b>	<b>2</b>	<b>3</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	40	40	20	-	-	-	100
CAT2	40	40	20	-	-	-	100
Individual Assessment 1 / Case Study 1/ Seminar 1 / Project1	40	40	20	-	-	-	100
Individual Assessment 2 / Case Study 2/ Seminar 2 / Project 2	40	40	20	-	-	-	100
ESE	40	40	20	-	-	-	100

<b>23SEOEO2</b>	<b>PLANNING OF SMART CITIES</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>OE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To have an exposure on planning of smart cities with consideration of the recent challenges and to address the importance of sustainable development of urban area.</li></ul>		
<b>UNIT – I</b>	<b>SMART CITIES DEVELOPMENT POTENTIALS AND CHALLENGES</b>	<b>9 Periods</b>	
Perspectives of Smart Cities: Introduction and Overview - Implementation Challenges - Methodological issues - Spatial distribution of startup cities – Re imagining postindustrial cities - Implementation Challenges for Establishing Smart Urban Information and Knowledge Management System.			
<b>UNIT – II</b>	<b>SUSTAINABLE URBAN PLANNING</b>	<b>9 Periods</b>	
Optimising Green Spaces for Sustainable Urban Planning - 3D City Models for Extracting Urban Environmental Quality Indicators - Assessing the Rainwater Harvesting Potential - The Strategic Role of Green Spaces - Monitoring Urban Expansion.			
<b>UNIT – III</b>	<b>ENERGY MANAGEMENT AND SUSTAINABLE DEVELOPMENT</b>	<b>9 Periods</b>	
Alternatives for Energy Stressed Cities - Social Acceptability of Energy - Efficient Lighting - Energy Management - Urban Dynamics and Resource Consumption - Issues and Challenges of Sustainable Tourism - Green Buildings: Eco-friendly Technique for Modern Cities.			
<b>UNIT – IV</b>	<b>MULTIFARIOUS MANAGEMENT FOR SMART CITIES</b>	<b>9 Periods</b>	
Assessment of Domestic Water Use Practices - Issue of Governance in Urban Water Supply - Assessment of Water Consumption at Urban Household Level - Water Sustainability - Socio-economic Determinants and Reproductive Healthcare System - Problems and Development of Slums.			
<b>UNIT – V</b>	<b>INTELLIGENT TRANSPORT SYSTEM</b>	<b>9 Periods</b>	
Introduction to Intelligent Transport Systems (ITS) - The Range of ITS Applications -Network Optimization - Sensing Traffic using Virtual Detectors - Vehicle Routing and Personal route information - The Smart Car - Commercial Routing and Delivery - Electronic Toll Collection - The Smart Card - Dynamic Assignment - Traffic Enforcement. Urban Mobility and Economic Development.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods</b>		<b>Tutorial: 0 Periods</b>	<b>Practical: 0 Periods</b>
<b>Total: 45 Periods</b>			

#### REFERENCES

1	Poonam Sharma, Swati Rajput, <b>"Sustainable Smart Cities In India Challenges And Future Perspectives"</b> , Springer 2017 Co.(P) Ltd. 2013.
2	Ivan Nunes Da Silva, <b>"Rogerio Andrade Flauzino-Smart Cities Technologies-Exli4eva"</b> , 2016.
3	Stan McClellan, Jesus A. Jimenez, George Koutitas <b>"Smart Cities_ Applications, Technologies, Standards"</b> , and Driving Factors-Springer International Publishing, 2018.
4	Stan Geertman, Joseph Ferreira, Jr., Robert Goodspeed, John Stillwell, <b>"Planning Support Systems And Smart Cities"</b> , Springer, 2015.
5	Pradip Kumar Sarkar and Amit Kumar Jain <b>"Intelligent Transport Systems"</b> , PHI Learning, 2018.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Indicate the potential challenges in smart city development.	K2
C02	Select the different tools for sustainable urban planning.	K3
C03	Choose appropriate energy conservation system for smart cities.	K3
C04	Identify the proper method of water management system.	K3
C05	Apply Intelligent Transport System concepts in planning of smart city.	K3

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
C01	1	-	2	3	1	1
C02	1	1	1	3	2	1
C03	1	1		2	2	1
C04	1	-	1	2	1	1
C05	1	-	1	3	1	-
<b>23SEOE02</b>	<b>1</b>	<b>1</b>	<b>2</b>	<b>3</b>	<b>2</b>	<b>1</b>
1 - Slight, 2 - Moderate, 3 - Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	25	45	30	-	-	-	100
CAT2	25	45	30	-	-	-	100
Individual Assessment 1 / Case Study 1 / Seminar 1 / Project1	15	40	45	-	-	-	100
Individual Assessment 2 / Case Study 2 / Seminar 2 / Project 2	10	45	45	-	-	-	100
ESE	20	40	40	-	-	-	100

<b>23SEOE03</b>	<b>GREEN BUILDING</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
NIL	<b>OE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	• To introduce the different concepts of energy efficient buildings, indoor environmental quality management, green buildings and its design.		
<b>UNIT – I</b>	<b>INTRODUCTION</b>	<b>9 Periods</b>	
Life cycle impacts of materials and products – sustainable design concepts – strategies of design for the Environment -The sun-earth relationship and the energy balance on the earth’s surface, climate, wind – Solar radiation and solar temperature – Sun shading and solar radiation on surfaces – Energy impact on the shape and orientation of buildings – Thermal properties of building materials.			
<b>UNIT – II</b>	<b>ENERGY EFFICIENT BUILDINGS</b>	<b>9 Periods</b>	
Passive cooling and day lighting – Active solar and photovoltaic- Building energy analysis methods- Building energy simulation- Building energy efficiency standards-Lighting system design- Lighting economics and aesthetics- Impacts of lighting efficiency – Energy audit and energy targeting- Technological options for energy management.			
<b>UNIT – III</b>	<b>INDOOR ENVIRONMENTAL QUALITY MANAGEMENT</b>	<b>9 Periods</b>	
Psychrometry- Comfort conditions- Thermal comfort- Ventilation and air quality-Air conditioning requirement- Visual perception- Illumination requirement- Auditory requirement- Energy management options- Air conditioning systems- Energy conservation in pumps- Fans and blowers- Refrigerating machines- Heat rejection equipment- Energy efficient motors- Insulation.			
<b>UNIT – IV</b>	<b>GREEN BUILDING CONCEPTS</b>	<b>9 Periods</b>	
Green building concept- Green building rating tools- Leeds and IGBC codes. – Material selection Embodied energy- Operating energy- Façade systems- Ventilation systems-Transportation- Water treatment systems- Water efficiency- Building economics			
<b>UNIT – V</b>	<b>GREEN BUILDING DESIGN - CASE STUDY</b>	<b>9 Periods</b>	
Case studies - Building form, orientation and site considerations; conservation measures; energy modeling; heating system and fuel choices; renewable energy systems; material choices - construction budget			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods    Tutorial: 0 Periods    Practical: 0 Periods    Total: 45 Periods</b>			

#### REFERENCES :

1	Sam Kubba <i>"Handbook of Green Building Design and Construction: LEED, BREEAM, and Green Globes"</i> , Elsevier Science, 2012.
2	Yudelton, Jerry, McGraw-Hill, <i>"Greening existing buildings"</i> , New York, 2010
3	Charles J. Kibert, John Wiley & Sons, <i>"Sustainable Construction: Green Building Design and Delivery"</i> , 3rd Edition, 2012
4	R.S. Means, John Wiley & Sons, <i>"Green Building: Project Planning &amp; Cost Estimating"</i> , 2010.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Apply the concepts of sustainable design in building construction.	K3
C02	Execute green building techniques including energy efficiency management in the building design.	K3
C03	Establish indoor environmental quality in green building.	K3
C04	Perform the green building rating using various tools.	K3
C05	Create drawings and models of green buildings.	K3

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	3	2	3	3	3
C02	3	3	2	3	3	3
C03	2	2	2	2	3	3
C04	2	3	1	3	3	3
C05	3	3	1	3	3	3
<b>23SEOE03</b>	3	3	2	3	3	3
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	40	40	20	-	-	-	100
CAT2	40	40	20	-	-	-	100
Individual Assessment 1 / Case Study 1/ Seminar 1 / Project1	40	40	20	-	-	-	100
Individual Assessment 2 / Case Study 2/ Seminar 2 / Project 2	40	40	20	-	-	-	100
ESE	40	40	20	-	-	-	100



<b>23EEOE04</b>	<b>ENVIRONMENT HEALTH AND SAFETY MANAGEMENT</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>OE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To impart knowledge on occupational health hazards, safety measures at work place, accident prevention, safety management and safety measures in industries.</li></ul>		
<b>UNIT – I</b>	<b>OCCUPATIONAL HEALTH HAZARDS</b>	<b>9 Periods</b>	
Occupation, Health and Hazards - Safety Health and Management: Occupational Health Hazards - Ergonomics - Importance of Industrial Safety - Radiation and Industrial Hazards: Types and effects - Vibration - Industrial Hygiene - Different air pollutants in industries and their effects - Electrical, fire and Other Hazards.			
<b>UNIT – II</b>	<b>SAFETY AT WORKPLACE</b>	<b>9 Periods</b>	
Safety at Workplace - Safe use of Machines and Tools: Safety in use of different types of unit operations - Ergonomics of Machine guarding - working in different workplaces - Operation, Inspection and maintenance - Housekeeping, Industrial lighting, Vibration and Noise.			
<b>UNIT – III</b>	<b>ACCIDENT PREVENTION</b>	<b>9 Periods</b>	
Accident Prevention Techniques - Principles of accident prevention - Hazard identification and analysis, Event tree analysis, Hazop studies, Job safety analysis - Theories and Principles of Accident causation - First Aid: Body structure and functions - Fracture and Dislocation, Injuries to various body parts.			
<b>UNIT – IV</b>	<b>SAFETY MANAGEMENT</b>	<b>9 Periods</b>	
Safety Management System and Law - Legislative measures in Industrial Safety - Occupational safety, Health and Environment Management, Bureau of Indian Standards on Health and Safety, IS 14489 standards - OSHA, Process safety management (PSM) and its principles - EPA standards			
<b>UNIT – V</b>	<b>GENERAL SAFETY MEASURES</b>	<b>9 Periods</b>	
Plant Layout for Safety - design and location, distance between hazardous units, lighting, colour coding, pilot plant studies, Housekeeping - Accidents Related with Maintenance of Machines - Work Permit System - Significance of Documentation - Case studies involving implementation of health and safety measures in Industries.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods</b>		<b>Tutorial: 0 Periods</b>	<b>Practical: 0 Periods</b>
		<b>Total: 45 Periods</b>	

#### REFERENCES:

1	<b><i>“Physical Hazards of the Workplace”</i>, Barry Spurlock, CRC Press, 2017.</b>
2	<b><i>“Handbook of Occupational Safety and Health”</i>, S. Z. Mansdorf, Wiley Publications, 2019</b>
3	<b><i>“Safety, Health, and Environment”</i>, NAPTA, 2nd Edition, Pearson Publications, 2019.</b>
4	<b><i>“Occupational Health and Hygiene in Industries”</i>, Raja Sekhar Mamillapalli, Visweswara Rao PharmaMed Press, 1st edition, 2021.</b>

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
<b>C01</b>	Identify the occupational health hazards.	K3
<b>C02</b>	Execute various safety measures at workplace.	K3
<b>C03</b>	Analyze and execute accident prevention techniques.	K3
<b>C04</b>	Implement safety management as per various standards.	K3
<b>C05</b>	Develop awareness on safety measures in Industries.	K3

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
CO1	1	2	2	2	3	2
CO2	2	2	2	1	2	2
CO3	2	3	2	1	2	2
CO4	1	1	1	2	2	2
CO5	1	1	1	1	1	2
<b>23EEOE04</b>	<b>1</b>	<b>2</b>	<b>2</b>	<b>1</b>	<b>2</b>	<b>2</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	25	35	20	10	5	5	100
CAT2	25	35	20	10	5	5	100
Individual Assessment 1/ Case Study 1/ Seminar 1 / Project 1	20	40	30	10	-	-	100
Individual Assessment 2/ Case Study 2/ Seminar 2/ Project 2	20	40	30	10	-	-	100
ESE	25	35	20	10	5	5	100

<b>23EEOE05</b>	<b>CLIMATE CHANGE AND ADAPTATION</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	OE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To understand the Earth's climate system, changes and their effects on the earth, identifying the impacts, adaptation, mitigation of climate change and for gaining knowledge on clean technology, carbon trading and alternate energy sources.</li></ul>		
<b>UNIT – I</b>	<b>EARTH'S CLIMATE SYSTEM</b>	<b>9 Periods</b>	
Introduction-Climate in the spotlight - The Earth's Climate Machine – Climate Classification- Global Wind Systems – Trade Winds and the Hadley Cell – The Westerlies – Cloud Formation and Monsoon Rains – Storms and Hurricanes - The Hydrological Cycle – Global Ocean Circulation – El Nino and its Effect - Solar Radiation – The Earth's Natural Green House Effect – Green House Gases and Global Warming – Carbon Cycle.			
<b>UNIT – II</b>	<b>OBSERVED CHANGES AND ITS CAUSES</b>	<b>9 Periods</b>	
Observation of Climate Change – Changes in patterns of temperature, precipitation and sea level rise – Observed effects of Climate Changes – Patterns of Large-Scale Variability –Drivers of Climate Change – Climate Sensitivity and Feedbacks – The Montreal Protocol –UNFCCC – IPCC – Evidences of Changes in Climate and Environment – on a Global Scale and in India – climate change modeling.			
<b>UNIT – III</b>	<b>IMPACTS OF CLIMATE CHANGE</b>	<b>9 Periods</b>	
Impacts of Climate Change on various sectors – Agriculture, Forestry and Ecosystem – Water Resources – Human Health – Industry, Settlement and Society – Methods and Scenarios –Projected Impacts for Different Regions – Uncertainties in the Projected Impacts of Climate Change – Risk of Irreversible Changes.			
<b>UNIT – IV</b>	<b>CLIMATE CHANGE ADAPTATION AND MITIGATION MEASURES</b>	<b>9 Periods</b>	
Adaptation Strategy/Options in various sectors – Water – Agriculture -- Infrastructure and Settlement including coastal zones – Human Health – Tourism – Transport – Energy – Key Mitigation Technologies and Practices – Energy Supply – Transport – Buildings – Industry –Agriculture – Forestry - Carbon sequestration – Carbon capture and storage (CCS) – Waste (MSW & Bio waste, Biomedical, Industrial waste – International and Regional cooperation.			
<b>UNIT – V</b>	<b>CLEAN TECHNOLOGY AND ENERGY</b>	<b>9 Periods</b>	
Clean Development Mechanism – Carbon Trading - examples of future Clean Technology –Biodiesel – Natural Compost – Eco- Friendly Plastic – Alternate Energy – Hydrogen – Biofuels– Solar Energy – Wind – Hydroelectric Power – Mitigation Efforts in India and Adaptation funding.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods</b>		<b>Tutorial: 0Periods</b>	<b>Practical: 0 Periods</b>
		<b>Total:45 Periods</b>	

#### REFERENCES

1	<i>"Impacts of Climate Change and Climate Variability on Hydrological Regimes", Jan C. Van Dam, Cambridge University Press, 2003.</i>
2	<i>IPCC fourth assessment report - The AR4 synthesis report, 2007</i>
3	<i>IPCC fourth assessment report –Working Group I Report, "The physical sciencebasis",2007</i>
4	<i>IPCC fourth assessment report - Working Group II Report, "Impacts, Adaptation and Vulnerability", 2007</i>
5	<i>IPCC fourth assessment report – Working Group III Report" Mitigation of Climate Change", 2007</i>
6	<i>"Climate Change and Water". Technical Paper of the Intergovernmental Panel on Climate</i>

*Change, Bates, B.C., Z.W. Kundzewicz, S. Wu and J.P. Palutikof, Eds., IPCC Secretariat, Geneva, 2008.*

# **COURSE OUTCOMES:**

Upon completion of the course, the students will be able to:

## **Bloom's Taxonomy Mapped**

C01	Classify the Earth's climatic system and factors causing climate change and global warming.	K2
C02	Relate the Changes in patterns of temperature, precipitation and sea level rise and Observed effects of Climate Changes	K2
C03	Illustrate the uncertainty and impact of climate change and risk of reversible changes.	K3
C04	Articulate the strategies for adaptation and mitigation of climatic changes.	K3
C05	Discover clean technologies and alternate energy source for sustainable growth.	K3

# **COURSE ARTICULATION MATRIX**

COs/POs	P01	P02	P03	P04	P05	P06
C01	2	2	3	2	3	1
C02	3	2	2	2	3	2
C03	2	2	2	2	3	2
C04	3	2	2	2	2	2
C05	3	3	2	3	3	3
<b>23EEOE05</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

# **ASSESSMENT PATTERN – THEORY**

Test / Bloom's Category*	Remembering (K1) %	Understanding (K2) %	Applying (K3) %	Analyzing (K4) %	Evaluating (K5) %	Creating (K6) %	Total %
CAT1	25	30	35	10	-	-	100
CAT2	25	30	35	10	-	-	100
Individual Assessment 1/ Case Study 1/ Seminar 1 / Project 1	20	30	40	10	-	-	100
Individual Assessment 2/ Case Study 2/ Seminar 2/ Project 2	20	30	40	10	-	-	100
ESE	25	30	35	10	-	-	100

<b>23EEOE06</b>	<b>WASTE TO ENERGY</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	OE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To classify waste as fuel, introduce conversion devices, gain knowledge about Biomass Pyrolysis, demonstrate methods, factors for biomass gasification, and acquire knowledge about biogas and its development in India.</li></ul>		
<b>UNIT – I</b>	<b>INTRODUCTION</b>	<b>9 Periods</b>	
Introduction to Energy from Waste: Classification of waste as fuel – Agro based, Forest residue, Industrial waste - MSW – Conversion devices – Incinerators, Gasifiers, Digestors.			
<b>UNIT – II</b>	<b>BIOMASS PYROLYSIS</b>	<b>9 Periods</b>	
Biomass Pyrolysis: Pyrolysis -Types, Slow Pyrolysis, Fast Pyrolysis – Manufacture of charcoal – Methods – Yields and Applications – Manufacture of Pyrolytic oils and gases, Yields and Applications.			
<b>UNIT – III</b>	<b>BIOMASS GASIFICATION</b>	<b>9 Periods</b>	
Gasifiers – Fixed bed system – Downdraft and updraft gasifiers – Fluidized bed gasifiers – Design, Construction and Operation – Gasifier burner arrangement for thermal heating – Gasifier Engine arrangement and electrical power – Equilibrium and Kinetic Considerations in gasifier operation.			
<b>UNIT – IV</b>	<b>BIOMASS COMBUSTION</b>	<b>9 Periods</b>	
Biomass Combustion – Biomass Stoves – Improved Chullahs, types, some exotic designs, Fixed bed combustors, types – Inclined grate combustors – Fluidized bed combustors, design, construction and operation of all the above biomass combustors.			
<b>UNIT – V</b>	<b>BIOENERGY SYSTEM</b>	<b>9 Periods</b>	
Biogas: Properties of biogas (Calorific value and composition) – Biogas plant technology and status – Bio energy system – Design and constructional features – Biomass resources and their classification - Biomass conversion processes – Thermo chemical conversion – Direct combustion – biomass gasification – pyrolysis and liquefaction – biochemical conversion – anaerobic digestion – Types of biogas plants – Applications – Alcohol production from biomass – Bio diesel production – Urban waste to energy conversion – Biomass energy programme in India.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods</b>		<b>Tutorial: 0 Periods</b>	<b>Practical: 0 Periods</b>
<b>Total: 45 Periods</b>			

#### REFERENCES:

1	<b><i>“Energy Recovery from Municipal Solid Waste by Thermal Conversion Technologies”, P Jayaram Reddy, Taylor and Francis Publications, 2016.</i></b>
2	<b><i>“Waste – to – Energy: Technologies and project Implementations”, Marc J Rogoff, Francois Screve, ELSEVIER Publications, Third Edition, 2019.</i></b>
3	<b><i>“Biogas Technology and Principles”, Brad Hill, NY RESEARCH PRESS Publications, Illustrated Edition, 2015.</i></b>
4	<b><i>“Biomass Gasification and Pyrolysis Practical Design and Theory”, Prabir ELSEVIER Publications, 2010.</i></b>

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
CO1	Investigate solid waste management techniques.	K2
CO2	Get knowledge about biomass pyrolysis.	K3
CO3	Demonstrate methods and factors considered for biomass gasification.	K3
CO4	Identify the features of different facilities available for biomass combustion.	K4
CO5	Analyze the potential of different Bioenergy systems with respect to Indian condition.	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
CO1	2	3	3	2	3	1
CO2	3	2	2	2	3	1
CO3	3	3	2	3	2	1
CO4	3	2	2	3	3	1
CO5	2	3	3	3	2	1
<b>23EEOE06</b>	3	3	3	3	3	1
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	10	20	20	25	15	10	100
CAT2	10	25	20	10	25	10	100
Individual Assessment 1/ Case Study 1/ Seminar 1 / Project 1	-	15	35	50	-	-	100
Individual Assessment 2/ Case Study 2/ Seminar 2/ Project 2	-	10	40	50	-	-	100
ESE	10	25	25	20	10	10	100

<b>23GEOE07</b>	<b>ENERGY IN BUILT ENVIRONMENT</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	OE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To understand constructional energy requirements of buildings, energy audit methods and conservation of energy.</li></ul>		
<b>UNIT-I</b>	<b>INTRODUCTION</b>	<b>9 Periods</b>	
Indoor activities and environmental control - Internal and external factors on energy use – Characteristics of energy use and its management -Macro aspect of energy use in dwellings and its implications –Thermal comfort-Ventilation and air quality-Air-conditioning requirement-Visual perception-Illumination requirement-Auditory requirement.			
<b>UNIT-II</b>	<b>LIGHTING REQUIREMENTS IN BUILDING</b>	<b>9 Periods</b>	
The sun-earth relationship - Climate, wind, solar radiation and temperature - Sun shading and solar radiation on surfaces-Energy impact on the shape and orientation of buildings–Lighting and day lighting :Characteristics and estimation, methods of day-lighting–Architectural considerations for day-lighting.			
<b>UNIT-III</b>	<b>ENERGY REQUIREMENTS IN BUILDING</b>	<b>9 Periods</b>	
Steady and unsteady heat transfer through wall and glazed window-Standards for thermal performance of building envelope- Evaluation of the overall thermal transfer- Thermal gain and net heat gain-End-Use energy requirements-Status of energy use in buildings-Estimation of energy use in a building.			
<b>UNIT-IV</b>	<b>ENERGY AUDIT</b>	<b>9 Periods</b>	
Energy audit and energy targeting-Technological options for energy management-Natural and forced ventilation–Indoor environment and air quality-Air flow and air pressure on buildings-Flow due to Stack effect.			
<b>UNIT-V</b>	<b>COOLING IN BUILT ENVIRONMENT</b>	<b>9 Periods</b>	
Passive building architecture–Radiative cooling-Solar cooling techniques-Solar desiccant dehumidification for ventilation-Natural and active cooling with adaptive comfort–Evaporative cooling –Zero energy building concept.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods    Tutorial: 0 Period        Practical: 0 Period        Total: 45 Periods</b>			

## REFERENCES

1	J.Krieder and A.Rabl, " <b>Heating and Cooling of Buildings: Design for Efficiency</b> ", McGraw-Hill, 2000.
2	S.M.Guinness and Reynolds, " <b>Mechanical and Electrical Equipment for Buildings</b> ", Wiley, 1989.
3	A.Shaw, " <b>Energy Design for Architects</b> ", AEE Energy Books, 1991.
4	ASHRAE, " <b>Hand book of Fundamentals</b> ", ASHRAE, Atlanta, GA., 2001.
5	Reference Manuals of DOE-2 (1990), Orlando Lawrence-Berkeley Laboratory, University of California, and Blast, University of Illinois, USA.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
CO1	Understand energy and its usage	K2
CO2	Know lighting to be given to a building	K1
CO3	Analyse the energy requirements in a building	K3
CO4	Apply the energy audit concepts.	K3
CO5	Study architectural specifications of a building	K1

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
C01	2	-	3	1	2	1
C02	2	-	3	1	2	1
C03	2	-	3	1	2	1
C04	2	-	3	1	2	1
C05	2	-	3	1	2	1
<b>23GEOE07</b>	<b>2</b>	<b>-</b>	<b>3</b>	<b>1</b>	<b>2</b>	<b>1</b>
1-Slight, 2-Moderate, 3-Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT 1	40	40	20	-	-	-	100
CAT 2	40	40	20	-	-	-	100
Individual Assessment 1 / Case Study 1 / Seminar 1 / Project1	50	50	-	-	-	-	100
Individual Assessment 2 / Case Study 2 / Seminar 2 / Project 2	50	50	-	-	-	-	100
ESE	40	40	20	-	-	-	100



<b>23GEOE08</b>	<b>EARTH AND ITS ENVIRONMENT</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>OE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To know about the planet earth, the geosystems and the resources like ground water and air and to learn about the Environmental Assessment and sustainability.</li></ul>		
<b>UNIT-I</b>	<b>EVOLUTION OF EARTH</b>	<b>9 Periods</b>	
Evolution of earth as habitable planet-Evolution of continents-oceans and landforms-evolution of life through geological times - Exploring the earth's interior - thermal and chemical structure - origin of gravitational and magnetic fields.			
<b>UNIT-II</b>	<b>GEOSYSTEMS</b>	<b>9 Periods</b>	
Plate tectonics - working and shaping the earth - Internal geosystems – earthquakes – volcanoes - climatic excursions through time - Basic Geological processes - igneous, sedimentation – metamorphic processes.			
<b>UNIT-III</b>	<b>GROUND WATER GEOLOGY</b>	<b>9 Periods</b>	
Geology of ground water occurrence –recharge process-Ground water movement-Ground water discharge and catchment hydrology – Ground water as a resource - Natural ground water quality and contamination-Modelling and managing ground water systems.			
<b>UNIT-IV</b>	<b>ENVIRONMENTAL ASSESMENT AND SUSTAINABILITY</b>	<b>9 Periods</b>	
Engineering and sustainable development - population and urbanization - toxic chemicals and finite resources - water scarcity and conflict - Environmental risk - risk assessment and characterization – hazard assessment-exposure assessment.			
<b>UNIT-V</b>	<b>AIR AND SOLIDWASTE</b>	<b>9 Periods</b>	
Air resources engineering-introduction to atmospheric composition–behaviour-atmospheric photo chemistry-Solid waste management–characterization-management concepts.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods</b>		<b>Tutorial: 0 Period</b>	<b>Practical: 0 Period</b>
		<b>Total: 45 Periods</b>	

## REFERENCES

1	John Grotzinger and Thomas H.Jordan, " <b>Understanding Earth</b> ", Sixth Edition, W.H.Freeman, 2010.
2	Younger,P.L., " <b>Ground water in the Environment: An introduction</b> ", Blackwell Publishing,2007.
3	Mihelcic, J. R., Zimmerman, J. B., " <b>Environmental Engineering:Fundamentals, Sustainability and Design</b> ",Wiley,NJ, 2010.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
CO1	To know about evolution of earth and the structure of the earth.	K2
CO2	To understand the internal geosystems like earthquakes and volcanoes and the Various geological processes.	K2
CO3	To able to find the geological process of occurrence and movement of Ground water and the modeling systems.	K3
CO4	To assess the Environmental risks and the sustainability developments.	K3
CO5	To learn about the photochemistry of atmosphere and the solid waste Management concepts.	K1

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
CO1	1	-	-	2	2	-
CO2	3	-	3	3	-	3
CO3	2	-	-	-	-	-
CO4	-	2	-	-	1	-
CO5	2	2	-	1	-	-
<b>23GEOE08</b>	2	2	3	3	2	3
1-Slight, 2-Moderate, 3-Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT 1	40	40	20	-	-	-	100
CAT 2	40	40	20	-	-	-	100
Individual Assessment 1 / Case Study 1/ Seminar 1 / Project1	-	50	50	-	-	-	100
Individual Assessment 2 / Case Study 2/ Seminar 2 / Project 2	-	50	50	-	-	-	100
<b>ESE</b>	<b>40</b>	<b>40</b>	<b>20</b>	<b>-</b>	<b>-</b>	<b>-</b>	<b>100</b>

<b>23GEOE09</b>	<b>NATURAL HAZARDS AND MITIGATION</b> (Common to all Branches)
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<b>PREREQUISITES:</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>OE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To get idea on the causes, effects and mitigation measures of different types of hazards with case studies.</li></ul>	
<b>UNIT-I</b>	<b>EARTH QUAKES</b>	<b>9 Periods</b>
Definitions and basic concepts-different kinds of hazards-causes-Geologic Hazards-Earthquakes-causes of earthquakes-effects-plate tectonics-seismic waves-measures of size of earthquakes-earthquake resistant design concepts.		
<b>UNIT-II</b>	<b>SLOPE STABILITY</b>	<b>9 Periods</b>
Slope stability and landslides-causes of landslides-principles of stability analysis-remedial and corrective measures for slope stabilization.		
<b>UNIT-III</b>	<b>FLOODS</b>	<b>9 Periods</b>
Climatic Hazards-Floods-causes of flooding-regional flood frequency analysis-flood control measures-flood routing-flood forecasting-warning systems.		
<b>UNIT-IV</b>	<b>DROUGHTS</b>	<b>9 Periods</b>
Droughts -causes - types of droughts -effects of drought -hazard assessment - decision making-Use of GIS in natural hazard assessment-mitigation-management.		
<b>UNIT-V</b>	<b>TSUNAMI</b>	<b>9 Periods</b>
Tsunami-causes-effects-under sea earthquakes-landslides-volcanic eruptions-impact of sea meteorite-remedial measures-precautions-case studies.		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods</b>	<b>Tutorial: 0 Period</b>	<b>Practical: 0 Period</b>
		<b>Total: 45 Periods</b>

	<b>REFERENCES</b>
1	Donald Hyndman and David Hyndman, " <b>Natural Hazards and Disasters</b> ", Brooks/Cole Cengage Learning, 2008.
2	Edward Bryant, " <b>Natural Hazards</b> ", Cambridge University Press,2005.
3	J Michael Duncan and Stephan G Wright, " <b>Soil Strength and Slope Stability</b> ", John Wiley & Sons, Inc,2005.
4	AmrS.Elnashai and Luigi Di Sarno," <b>Fundamentals of Earthquake Engineering</b> ", John Wiley & Sons,Inc,2008

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Learn the basic concepts of earthquakes and the design concepts of earthquake Resistant buildings.	K2
C02	Acquire knowledge on the causes and remedial measures of slope stabilization.	K3
C03	As certain the causes and control measures of flood.	K3
C04	Know the types, causes and mitigation of droughts.	K2
C05	Study the causes, effects and precautionary measures of Tsunami.	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	1	-	3	2	3
C02	3	1	2	3	3	3
C03	3	2	3	-	-	3
C04	3	-	-	3	2	3
C05	3	-	2	2	-	3
<b>23GEOE09</b>	<b>3</b>	<b>1</b>	<b>2</b>	<b>3</b>	<b>2</b>	<b>3</b>
1–Slight, 2–Moderate, 3–Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT 1	40	40	20	-	-	-	100
CAT 2	40	40	20	-	-	-	100
Individual Assessment 1 / Case Study 1/ Seminar 1 / Project1	-	50	50	-	-	-	100
Individual Assessment 2 / Case Study 2/ Seminar 2 / Project 2	-	50	50	-	-	-	100
<b>ESE</b>	<b>40</b>	<b>40</b>	<b>20</b>	<b>-</b>	<b>-</b>	<b>-</b>	<b>100</b>

<b>23EDOE10</b>	<b>BUSINESS ANALYTICS</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>OE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objectives</b>	<ul style="list-style-type: none"><li>• To apprehend the fundamentals of business analytics and its life cycle.</li><li>• To gain knowledge about fundamental business analytics.</li><li>• To study modeling for uncertainty and statistical inference.</li><li>• To apprehend analytics the usage of Hadoop and Map Reduce frameworks.</li><li>• To acquire insight on other analytical frameworks.</li></ul>		
<b>UNIT – I</b>	<b>BUSINESS ANALYTICS AND PROCESS</b>	<b>9 Periods</b>	
Business analytics: Overview of Business analytics, Scope of Business analytics, Business Analytics Process, Relationship of Business Analytics Process and organization, competitive advantages of Business Analytics. Statistical Tools: Statistical Notation, Descriptive Statistical methods, Review of probability distribution and data modelling, sampling and estimation methods overview.			
<b>UNIT – II</b>	<b>REGRESSION ANALYSIS</b>	<b>9 Periods</b>	
Trendiness and Regression Analysis: Modelling Relationships and Trends in Data, simple Linear Regression. Important Resources, Business Analytics Personnel, Data and models for Business analytics, problem solving, Visualizing and Exploring Data, Business Analytics Technology.			
<b>UNIT – III</b>	<b>STRUCTURE OF BUSINESS ANALYTICS</b>	<b>9 Periods</b>	
Organization Structures of Business analytics, Team management, Management Issues, Designing Information Policy, Outsourcing, Ensuring Data Quality, Measuring contribution of Business analytics, Managing Changes. Descriptive Analytics, predictive analytics, predicative Modelling, Predictive analytics analysis, Data Mining, Data Mining Methodologies, Prescriptive analytics and its step in the business analytics Process, Prescriptive Modelling, nonlinear Optimization.			
<b>UNIT – IV</b>	<b>FORECASTING TECHNIQUES</b>	<b>9 Periods</b>	
Forecasting Techniques: Qualitative and Judgmental Forecasting, Statistical Forecasting Models, Forecasting Models for Stationary Time Series, Forecasting Models for Time Series with a Linear Trend, Forecasting Time Series with Seasonality, Regression Forecasting with Casual Variables, Selecting Appropriate Forecasting Models. Monte Carlo Simulation and Risk Analysis: Monte Carle Simulation Using Analytic Solver Platform, New-Product Development Model, Newsvendor Model, Overbooking Model, Cash Budget Model.			
<b>UNIT – V</b>	<b>DECISION ANALYSIS AND RECENT TRENDS IN BUSINESS ANALYTICS</b>	<b>9 Periods</b>	
Decision Analysis: Formulating Decision Problems, Decision Strategies with the without Outcome Probabilities, Decision Trees, The Value of Information, Utility and Decision Making.Recent Trends: Embedded and collaborative business intelligence, Visual data recovery, Data Storytelling and Data journalism			
<b>Contact Periods:</b> <b>Lecture: 45 Periods    Tutorial: 0 Period    Practical: 0 Period    Total: 45 Periods</b>			

## REFERENCES

1	VigneshPrajapati, <b>"Big Data Analytics with R and Hadoop"</b> , Packt Publishing, 2013.
2	Umesh R Hodeghatta, UmeshaNayak, <b>"Business Analytics Using R - A Practical Approach"</b> , Apress, 2017.
3	AnandRajaraman, Jeffrey David Ullman, <b>"Mining of Massive Datasets"</b> , Cambridge University Press, 2012.
4	Jeffrey D. Camm, James J. Cochran, Michael J. Fry, Jeffrey W. Ohlmann, David R. Anderson, <b>"Essentials of Business Analytics"</b> , Cengage Learning, second Edition, 2016.
5	U. Dinesh Kumar, <b>"Business Analytics: The Science of Data-Driven Decision Making"</b> , Wiley, 2017.
6	Rui Miguel Forte, <b>"Mastering Predictive Analytics with R"</b> , Packt Publication, 2015.

COURSE OUTCOMES:		Bloom's Taxonomy Mapped
Upon completion of the course, the students will be able to:		
C01	Identify the real world business problems and model with analytical solutions.	K4
C02	Solve analytical problem with relevant mathematics background knowledge.	K4
C03	Convert any real world decision making problem to hypothesis and apply suitable statistical testing.	K4
C04	Write and Demonstrate simple applications involving analytics using Hadoop and Map Reduce	K4
C05	Use open source frameworks for modeling and storing data.	K4

COURSE ARTICULATION MATRIX					
COs/POs	PO1	PO2	PO3	PO4	PO5
C01	1	2	1	2	1
C02	1	1	1	2	1
C03	2	2	1	1	-
C04	2	2	1	-	-
C05	1	2	-	-	-
<b>23EDOE10</b>	<b>1</b>	<b>2</b>	<b>1</b>	<b>2</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial					

## ASSESSMENT PATTERN – THEORY

Test / Bloom's Category*	Remembering (K1) %	Understanding (K2) %	Applying (K3) %	Analyzing (K4) %	Evaluating (K5) %	Creating (K6) %	Total %
CAT1	25	25	25	25			100
CAT2	20	25	25	30			100
Assignment 1	25	30	25	20			100
Assignment 2	30	20	30	20			100
ESE	20	30	20	30			100



<b>23EDOE11</b>	<b>INTRODUCTION TO INDUSTRIAL SAFETY</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	OE	3	0	0	3

<b>Course Objectives</b>	<ul style="list-style-type: none"><li>Summarize basics of industrial safety.</li><li>Describe fundamentals of maintenance engineering.</li><li>Explain wear and corrosion.</li><li>Illustrate fault tracing.</li><li>Identify preventive and periodic maintenance.</li></ul>		
<b>UNIT – I</b>	<b>INTRODUCTION</b>	<b>9 Periods</b>	
Accident, causes, types, results and control, mechanical and electrical hazards, types, causes and preventive steps/procedure, describe salient points of factories act 1948 for health and safety, wash rooms, drinking water layouts, light, cleanliness, fire, guarding, pressure vessels, etc., Safety color codes. Fire prevention and firefighting, equipment and methods.			
<b>UNIT – II</b>	<b>FUNDAMENTALS OF MAINTENANCE ENGINEERING</b>	<b>9 Periods</b>	
Definition and aim of maintenance engineering, Primary and secondary functions andresponsibility of maintenance department, Types of maintenance, Types and applications of tools used for maintenance, Maintenance cost & its relation with replacement economy, Service life of equipment.			
<b>UNIT – III</b>	<b>WEAR AND CORROSION AND THEIR PREVENTION</b>	<b>9 Periods</b>	
Wear- types, causes, effects, wear reduction methods, lubricants-types and applications, Lubrication methods, general sketch, working and applications, i. Screw down grease cup, ii. Pressure grease gun, iii. Splash lubrication, iv. Gravity lubrication, v. Wick feed lubrication vi. Side feed lubrication, vii. Ring lubrication, Definition, principle and factors affecting the corrosion. Types of corrosion, corrosion prevention methods.			
<b>UNIT – IV</b>	<b>FAULT TRACING</b>	<b>9 Periods</b>	
Fault tracing-concept and importance, decision tree concept, need and applications, sequence of fault-finding activities, show as decision tree, draw decision tree for problems in machine tools, hydraulic, pneumatic, automotive, thermal and electrical equipment's like, I. Any one machine tool, ii. Pump iii. Air compressor, iv. Internal combustion engine, v. Boiler, vi. Electrical motors, Types of faults in machine tools and their general causes.			
<b>UNIT – V</b>	<b>PERIODIC AND PREVENTIVE MAINTENANCE</b>	<b>9 Periods</b>	
Periodic inspection-concept and need, degreasing, cleaning and repairing schemes, overhauling of mechanical components, overhauling of electrical motor, common troubles and remedies of electric motor, repair complexities and its use, definition, need, steps and advantages of preventive maintenance. Steps/procedure for periodic and preventive maintenance of: I. Machine tools, ii. Pumps, iii. Air compressors, iv. Diesel generating (DG) sets, Program and schedule of preventive maintenance of mechanical and electrical equipment, advantages of preventive maintenance. Repair cycle concept and importance			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods</b>		<b>Tutorial: 0 Period</b>	<b>Practical: 0 Period</b>
			<b>Total: 45 Periods</b>



## REFERENCES

1	Hans F. Winterkorn, <i>"Foundation Engineering Handbook"</i> , Chapman & Hall London, 2013.
2	<i>"Maintenance Engineering"</i> by Dr. Siddhartha Ray, New Age International (P) Ltd., Publishers, 2017
3	<i>"Industrial Safety Management"</i> , McGraw Hill Education; New edition (1 July 2017)
4	<i>"Industrial Engineering And Production Management"</i> , S. Chand Publishing; Third edition, 2018
5	<i>"Industrial Safety and Maintenance Engineering"</i> , Parth B. Shah, 2021.

COURSE OUTCOMES:		Bloom's Taxonomy Mapped
Upon completion of the course, the students will be able to:		
C01	Ability to summarize basics of industrial safety	K4
C02	Ability to describe fundamentals of maintenance engineering	K4
C03	Ability to explain wear and corrosion	K4
C04	Ability to illustrate fault tracing	K4
C05	Ability to identify preventive and periodic maintenance	K4

COURSE ARTICULATION MATRIX					
COs/POs	P01	P02	P03	P04	P05
CO1	2	1	1	-	-
CO2	2	2	1	-	1
CO3	1	2	1	1	1
CO4	2	1	1	1	1
CO5	2	1	2	1	1
<b>23EDOE11</b>	<b>2</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial					

ASSESSMENT PATTERN – THEORY							
Test / Bloom's Category*	Remembering (K1) %	Understanding (K2) %	Applying (K3) %	Analyzing (K4) %	Evaluating (K5) %	Creating (K6) %	Total %
CAT1	25	25	25	25			100
CAT2	20	25	25	30			100
Assignment 1	25	30	25	20			100
Assignment 2	30	20	30	20			100
ESE	20	30	20	30			100

<b>23EDOE12</b>	<b>OPERATIONS RESEARCH</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>OE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objectives</b>	<ul style="list-style-type: none"><li>• Solve linear programming problem and solve using graphical method.</li><li>• Solve LPP using simplex method.</li><li>• Solve transportation, assignment problems.</li><li>• Solve project management problems.</li><li>• Solve scheduling problems.</li></ul>	
<b>UNIT – I</b>	<b>INTRODUCTION</b>	<b>9 Periods</b>
Optimization Techniques, Model Formulation, models, General L.R Formulation, Simplex Techniques, Sensitivity Analysis, Inventory Control Models		
<b>UNIT – II</b>	<b>LINEAR PROGRAMMING PROBLEM</b>	<b>9 Periods</b>
Formulation of a LPP - Graphical solution revised simplex method - duality theory - dual simplex method - sensitivity analysis - parametric programming		
<b>UNIT – III</b>	<b>NON-LINEAR PROGRAMMING PROBLEM</b>	<b>9 Periods</b>
Nonlinear programming problem - Kuhn-Tucker conditions min cost flow problem - max flow problem - CPM/PERT		
<b>UNIT – IV</b>	<b>SEQUENCING AND INVENTORY MODEL</b>	<b>9 Periods</b>
Scheduling and sequencing - single server and multiple server models - deterministic inventory models - Probabilistic inventory control models - Geometric Programming.		
<b>UNIT – V</b>	<b>GAME THEORY</b>	<b>9 Periods</b>
Competitive Models, Single and Multi-channel Problems, Sequencing Models, Dynamic Programming, Flow in Networks, Elementary Graph Theory, Game Theory Simulation		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods    Tutorial: 0 Period    Practical: 0 Period</b>		<b>Total: 45 Periods</b>

## REFERENCES

1	H.A. Taha "Operations Research, An Introduction", PHI, 2017.
2	"Industrial Engineering and Management", O. P. Khanna, 2017.
3	"Operations Research", S.K. Patel, 2017.
4	"Operation Research", AnupGoel, RuchiAgarwal, Technical Publications, Jan 2021.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Formulate linear programming problem and solve using graphical method.	K4
C02	Solve LPP using simplex method.	K4
C03	Formulate and solve transportation, assignment problems.	K4
C04	Solve project management problems.	K4
C05	Solve scheduling problems	K4

<b>COURSE ARTICULATION MATRIX</b>					
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>
CO1	2	1	1	-	-
CO2	2	2	1	-	-
CO3	1	1	2	1	1
CO4	1	1	-	-	-
CO5	2	1	-	-	-
<b>23EDOE12</b>	<b>2</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial					

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	25	25	25	25			100
CAT2	20	25	25	30			100
Assignment 1	25	30	25	20			100
Assignment 2	30	20	30	20			100
ESE	20	30	20	30			100

<b>23MFOE13</b>	<b>OCCUPATIONAL HEALTH AND SAFETY</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
NIL	OE	3	0	0	3

<b>Course Objectives</b>	<ul style="list-style-type: none"><li>• To gain knowledge about occupational health hazard and safety measures at work place.</li><li>• To learn about accident prevention and safety management.</li><li>• To learn about general safety measures in industries.</li></ul>		
<b>UNIT – I</b>	<b>OCCUPATIONAL HEALTH AND HAZARDS</b>	<b>9 Periods</b>	
Safety- History and development, National Safety Policy- Occupational Health Hazards - Ergonomics - Importance of Industrial Safety Radiation and Industrial Hazards- Machine Guards and its types, Automation.			
<b>UNIT – II</b>	<b>SAFETY AT WORKPLACE</b>	<b>9 Periods</b>	
Safety at Workplace - Safe use of Machines and Tools: Safety in use of different types of unit operations - Ergonomics of Machine guarding - working in different workplaces - Operation, Inspection and maintenance, Plant Design and Housekeeping, Industrial lighting, Vibration and Noise Case studies.			
<b>UNIT – III</b>	<b>ACCIDENT PREVENTION</b>	<b>9 Periods</b>	
Accident Prevention Techniques - Principles of accident prevention - Definitions, Theories, Principles – Hazard identification and analysis, Event tree analysis, Hazop studies, Job safety analysis - Theories and Principles of Accident causation - First Aid : Body structure and functions - Fracture and Dislocation, Injuries to various body parts.			
<b>UNIT – IV</b>	<b>SAFETY MANAGEMENT</b>	<b>9 Periods</b>	
Safety Management System and Law - Legislative measures in Industrial Safety: Various acts involved in Detail- Occupational safety, Health and Environment Management: Bureau of Indian Standards on Health and Safety, 14489, 15001 - OSHA, Process safety management (PSM) and its principles - EPA standards- Safety Management: Organisational & Safety Committee - its structure and functions.			
<b>UNIT – V</b>	<b>GENERAL SAFETY MEASURES</b>	<b>9 Periods</b>	
Plant Layout for Safety -design and location, distance between hazardous units, lighting, colour coding, pilot plant studies, Housekeeping - Accidents Related with Maintenance of Machines - Work Permit System: Significance of Documentation Directing Safety, Leadership -Case studies involving implementation of health and safety measures in Industries.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods    Tutorial: 0 Period    Practical: 0 Period    Total: 45 Periods</b>			

#### REFERENCES:

1	Benjamin O.Alli, <i>Fundamental Principles of Occupational Health and Safety</i> ILO 2008.
2	Danuta Koradecka, <i>Handbook of Occupational Health and Safety</i> , CRC, 2010.
3	Dr. Siddhartha Ray, <i>Maintenance Engineering</i> , New Age International (P) Ltd., Publishers, 2017
4	Deshmukh. L.M., <i>Industrial Safety Management</i> , 3 <sup>rd</sup> Edition, Tata McGraw Hill, New Delhi, 2008.
5	<a href="https://nptel.ac.in/courses/110105094">https://nptel.ac.in/courses/110105094</a>

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Gain the knowledge about occupational health hazard and safety measures at work place.	K3
C02	Learn about accident prevention and safety management.	K2
C03	Understand occupational health hazards and general safety measures in industries.	K3
C04	Know various laws, standards and legislations.	K2
C05	Implement safety and proper management of industries.	K4

<b>COURSE ARTICULATION MATRIX:</b>					
<b>Cos/Pos</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>
C01	2	1	1	1	1
C02	2	2	1	1	1
C03	1	2	1	1	1
C04	2	1	1	1	1
C05	2	1	2	1	1
<b>23MFOE13</b>	<b>2</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial					

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remem bering (K1) %</b>	<b>Understandin g (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzin g (K4) %</b>	<b>Evaluatin g (K5) %</b>	<b>Creating (K6) %</b>	<b>Tota l %</b>
CAT1		50	50				100
CAT2		50	30	20			100
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1		50	50				100
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2		50	30	20			100
ESE		40	40	20			100

<b>23MFOE14</b>	<b>COST MANAGEMENT OF ENGINEERING PROJECTS</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
NIL	OE	3	0	0	3

<b>Course Objectives</b>	<ul style="list-style-type: none"><li>• To understand the costing concepts and their role in decision making.</li><li>• To acquire the project management concepts and their various aspects in selection.</li><li>• To gain the knowledge in costing concepts with project execution.</li><li>• To develop knowledge of costing techniques in service sector and various budgetary control techniques.</li><li>• To familiarize with quantitative techniques in cost management.</li></ul>		
<b>UNIT – I</b>	<b>INTRODUCTION TO COSTING CONCEPTS</b>	<b>9 Periods</b>	
Introduction and Overview of the Strategic Cost Management Process, Cost concepts in decision-making; Relevant cost, Differential cost, Incremental cost and Opportunity cost. Objectives of a Costing System; Inventory valuation; Creation of a Database for operational control; Provision of data for Decision - Making.			
<b>UNIT – II</b>	<b>PROJECT PLANNING ACTIVITIES</b>	<b>9 Periods</b>	
Project: meaning, Different types, why to manage, cost overruns centers, various stages of project execution: conception to commissioning. Project execution as conglomeration of technical and nontechnical activities. Detailed Engineering activities. Pre project execution main clearances and documents Project team: Role of each member. Importance Project site: Data required with significance. Project contracts. Types and contents. Project execution Project cost control. Bar charts and Network diagram. Project commissioning: mechanical and process.			
<b>UNIT – III</b>	<b>COST ANALYSIS</b>	<b>9 Periods</b>	
Cost Behaviour and Profit Planning Marginal Costing; Distinction between Marginal Costing and Absorption Costing; Break-even Analysis, Cost-Volume-Profit Analysis. Various decision-making problems. Standard Costing and Variance Analysis.			
<b>UNIT – IV</b>	<b>PRICING STRATEGIES AND BUDGETORY CONTROL</b>	<b>9 Periods</b>	
Pricing strategies: Pareto Analysis. Target costing, Life Cycle Costing, Costing of service sector, Just-in-time approach, Material Requirement Planning, Enterprise Resource Planning. Budgetary Control; Flexible Budgets; Performance budgets; Zero-based budgets. Measurement of Divisional profitability pricing decisions including transfer pricing.			
<b>UNIT – V</b>	<b>TQM AND OPERATIONS REASEARCH TOOLS</b>	<b>9 Periods</b>	
Total Quality Management and Theory of constraints, Activity-Based Cost Management, Bench Marking; Balanced Score Card and Value-Chain Analysis. Quantitative techniques for cost management, Linear Programming, PERT/CPM, Transportation problems, Assignment problems, Simulation, Learning Curve Theory.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods    Tutorial: 0 Period    Practical: 0 Period    Total: 45 Periods</b>			

**REFERENCES:**

1	Charles T. Horngren and George Foster, <i>Advanced Management Accounting</i> , 2018.
2	John M. Nicholas, <i>Project Management for Engineering, Business and Technology</i> , Taylor & Francis, 2016
3	Nigel J, <i>Engineering Project Management</i> , John Wiley and Sons Ltd, Smith 2015.
4	Charles T. Horngren and George Foster <i>Cost Accounting a Managerial Emphasis</i> , Prentice Hall of India, New Delhi, 2011.
5	<a href="https://archive.nptel.ac.in/courses/110/104/110104073/">https://archive.nptel.ac.in/courses/110/104/110104073/</a>

**COURSE OUTCOMES:**

Upon completion of the course, the students will be able to:

**Bloom's  
Taxonomy  
Mapped**

C01	Apply the costing concepts and their role in decision making.	K3
C02	Apply the project management concepts and analyze their various aspects in selection.	K4
C03	Interpret costing concepts with project execution.	K4
C04	Gain knowledge of costing techniques in service sector and various budgetary control techniques.	K2
C05	Become familiar with quantitative techniques in cost management.	K3

**COURSE ARTICULATION MATRIX:**

COs/Pos	P01	P02	P03	P04	P05
C01	1	1	2	1	1
C02	2	1	1	1	-
C03	2	2	2	-	-
C04	1	1	1	1	1
C05	1	2	1	1	-
<b>23MFOE14</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial					

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1			40	60			100
CAT2		30	30	40			100
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1			40	60			100
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2		30	30	40			100
ESE		20	40	40			100





<b>23MFOE15</b>	<b>COMPOSITE MATERIALS</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	OE	3	0	0	3

<b>Course Objectives</b>	1. To summarize the characteristics of composite materials and effect of reinforcement in composite materials. 2. To identify the various reinforcements used in composite materials. 3. To compare the manufacturing process of metal matrix composites. 4. To understand the manufacturing processes of polymer matrix composites. 5. To analyze the strength of composite materials.		
<b>UNIT – I</b>	<b>INTRODUCTION</b>	<b>9 Periods</b>	
Definition – Classification and characteristics of Composite materials. Advantages and application of composites. Functional requirements of reinforcement and matrix. Effect of reinforcement on overall composite performance.			
<b>UNIT – II</b>	<b>REINFORCEMENT</b>	<b>9 Periods</b>	
Preparation-layup, curing, properties and applications of glass fibers, carbon fibers, Kevlar fibers and Boron fibers. Properties and applications of whiskers, particle reinforcements. Mechanical Behavior of composites: Rule of mixtures, Inverse rule of mixtures. Isostrain and Isosteresconditions.			
<b>UNIT – III</b>	<b>MANUFACTURING OF METAL MATRIX COMPOSITES</b>	<b>9 Periods</b>	
Casting – Solid State diffusion technique, Cladding – Hot isostatic pressing- Manufacturing of Ceramic Matrix Composites: Liquid Metal Infiltration – Liquid phase sintering–Manufacturing of Carbon – Carbon composites: Knitting, Braiding, Weaving- Properties and applications.			
<b>UNIT – IV</b>	<b>MANUFACTURING OF POLYMER MATRIX COMPOSITE</b>	<b>9 Periods</b>	
Preparation of Moulding compounds and prepregs – hand layup method – Autoclave method – Filament winding method – Compression moulding – Reaction injection moulding. Properties and applications.			
<b>UNIT – V</b>	<b>STRENGTH ANALYSIS OF COMPOSITES</b>	<b>9 Periods</b>	
Laminar Failure Criteria-strength ratio, maximum stress criteria, maximum strain criteria, interacting failure criteria, hygrothermal failure. Laminate first play failure-insight strength; Laminate strength-ply discount truncated maximum strain criterion; strength design using caplet plots; stress concentrations.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods</b>		<b>Tutorial: 0 Period</b>	<b>Practical: 0 Period</b>
<b>Total: 45 Periods</b>			

#### REFERENCES:

1	Chawla K.K., <i>Composite Materials</i> , Springer, 2013.
2	Lubin.G, <i>Hand Book of Composite Materials</i> , Springer New York, 2013.
3	Deborah D.L. Chung, <i>Composite Materials Science and Applications</i> , Springer, 2011.
4	uLektz, <i>Composite Materials and Mechanics</i> , uLektz Learning Solutions Private Limited, Lektz, 2013.
5	<a href="https://nptel.ac.in/courses/112104168">https://nptel.ac.in/courses/112104168</a>

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Know the characteristics of composite materials and effect of reinforcement in composite materials.	K2
C02	Know the various reinforcements used in composite materials.	K2
C03	Understand and apply the manufacturing processes of metal matrix composites	K3
C04	Understand and apply the manufacturing processes of polymer matrix composites.	K3
C05	Analyze the strength of composite materials.	K4

<b>COURSE ARTICULATION MATRIX:</b>					
<b>COs/Pos</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>
C01	1	2	1	1	1
C02	2	2	1	1	2
C03	2	1	2	1	1
C04	1	2	2	2	1
C05	1	2	1	1	1
<b>23MFOE15</b>	<b>1</b>	<b>2</b>	<b>2</b>	<b>1</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial					

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1		60	40				100
CAT2			60	40			100
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1		60	40				100
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2			60	40			100
ESE		40	40	20			100

23TEOE16	<b>GLOBAL WARMING SCIENCE</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	OE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To make the students learn about the material consequences of climate change, sea level change due to increase in the emission of greenhouse gases and to examine the science behind mitigation and adaptation proposals.</li></ul>		
<b>UNIT – I</b>	<b>INTRODUCTION</b>	<b>9 Periods</b>	
Terminology relating to atmospheric particles – Aerosols - Types, characteristics, measurements – Particle mass spectrometry - Anthropogenic-sources, effects on humans.			
<b>UNIT – II</b>	<b>CLIMATE MODELS</b>	<b>9 Periods</b>	
General climate modeling- Atmospheric general circulation model - Oceanic general circulation model, sea ice model, land model concept, paleo-climate - Weather prediction by numerical process. Impacts of climate change - Climate Sensitivity - Forcing and feedback.			
<b>UNIT – III</b>	<b>EARTH CARBON CYCLE AND FORECAST</b>	<b>9 Periods</b>	
Carbon cycle-process, importance, advantages - Carbon on earth - Global carbon reservoirs - Interactions between human activities and carbon cycle - Geologic time scales - Fossil fuels and energy - Perturbed carbon cycle.			
<b>UNIT – IV</b>	<b>GREENHOUSE GASES</b>	<b>9 Periods</b>	
Blackbody radiation - Layer model - Earth's atmospheric composition and Green house gases effects on weather and climate - Radioactive equilibrium - Earth's energy balance.			
<b>UNIT – V</b>	<b>GEO ENGINEERING</b>	<b>9 Periods</b>	
Solar mitigation - Strategies – Carbon dioxide removal - Solar radiation management - Recent observed trends in global warming for sea level rise, drought, glacier extent.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial: 0Periods      Practical: 0 Periods      Total: 45 Periods</b>			

#### REFERENCES:

1	Eli Tziperman, <b>"Global Warming Science: A Quantitative Introduction to Climate Change and Its Consequences"</b> , Princeton University Press, 1 <sup>st</sup> Edition, 2022.
2	John Houghton, <b>"Global warming: The Complete Briefing"</b> , Cambridge University Press, 5 <sup>th</sup> Edition, 2015.
3	David Archer, <b>"Global warming: Understanding the Forecast"</b> , Wiley, 2 <sup>nd</sup> Edition, 2011.
4	David S.K. Ting, Jacqueline A Stagner, <b>"Climate Change Science: Causes, Effects and Solutions for Global Warming"</b> , Elsevier, 1 <sup>st</sup> Edition, 2021.
5	Frances Drake, <b>"Global Warming: The Science of Climate Change"</b> , Routledge, 1 <sup>st</sup> edition, 2000.
6	Dickinson, <b>"Climate Engineering-A review of aerosol approaches to changing the global energybalance"</b> , Springer, 1996.
7	Andreas Schmittner, <b>"Introduction to Climate Science"</b> , Oregon State University, 2018.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
CO1	Understand the global warming in relation to climate changes throughout the earth.	K2
CO2	Assess the best predictions of current climate models.	K4
CO3	Understand the importance of carbon cycle and its implication on fossil fuels.	K2
CO4	Know about current issues, including impact from society, environment, economy as well as ecology related to greenhouse gases.	K4
CO5	Know the safety measures and precautions regarding global warming.	K5

<b>Course Articulation Matrix</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
CO1	2	1	2	1	1	2
CO2	1	1	2	1	1	1
CO3	1	2	1	1	1	2
CO4	1	1	1	1	1	2
CO5	2	1	2	1	1	2
<b>23TEOE16</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>2</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>Assessment pattern – theory</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20	35	35	10	-	-	100
CAT2	15	25	25	20	15	-	100
Individual Assessment 1 / Case Study 1 / Seminar 1 / Project 1	25	20	20	35	-	-	100
Individual Assessment 2 / Case Study 2 / Seminar 2 / Project 2	20	20	35	15	10	-	100
ESE	25	20	25	20	10	-	100

<b>23TEOE17</b>	<b>INTRODUCTION TO NANO ELECTRONICS</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	OE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To make the students provide strong, essential, important methods and foundations of quantum mechanics and apply quantum mechanics on engineering fields.</li></ul>		
<b>UNIT – I</b>	<b>INTRODUCTION</b>	<b>9 Periods</b>	
Particles and Waves - Operators in quantum mechanics - The Postulates of quantum mechanics - The Schrodinger equation values and wave packet Solutions - Ehrenfest's Theorem.			
<b>UNIT – II</b>	<b>ELECTRONIC STRUCTURE AND MOTION</b>	<b>9 Periods</b>	
Atoms- The Hydrogen Atom - Many-Electron Atoms – Pseudopotentials, Nuclear Structure, Molecules, Crystals - Translational motion – Penetration through barriers – Particle in a box - Two terminal quantum dot devices - Two terminal quantum wire devices.			
<b>UNIT – III</b>	<b>SCATTERING THEORY</b>	<b>9 Periods</b>	
The formulation of scattering events - Scattering cross section - Stationary scattering state - Partial wave stationary scattering events - multi-channel scattering - Solution for Schrodinger equation- Radial and wave equation - Greens' function.			
<b>UNIT – IV</b>	<b>CLASSICAL STATISTICS</b>	<b>9 Periods</b>	
Probabilities and microscopic behaviours - Kinetic theory and transport processes in gases - Magnetic properties of materials - The partition function.			
<b>UNIT – V</b>	<b>QUANTUM STATISTICS</b>	<b>9 Periods</b>	
Statistical mechanics - Basic Concepts - Statistical models applied to metals and semiconductors - The thermal properties of solids- The electrical properties of materials - Black body radiation - Low temperatures and degenerate systems.			
<b>Contact Periods:</b>			
<b>Lecture:45 Periods</b>		<b>Tutorial: 0 Periods</b>	<b>Practical: 0 Periods</b>
		<b>Total:45 Periods</b>	

#### REFERENCES:

1	Vladimi V.Mitin, Viatcheslav A. Kochelap and Michael A.Stroscio, <b>"Introduction to Nanoelectronics: Science, Nanotechnology, Engineering, and Applications"</b> , Cambridge University Press, 1 <sup>st</sup> Edition, 2007.
2	Vinod Kumar Khanna, <b>"Introductory Nanoelectronics: Physical Theory and Device Analysis"</b> , Routledge, 1 <sup>st</sup> Edition, 2020.
3	George W. Hanson, <b>"Fundamentals of Nanoelectronics"</b> , Pearson Publishers, United States Edition, 2007.
4	Marc Baldo, <b>"Introduction to Nanoelectronics"</b> , MIT Open Courseware Publication, 2011.
5	Vladimi V.Mitin, <b>"Introduction to Nanoelectronics"</b> , Cambridge University Press, South Asian Edition, 2009.
6	Peter L. Hagelstein, Stephen D. Senturia and Terry P. Orlando, <b>"Introductory Applied Quantum Statistical Mechanics"</b> , Wiley, 2004.

7	A. F. J. Levi, “ <i>Applied Quantum Mechanics</i> ”, 2 <sup>nd</sup> Edition, Cambridge, 2012.	
<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Understand the postulates of quantum mechanics.	K2
C02	Know about nano electronic systems and building blocks.	K2
C03	Solve the Schrodinger equation in 1D, 2D and 3D different applications.	K4
C04	Learn the concepts involved in kinetic theory of gases.	K2
C05	Know about statistical models applies to metals and semiconductor.	K3

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	1	1	1	1	1	1
C02	2	2	1	1	1	1
C03	2	2	2	1	1	1
C04	1	1	1	1	1	1
C05	1	1	1	1	1	1
<b>23TEOE17</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	30	30	20	20	-	-	100
CAT2	30	30	20	20	-	-	100
Individual Assessment 1 / Case Study 1 / Seminar 1 / Project 1	35	25	20	20	-	-	100
Individual Assessment 2 / Case Study 2 / Seminar 2 / Project 2	30	25	20	25	-	-	100
ESE	20	30	30	20	-	-	100

<b>22TEOE18</b>	<b>GREEN SUPPLY CHAIN MANAGEMENT</b> (Common to all Branches)
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PRE REQUISITES		CATEGORY	L	T	P	C
NIL		OE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To make the students learn and focus on the fundamental strategies, tools and techniques required to analyze and design environmentally sustainable supply chain systems.</li></ul>		
<b>UNIT - I</b>	<b>INTRODUCTION</b>	<b>9 Periods</b>	
Intro to SCM – complexity in SCM, Facility location - Logistics – Aim, activities, importance, progress, current trends - Integrating logistics with an organization.			
<b>UNIT - II</b>	<b>ESSENTIALS OF SUPPLY CHAIN MANAGEMENT</b>	<b>9 Periods</b>	
Basic concepts of supply chain management - Supply chain operations – Planning and sourcing - Making and delivering - Supply chain coordination and use of technology - Developing supply chain systems.			
<b>UNIT - III</b>	<b>PLANNING THE SUPPLY CHAIN</b>	<b>9 Periods</b>	
Types of decisions – strategic, tactical, operational - Logistics strategies, implementing the strategy - Planning resources – types, capacity, schedule, controlling material flow, measuring and improving performance.			
<b>UNIT - IV</b>	<b>ACTIVITIES IN THE SUPPLY CHAIN</b>	<b>9 Periods</b>	
Procurement – cycle, types of purchase – Framework of e-procurement - Inventory management – EOQ, uncertain demand and safety stock, stock control - Material handling – Purpose of warehouse and ownership, layout, packaging - Transport – mode, ownership, vehicle routing and scheduling models- Travelling salesman problems - Exact and heuristic methods.			
<b>UNIT - V</b>	<b>SUPPLY CHAIN MANAGEMENT STRATEGIES</b>	<b>9 Periods</b>	
Five key configuration components - Four criteria of good supply chain strategies - Next generation strategies- New roles for end-to-end supply chain management - Evolution of supply chain organization – International issues in SCM – Regional differences in logistics.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>			

#### REFERENCES:

1	Charisios Achillas, Dionysis D. Bochtis, Dimitrios Aidonis and Dimitris Folinas, " <b>Green Supply Chain Management</b> ", Routledge, 1 <sup>st</sup> Edition, 2019.
2	Hsiao-Fan Wang and Surendra M.Gupta, " <b>Green Supply Chain Management: Product Life Cycle Approach</b> ", McGraw-Hill Education, 1 <sup>st</sup> Edition, 2011.
3	Joseph Sarkis and Yijie Dou, " <b>Green Supply Chain Management</b> ", Routledge, 1 <sup>st</sup> Edition, 2017.
4	Arunachalam Rajagopal, " <b>Green Supply Chain Management: A Practical Approach</b> ", Replica, 2021.
5	Mehmood Khan, Matloub Hussain and Mian M. Ajmal, " <b>Green Supply Chain Management for Sustainable Business Practice</b> ", IGI Global, 1 <sup>st</sup> Edition, 2016.

6	<i>S Emmett, "Green Supply Chains: An Action Manifesto", John Wiley &amp; Sons Inc, 2010.</i>
7	<i>Joseph Sarkis and Yijie Dou, "Green Supply Chain Management: A Concise Introduction", Routledge, 1<sup>st</sup> Edition, 2017.</i>

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Integrate logistics with an organization.	K2
C02	Evaluate complex qualitative and quantitative data to support strategic and operational decisions.	K5
C03	Develop self-leadership strategies to enhance personal and professional effectiveness.	K3
C04	Analyze inventory management models and dynamics of supply chain.	K4
C05	Identify issues in international supply chain management and outsources strategies.	K3

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	1	1	1	1	1	3
C02	2	2	1	1	1	1
C03	2	1	2	1	1	1
C04	2	2	1	1	2	2
C05	1	1	2	1	1	3
<b>23TEOE18</b>	<b>2</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>2</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	25	25	30	10	10	-	100
CAT2	30	40	20	10	-	-	100
Individual Assessment 1 / Case Study 1 / Seminar 1 / Project 1	30	20	25	15	10	-	100
Individual Assessment 2 / Case Study 2 / Seminar 2 / Project 2	35	30	25	10	-	-	100
ESE	30	30	20	10	10	-	100



<b>23PSOE19</b>	<b>DISTRIBUTION AUTOMATION SYSTEM</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
NIL	OE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To study about the distributed automation and economic evaluation schemes of power network</li></ul>	
<b>UNIT - I</b>	<b>INTRODUCTION</b>	<b>9 Periods</b>
Introduction to Distribution Automation (DA) - Control system interfaces- Control and data requirements- Centralized (vs) decentralized control- DA system-DA hardware-DAS software.		
<b>UNIT - II</b>	<b>DISTRIBUTION AUTOMATION FUNCTIONS</b>	<b>9 Periods</b>
DA capabilities - Automation system computer facilities- Management processes- Information management- System reliability management- System efficiency management- Voltage management- Load management.		
<b>UNIT - III</b>	<b>COMMUNICATION SYSTEMS</b>	<b>9 Periods</b>
Communication requirements - reliability- Cost effectiveness- Data requirements- Two way capability- Communication during outages and faults - Ease of operation and maintenance- Conforming to the architecture of flow. Distribution line carrier- Ripple control-Zero crossing technique- Telephone, cableTV, radio, AM broadcast, FM SCA,VHF radio, microwave satellite, fiber optics-Hybrid communication systems used in field tests.		
<b>UNIT - IV</b>	<b>ECONOMIC EVALUATION METHODS</b>	<b>9 Periods</b>
Development and evaluation of alternate plans- select study area – Select study period- Project load growth-Develop alternatives- Calculate operating and maintenance costs-Evaluate alternatives.		
<b>UNIT - V</b>	<b>ECONOMIC COMPARISON</b>	<b>9 Periods</b>
Economic comparison of alternate plans-Classification of expenses - capital expenditures-Comparison of revenue requirements of alternative plans-Book life and continuing plant analysis- Year by year revenue requirement analysis, Short term analysis- End of study adjustment-Break even analysis, sensitivity analysis - Computational aids.		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>		

## REFERENCES

1	M.K. Khedkar, G.M. Dhole, <b>"A Textbook of Electric Power Distribution Automation"</b> , Laxmi Publications, Ltd., 2010.
2	Maurizio Di Paolo Emilio, <b>"Data Acquisition Systems: From Fundamentals to Applied Design"</b> , Springer Science & Business Media, 21-Mar-2013
3	IEEE Tutorial course <b>"Distribution Automation"</b> , IEEE Working Group on Distribution Automation, IEEE Power Engineering Society. Power Engineering Education Committee, IEEE Power Engineering Society. Transmission and Distribution Committee, Institute of Electrical and Electronics Engineers, 1988
4	Taub, <b>"Principles Of Communication Systems"</b> , Tata McGraw-Hill Education, 07-Sep-2008

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Analyse the requirements of distributed automation	K1
C02	Know the functions of distributed automation	K2
C03	Perform detailed analysis of communication systems for distributed automation.	K3
C04	Study the economic evaluation method	K4
C05	Understand the comparison of alternate plans	K5

<b>COURSE ARTICULATION MATRIX</b>				
<b>COs/Pos</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>
C01	2	-	1	3
C02	3	-	3	2
C03	3	-	3	2
C04	3	-	3	1
C05	2	-	1	2
<b>23PS0E19</b>	<b>3</b>	<b>-</b>	<b>3</b>	<b>2</b>
1 – Slight, 2 – Moderate, 3 – Substantial				

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20%	30%	20%	10%	20%	-	100%
CAT2	20%	20%	20%	20%	20%	-	100%
Individual Assessment1 / Case study1/ Seminar 1/Project1	20%	10%	30%	20%	20%	-	100%
Individual Assessment2 / Case study2/ Seminar 2 /Project2	20%	30%	10%	20%	20%	-	100%
ESE	30%	20%	20%	20%	10%	-	100%

<b>23PSOE20</b>	<b>ELECTRICITY TRADING AND ELECTRICITY ACTS</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	OE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To acquire expertise on Electric supply and demand of Indian Grid, gain exposure on energy trading in the Indian market and infer the electricity acts and regulatory authorities.</li></ul>		
<b>UNIT – I</b>	<b>ENERGY DEMAND</b>	<b>9 Periods</b>	
Basic concepts in Economics - Descriptive Analysis of Energy Demand - Decomposition Analysis and Parametric Approach - Demand Side Management - Load Management - Demand Side Management - Energy Efficiency - Rebound Effect			
<b>UNIT – II</b>	<b>ENERGY SUPPLY</b>	<b>9 Periods</b>	
Supply Behavior of a Producer - Energy Investment - Economics of Non-renewable Resources - Economics of Renewable Energy Supply Setting the context - Economics of Renewable Energy Supply - Economics of Electricity Supply			
<b>UNIT – III</b>	<b>ENERGY MARKET</b>	<b>9 Periods</b>	
Perfect Competition as a Market Form - Why is the Energy Market not Perfectly Competitive? - Market Failure and Monopoly - Oil Market: Pre OPEC Era I - Oil Market: Pre OPEC Era II - Oil Market: OPEC			
<b>UNIT – IV</b>	<b>LAW ON ELECTRICITY</b>	<b>9 Periods</b>	
Introduction of the Electricity Law; Constitutional Design - Evolution of Laws on Electricity Salient Features of Electricity Act, 2003 - Evolution of Laws on Electricity - Salient Features of the Electricity Act 2003			
<b>UNIT – V</b>	<b>REGULATORY COMMISSIONS FOR ELECTRICITY ACT</b>	<b>9 Periods</b>	
Regulatory Commissions - Appellate Tribunal - Other Institutions under the Act - Electricity (Amendment) Bill 2020/2021. A Critical Comment - Renewable Energy - Role of Civil Society; Comments on Draft Renewable Energy Act, 2015			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>			

## REFERENCES

1	Bhattacharyya, Subhes. C. (2011). <i>"Energy Economics: Concepts, Issues, Markets and Governance"</i> . Springer.London, UK
2	Stevens, P. (2000). <i>"An Introduction to Energy Economics. In Stevens, P.(ed.) The Economics of Energy"</i> , Vol.1, Edward Elgar, Cheltenham, UK.
3	Nausir Bharucha, <i>"Guide to the Electricity Laws"</i> , LexisNexis, 2018
4	Mohammad Naseem, <i>"Energy Laws in India"</i> , Kluwer Law International, 3rd Edn, The Netherlands, 2017.
5	Alok Kumar & Sushanta K Chaterjee, <i>"Electricity Sector in India: Policy and Regulation"</i> , OUP, 2012.
6	Benjamin K Sovacool & Michael H Dwrkin, <i>"Global Energy Justice: Problems, Principles and Practices"</i> , Cambridge Univesity Press, 2014.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Describe electric supply and demand of power grid	K1
C02	Summarize various energy trading strategies	K2
C03	Relate the electricity acts practically	K3
C04	Cite the electricity regulatory authorities	K2
C05	Analyze/check the existing power grid for its technical and economical sustainability	K4

<b>COURSE ARTICULATION MATRIX</b>				
<b>COs/Pos</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>
C01	3	-	3	3
C02	3	-	1	1
C03	3	-	2	2
C04	3	-	1	2
C05	3	-	3	3
<b>23PSOE20</b>	<b>3</b>	<b>-</b>	<b>2</b>	<b>2</b>
1 – Slight, 2 – Moderate, 3 – Substantial				

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20%	30%	20%	30%	-	-	100%
CAT2	20%	20%	20%	20%	20%	-	100%
Individual Assessment1 / Case study1/ Seminar 1/Project1	20%	30%	30%	20%	-	-	100%
Individual Assessment2 / Case study2/ Seminar 2 /Project2	20%	30%	-	20%	-	40%	100%
ESE	30%	30%	-	20%	20%	-	100%

<b>23PSOE21</b>	<b>MODERN AUTOMOTIVE SYSTEMS</b> (Common to all Branches)
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<b>PRE REQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	OE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To expose the students with theory and applications of Automotive Electrical and Electronic Systems.</li></ul>		
<b>UNIT – I</b>	<b>INTRODUCTION TO MODERN AUTOMOTIVE ELECTRONICS</b>	<b>9 Periods</b>	
Introduction to modern automotive systems and need for electronics in automobiles- Role of electronics and microcontrollers- Sensors and actuators- Possibilities and challenges in automotive industry- Enabling technologies and industry trends.			
<b>UNIT – II</b>	<b>SENSORS AND ACTUATORS</b>	<b>9 Periods</b>	
Introduction- basic sensor arrangement- Types of sensors- Oxygen sensor, engine crankshaft angular position sensor – Engine cooling water temperature sensor- Engine oil pressure sensor- Fuel metering- vehicle speed sensor and detonation sensor- Pressure Sensor- Linear and angle sensors- Flow sensor- Temperature and humidity sensors- Gas sensor- Speed and Acceleration sensors- Knock sensor- Torque sensor- Yaw rate sensor- Tyre Pressure sensor- Actuators - Stepper motors – Relays.			
<b>UNIT – III</b>	<b>POWERTRAIN CONTROL SYSTEMS IN AUTOMOBILE</b>	<b>9 Periods</b>	
Electronic Transmission Control - Digital engine control system: Open loop and close loop control systems- Engine cooling and warm up control- Acceleration- Detonation and idle speed control - Exhaust emission control engineering- Onboard diagnostics- Future automotive powertrain systems.			
<b>UNIT – IV</b>	<b>SAFETY, COMFORT AND CONVENIENCE SYSTEMS</b>	<b>9 Periods</b>	
Cruise Control- Anti-lock Braking Control- Traction and Stability control- Airbag control system- Suspension control- Steering control- HVAC Control.			
<b>UNIT – V</b>	<b>ELECTRONIC CONTROL UNITS (ECU)</b>	<b>9 Periods</b>	
Introduction to Energy Sources for ECU, Need for ECUs- Advances in ECUs for automotives - Design complexities of ECUs- V-Model for Automotive ECU's- Architecture of an advanced microcontroller (XC166 Family, 32-bit Tricore) used in the design of automobile ECUs- On chip peripherals, protocol interfaces, analog and digital interfaces.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>			

## REFERENCES

1	Enrique Acha, Manuel Madrigal, <b>“Power System Harmonics: Computer Modeling and Analysis”</b> , John Wiley and Sons, 2001.
2	M. H. J. Bollen, <b>“Understanding Power Quality Problems, Voltage Sag and Interruptions”</b> , IEEE Press, series on Power Engineering, 2000.
3	Roger C. Dugan, Mark F. McGranaghan, Surya Santoso and Wayne Beaty H., <b>“Electrical Power System Quality”</b> , Second Edition, McGraw Hill Publication Co., 2008.
4	G.T.Heydt, <b>“Electric Power Quality”</b> , Stars in a Circle Publications, 1994(2nd edition).

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Acquire knowledge about conventional automotive control units and devices.	K1
C02	Recognize the practical issues in the automotive control systems	K2
C03	Analyze the impact of modern automotive techniques in various Engineering applications	K4
C04	Develop modern automotive control system for electrical and electronics systems	K6
C05	Understand the function of sensors and actuators	K2

<b>COURSE ARTICULATION MATRIX</b>				
<b>COs/Pos</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>
C01	3	-	1	3
C02	3	-	3	2
C03	3	-	3	2
C04	2	-	3	1
C05	2	-	1	2
<b>23PS0E21</b>	<b>3</b>	<b>-</b>	<b>2</b>	<b>2</b>
1 – Slight, 2 – Moderate, 3 – Substantial				

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20%	30%	20%	30%	-	-	100%
CAT2	20%	20%	20%	20%	20%	-	100%
Individual Assessment1 / Case study1/ Seminar 1/Project1	20%	30%	-	20%	-	30%	100%
Individual Assessment2 / Case study2/ Seminar 2 /Project2	20%	30%	-	20%	-	40%	100%
ESE	30%	30%	20%	20%	-	-	100%

<b>23PEOE22</b>	<b>VIRTUAL INSTRUMENTATION</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	OE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To comprehend the Virtual instrumentation programming concepts towards measurements and control and to instill knowledge on DAQ, signal conditioning and its associated software tools</li></ul>		
<b>UNIT – I</b>	<b>INTRODUCTION</b>	<b>7 Periods</b>	
Introduction - advantages - Block diagram and architecture of a virtual instrument - Conventional Instruments versus Traditional Instruments - Data-flow techniques, graphical programming in data flow, comparison with conventional programming.			
<b>UNIT – II</b>	<b>GRAPHICAL PROGRAMMING AND LabVIEW</b>	<b>9 Periods</b>	
Concepts of graphical programming - LabVIEW software - Concept of VIs and sub VI - Display types - Digital - Analog - Chart and Graphs. Loops - structures - Arrays – Clusters- Local and global variables – String - Timers and dialog controls.			
<b>UNIT – III</b>	<b>MANAGING FILES &amp; DESIGN PATTERNS</b>	<b>11 Periods</b>	
High-level and low-level file I/O functions available in LabVIEW – Implementing File I/O functions to read and write data to files – Binary Files – TDMS – sequential programming – State machine programming – Communication between parallel loops –Race conditions – Notifiers & Queues – Producer Consumer design patterns			
<b>UNIT – IV</b>	<b>PC BASED DATA ACQUISITION</b>	<b>9 Periods</b>	
Introduction to data acquisition on PC, Sampling fundamentals, ADCs, DACs, Calibration, Resolution, - analog inputs and outputs - Single-ended and differential inputs - Digital I/O, counters and timers, DMA, Data acquisition interface requirements - Issues involved in selection of Data acquisition cards - Use of timer-counter and analog outputs on the universal DAQ card.			
<b>UNIT – V</b>	<b>DATA ACQUISITION AND SIGNAL CONDITIONING</b>	<b>9 Periods</b>	
Components of a DAQ system, Bus, Signal and accuracy consideration when choosing DAQ hardware – Measurement of analog signal with Finite and continuous buffered acquisition- analog output generation – Signal conditioning systems – Synchronizing measurements in single & multiple devices – Power quality analysis using Electrical Power Measurement tool kit.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>			

#### REFERENCES :

1	Jeffrey Travis, Jim Kring, “ <b>LabVIEW for Everyone: Graphical Programming Made Easy and Fun</b> ” (3rd Edition), Prentice Hall, 2006.
2	Jovitha Jerome, “ <b>Virtual Instrumentation using LabVIEW</b> ”, PHI, 2010
3	Gary W. Johnson, Richard Jennings, “ <b>LabVIEW Graphical Programming</b> ”, McGraw Hill Professional Publishing, 2019
4	Robert H. Bishop, “ <b>Learning with LabVIEW</b> ”, Prentice Hall, 2013.

5	Kevin James, <i>"PC Interfacing and Data Acquisition: Techniques for Measurement, Instrumentation and Control"</i> , Newness, 2000
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COURSE OUTCOMES:		Bloom's Taxonomy Mapped
Upon completion of the course, the students will be able to:		
C01	Describe the graphical programming techniques using LabVIEW software.	K2
C02	Explore the basics of programming and interfacing using related hardware.	K4
C03	Analyse the aspects and utilization of PC based data acquisition and Instrument interfaces.	K4
C04	Create programs and Select proper instrument interface for a specific application.	K6
C05	Familiarize and experiment with DAQ and Signal Conditioning	K3

COURSE ARTICULATION MATRIX					
COs/POs	PO1	PO2	PO3	PO4	PO5
C01	3	-	3	2	1
C02	3	-	3	2	1
C03	3	-	2	2	2
C04	3	1	3	3	1
C05	3	1	3	3	2
<b>23PEOE22</b>	<b>3</b>	<b>1</b>	<b>3</b>	<b>2</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial					

ASSESSMENT PATTERN – THEORY							
Test / Bloom's Category*	Remembering (K1) %	Understanding (K2) %	Applying (K3) %	Analyzing (K4) %	Evaluating (K5) %	Creating (K6) %	Total %
CAT1	30	40	15	15	-	-	100
CAT2	15	10	25	30	20	-	100
Individual Assessment1/ Case study1/ Seminar 1/Project1	10	10	20	30	20	10	100
Individual Assessment2/ Case study2/ Seminar 2 /Project2	25	40	20	15	-	-	100
ESE	30	25	15	20	5	5	100



<b>23PEOE23</b>	<b>ENERGY MANAGEMENT SYSTEMS</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	OE	3	0	0	3

<b>Course Objective</b>	• To Comprehend energy management schemes, perform energy audit and execute economic analysis and load management in electrical systems.	
<b>UNIT – I</b>	<b>GENERAL ASPECTS OF ENERGY AUDIT AND MANAGEMENT</b>	<b>9 Periods</b>
Energy Conservation Act 2001 and policies – Eight National Missions - Basics of Energy and its forms (Thermal and Electrical) - Energy Management and Audit - Energy Managers and Auditors - Types and Methodology Audit Report - Material and energy balance diagrams - .Energy Monitoring and Targeting.		
<b>UNIT – II</b>	<b>STUDY OF BOILERS, FURNACES AND COGENERATION</b>	<b>9 Periods</b>
Boiler Systems - Types - Performance Evaluation of boilers - Energy Conservation Opportunity - Steam Distribution - Efficient Steam Utilisation - Furnaces:types and classification - Performance evaluation of a typical fuel fired furnace. Cogeneration: Need - Principle - Technical options - classification - Technical parameters and factors influencing cogeneration choice - Prime Movers - Trigeration.		
<b>UNIT – III</b>	<b>ENERGY STUDY OF ELECTRICAL SYSTEMS</b>	<b>9 Periods</b>
Electricity Billing – Electricity load management - Maximum Demand Control - Power Factor improvement and its benefits - pf controllers - capacitors - Energy efficient transformers and Induction motors - rewinding and other factors influencing energy efficiency - Standards and labeling programme of distribution transformers and IM - Analysis of distribution losses - demand side management - harmonics - filters - VFD and its selection.		
<b>UNIT – IV</b>	<b>STUDY OF ELECTRICAL UTILITIES</b>	<b>9 Periods</b>
Compressor types - Performance - Air system components - Efficient operation of compressed air systems- Compressor capacity assessment - HVAC: psychrometrics and air-conditioning processes - Types of refrigeration system - Compressor types and applications - Performance assessment of refrigeration plants - Lighting Systems: Energy efficient lighting controls - design of interior lighting - Case study.		
<b>UNIT – V</b>	<b>PERFORMANCE ASSESSMENT FOR EQUIPMENT</b>	<b>9 Periods</b>
Performing Financial analysis: Fixed and variable costs – Payback period – ROI - methods – factors affecting analysis. Energy Performance Assessment: Heat exchangers - Fans and Blowers - Pumps. Energy Conservation in buildings and ECBC.		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>		

#### REFERENCES:

1	Murphy W.R. and G.Mckay Butter worth , <b>“Energy Management”</b> , Heinemann Publications, 2007
2	Albert Thumann, Terry Niehus, William J. Younger, <b>“Handbook of Energy Audits”</b> , Ninth Edition, River Publishers, 2012.
3	Dr. Subhash Gadhave Anup Goel Siddu S. Laxmikant D. Jathar, <b>“Energy Audit &amp; Management”</b> , Second edition, Technical Publications, 2019.
4	S. M. Chaudhari, S. A. Asarkar, M. A. Chaudhari, <b>“Energy Conservation and Audit”</b> , Second Edition, Nirali Prakashan Publications, 2021.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Analyze the feature of energy audit methodology and documentation of report.	K3
C02	Perform action plan and financial analysis	K4
C03	Familiarize with thermal utilities.	K4
C04	Familiarize with electrical utilities.	K4
C05	Perform assessment of different systems.	K5

<b>COURSE ARTICULATION MATRIX</b>					
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>
C01	3	2	2	1	1
C02	3	2	2	1	1
C03	3	2	2	1	1
C04	3	2	2	1	1
C05	3	2	2	1	1
<b>23PEOE23</b>	<b>3</b>	<b>2</b>	<b>2</b>	<b>1</b>	<b>1</b>
1 - Slight, 2 - Moderate, 3 - Substantial					

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	10	30	30	20	10	-	100
CAT2	10	30	30	20	10	-	100
Individual Assessment1 / Case study1/ Seminar 1/Project1	-	30	30	20	20	-	100
Individual Assessment2 / Case study2/ Seminar 2 /Project2	-	30	30	20	20	-	100
ESE	10	30	30	20	10	-	100

<b>23PEOE24</b>	<b>ADVANCED ENERGY STORAGE TECHNOLOGY</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
NIL	OE	3	0	0	3

<b>Course Objective</b>	• To explore the fundamentals, technologies and applications of energy storage	
<b>UNIT – I</b>	<b>ENERGY STORAGE: HISTORICAL PERSPECTIVE, INTRODUCTION AND CHANGES</b>	<b>9 Periods</b>
Storage Needs- Variations in Energy Demand- Variations in Energy Supply- Interruptions in Energy Supply- Transmission Congestion - Demand for Portable Energy-Demand and scale requirements Environmental and sustainability issues-conventional energy storage methods: battery-types.		
<b>UNIT – II</b>	<b>TECHNICAL METHODS OF STORAGE</b>	<b>9 Periods</b>
Introduction: Energy and Energy Transformations, Potential energy (pumped hydro, compressed air, springs)- Kinetic energy (mechanical flywheels)- Thermal energy without phase change passive (adobe) and active (water)-Thermal energy with phase change (ice, molten salts, steam)- Chemical energy (hydrogen, methane, gasoline, coal, oil)- Electrochemical energy (batteries, fuel cells)- Electrostatic energy (capacitors), Electromagnetic energy (superconducting magnets)- Different Types of Energy Storage Systems.		
<b>UNIT – III</b>	<b>PERFORMANCE FACTORS OF ENERGY STORAGE SYSTEMS</b>	<b>9 Periods</b>
Energy capture rate and efficiency- Discharge rate and efficiency- Dispatch ability and load flowing characteristics, scale flexibility, durability – Cycle lifetime, mass and safety – Risks of fire, explosion, toxicity- Ease of materials, recycling and recovery- Environmental consideration and recycling , Merits and demerits of different types of Storage.		
<b>UNIT – IV</b>	<b>APPLICATION CONSIDERATION</b>	<b>9 Periods</b>
Comparing Storage Technologies- Technology options- Performance factors and metrics- Efficiency of Energy Systems- Energy Recovery - Battery Storage System: Introduction with focus on Lead Acid and Lithium- Chemistry of Battery Operation, Power storage calculations, Reversible reactions, Charging patterns, Battery Management systems, System Performance, Areas of Application of Energy Storage: Waste heat recovery, Solar energy storage, Green house heating, Power plant applications, Drying and heating for process industries, energy storage in automotive applications in hybrid and electric vehicles.		
<b>UNIT – V</b>	<b>HYDROGEN FUEL CELLS AND FLOW BATTERIES</b>	<b>9 Periods</b>
Hydrogen Economy and Generation Techniques, Storage of Hydrogen, Energy generation - Super capacitors: properties, power calculations – Operation and Design methods - Hybrid Energy Storage: Managing peak and Continuous power needs, options - Level 1: (Hybrid Power generation) Bacitor “Battery + Capacitor” Combinations: need, operation and Merits; Level 2: (Hybrid Power Generation) Bacitor + Fuel Cell or Flow Battery operation-Applications: Storage for Hybrid Electric Vehicles, Regenerative Power, capturing methods.		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>		

## REFERENCES :

1	DetlefStolten, <i>"Hydrogen and Fuel Cells: Fundamentals, Technologies and Applications"</i> , Wiley, 2010.
2	Jiujun Zhang, Lei Zhang, Hansan Liu, Andy Sun, Ru-Shi Liu, <i>"Electrochemical Technologies for Energy Storage and Conversion"</i> , John Wiley and Sons, 2012.
3	Francois Beguin and ElzbietaFrackowiak, <i>"Super capacitors"</i> , Wiley, 2013.
4	Doughty Liaw, Narayan and Srinivasan, <i>"Batteries for Renewable Energy Storage"</i> , The Electrochemical Society, New Jersey, 2010.

COURSE OUTCOMES:		Bloom's Taxonomy Mapped
Upon completion of the course, the students will be able to:		
C01	Recollect the historical perspective and technical methods of energy storage.	K1
C02	Explain the basics of different storage methods.	K2
C03	Determine the performance factors of energy storage systems.	K2
C04	Identify applications for renewable energy systems.	K4
C05	Outline the basics of Hydrogen cell and flow batteries.	K2

COURSE ARTICULATION MATRIX					
COs/POs	PO1	PO2	PO3	PO4	PO5
C01	3	1	3	3	3
C02	3	1	3	3	3
C03	3	1	3	3	3
C04	3	1	3	3	3
C05	3	1	3	3	3
<b>23PEOE24</b>	<b>3</b>	<b>1</b>	<b>3</b>	<b>3</b>	<b>3</b>
1 – Slight, 2 – Moderate, 3 – Substantial					

ASSESSMENT PATTERN – THEORY							
Test / Bloom's Category*	Remembering (K1) %	Understanding (K2) %	Applying (K3) %	Analyzing (K4) %	Evaluating (K5) %	Creating (K6) %	Total %
CAT1	10	30	30	20	10	-	100
CAT2	10	30	30	20	10	-	100
Individual Assessment1/ Case study1/ Seminar 1/ Project1	-	30	30	20	10	10	100
Individual Assessment2/ Case study2/ Seminar 2 / Project2	-	30	30	20	20	-	100
ESE	10	30	30	20	10	-	100

<b>23AEOE25</b>	<b>DESIGN OF DIGITAL SYSTEMS</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>OE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	
<ul style="list-style-type: none"> <li>To gain knowledge in the design and VHDL programming of synchronous and asynchronous sequential circuits, PLD's and the basic concepts of testing in VLSI circuits</li> </ul>	
<b>UNIT-I SYNCHRONOUS SEQUENTIAL CIRCUIT DESIGN</b>	<b>9 Periods</b>
Analysis of Clocked Synchronous Sequential Circuits - Modeling, state table reduction, state assignment, Design of Synchronous Sequential circuits, Design of iterative circuits- ASM chart –ASM realization.	
<b>UNIT-II ASYNCHRONOUS SEQUENTIAL CIRCUIT DESIGN</b>	<b>9 Periods</b>
Analysis of Asynchronous Sequential Circuits - Races in ASC – Primitive Flow Table - Flow Table Reduction Techniques, State Assignment Problem and the Transition Table – Design of ASC – Static and Dynamic Hazards – Essential Hazards– Data Synchronizers.	
<b>UNIT-III SYSTEM DESIGN USING PLDS</b>	<b>9 Periods</b>
Basic concepts – Programming Technologies - Programmable Logic Element (PLE) – Programmable Array Logic (PLA)-Programmable Array Logic (PAL) –Design of combinational and sequential circuits using PLDs– Complex PLDs (CPLDs).	
<b>UNIT- IV INTRODUCTION TO VHDL</b>	<b>9 Periods</b>
Design flow -Software tools – VHDL: Data Objects-Data types – Operators –Entities and Architectures – Components and Configurations – Signal Assignment – Concurrent and Sequential statements --Behavioral, Dataflow and Structural modeling– Transport and Inertial delays –Delta delays-Attributes - Generics– Packages and Libraries.	
<b>UNIT-V LOGIC CIRCUIT TESTING AND TESTABLE DESIGN</b>	<b>9 Periods</b>
Digital logic circuit testing - Fault models - Combinational logic circuit testing - Sequential logic circuit testing-Design for Testability - Built-in Self-test, Board and System Level Boundary Scan - Case Study: Traffic Light Controller.	
<b>Contact Periods:</b>	
<b>Lecture:45Periods      Tutorial:0Periods      Practical: 0Periods      Total: 45Periods</b>	

#### REFERENCES:

1	Donald G.Givone, " <b>Digital principles and Design</b> ", TataMcGrawHill, 2002.
2	Nelson, V.P., Nagale, H.T., Carroll, B.D., and Irwin, J.D., " <b>Digital Logic Circuit Analysis and Design</b> ", Prentice Hall International, Inc., NewJersey, 1995.
3	VolneiA.Pedroni, " <b>Circuit Design withVHDL</b> ",PHILearning,2011.
4	ParagK Lala, " <b>Digital Circuit Testing and Testability</b> ",AcademicPress,1997.
5	CharlesHRoth, " <b>Digital Systems Design Using VHDL</b> ",Cencage2ndEdition2012.
6	NripendraN.Biswas, " <b>Logic Design Theory</b> "PrenticeHallofIndia,2001.

<b>COURSEOUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course ,students will be able to/have:		
C01	To design synchronous sequential circuits based on specifications.	K3
C02	To design asynchronous sequential circuits based on specifications	K3
C03	Ability to illustrate digital design implementation using PLDs.	K2
C04	To develop algorithm and VHDL code for design of digital circuits.	K3
C05	Understand the different testing methods for combinational and sequential circuits.	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
C01	3	-	2	-	-	1
C02	3	-	2	-	-	1
C03	3	-	2	-	-	1
C04	3	-	2	-	-	1
C05	3	-	2	-	-	1
<b>23AEOE25</b>	<b>3</b>	<b>-</b>	<b>2</b>	<b>-</b>	<b>-</b>	<b>1</b>

1 – Slight, 2 – Moderate, 3 – Substantial

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	40%	40%	20%				100%
CAT2	40%	40%	20%				100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1		50%	50%				100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2		50%	50%				100%
ESE	20%	45%	35%				100%

23AEOE26	<b>BASICS OF NANO ELECTRONICS</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	OE	3	0	0	3

#### Course Objective

- The students will be able to acquire knowledge about nano device fabrication technology, nano structures, nano technology for memory devices and applications of nano electronics in data transmission.

<b>UNIT - I TECHNOLOGY AND ANALYSIS</b>	<b>9 Periods</b>
Fundamentals : Dielectric, Ferroelectric and Optical properties - Film Deposition Methods - Lithography Material removing techniques - Etching and Chemical Mechanical Polishing - Scanning Probe Techniques.	
<b>UNIT - II CARBON NANO STRUCTURES</b>	<b>9 Periods</b>
Principles and concepts of Carbon Nano tubes - Fabrication - Electrical, Mechanical and Vibration Properties - Applications of Carbon Nano tubes.	
<b>UNIT - III LOGIC DEVICES</b>	<b>9 Periods</b>
Silicon MOSFET's: Novel materials and alternative concepts - Single electron devices for logic applications - Super conductor digital electronics - Carbon Nano tubes for data processing.	
<b>UNIT - IV MEMORY DEVICES AND MASS STORAGE DEVICES</b>	<b>9 Periods</b>
Flash memories - Capacitor based Random Access Memories - Magnetic Random Access Memories - Information storage based on phase change materials - Resistive Random Access Memories - Holographic Data storage.	
<b>UNIT - V DATA TRANSMISSION AND INTERFACING DISPLAYS</b>	<b>9 Periods</b>
Photonic Networks - RF and Microwave Communication System - Liquid Crystal Displays - Organic Light emitting diodes.	
<b>Contact Periods:</b> <b>Lecture: 45 Periods    Tutorial: 0 Periods    Practical: 0 Periods    Total: 45 Periods</b>	

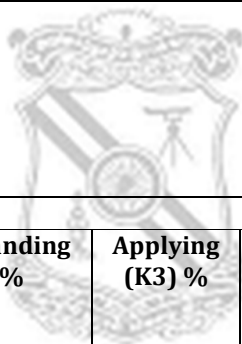
#### REFERENCES:

1	Rainer Waser, " <b>Nano Electronics and Information Technology, Advanced Electronic materials and novel devices</b> ", 3rd Edition, Wiley VCH, 2012.
2	T. Pradeep, " <b>Nano: The essentials</b> ", Tata McGraw Hill, 2007.
3	Charles Poole, " <b>Introduction to Nano Technology</b> ", Wiley Interscience, 2003
4	Vladimir V.Mitin, Viatcheslav A. Kochelap, Michael A. Stroscio, " <b>Introduction to Nano Electronics Science, Nanotechnology, Engineering and Applications</b> ", Cambridge University Press, 2011.
5	C.Wasshuber Simon, " <b>Simulation of Nano Structures Computational Single-Electronics</b> ", Springer, 2001.
6	Mark Reed and Takhee Lee, " <b>Molecular Nano Electronics, American Scientific Publisher, California</b> ", 2003.

<b>COURSE OUTCOMES:</b> Upon completion of the course, students will be able to/have:		<b>Bloom's Taxonomy Mapped</b>
C01	Explain principles of nano device fabrication technology.	K2
C02	Describe the concept of Nano tube and Nano structure.	K2
C03	Explain the function and application of various nano devices	K3
C04	Reproduce the concepts of advanced memory technologies.	K2
C05	Emphasize the need for data transmission and display systems.	K2

<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	-	2	-	-	1
C02	3	-	2	-	-	1
C03	3	-	2	-	-	1
C04	3	-	2	-	-	1
C05	3	-	2	-	-	1
<b>23AEOE26</b>	<b>3</b>	<b>-</b>	<b>2</b>	<b>-</b>	<b>-</b>	<b>1</b>

1 – Slight, 2 – Moderate, 3 – Substantial



<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	50%	25%	25%				100%
CAT2	50%	25%	25%				100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1	50%	25%	25%				100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2	50%	25%	25%				100%
ESE	50%	25%	25%				100%



<b>23AEOE27</b>	<b>ADVANCED PROCESSORS</b> (Common to all Branches)
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PRE REQUISITES	CATEGORY	L	T	P	C
NIL	OE	3	0	0	3

#### Course Objective

- The students will be able to acquire knowledge about the high performance RISC, CISC and special purpose processors.

<b>UNIT – I MICROPROCESSOR ARCHITECTURE</b>	<b>9 Periods</b>
Instruction set – Data formats – Instruction formats – Addressing modes – Memory hierarchy – registerfile – Cache – Virtual memory and paging – Segmentation – Pipelining – The instruction pipeline – pipeline hazards – Instruction level parallelism – reduced instruction set – Computer principles – RISCversus CISC – RISC properties – RISC evaluation.	
<b>UNIT – II HIGH PERFORMANCE CISC ARCHITECTURE –PENTIUM</b>	<b>9 Periods</b>
The software model – functional description – CPU pin descriptions – Addressing modes – Processor flags – Instruction set – Bus operations – Super scalar architecture – Pipe lining – Branch prediction – Theinstruction and caches – Floating point unit– Programming the Pentium processor.	
<b>UNIT – III HIGH PERFORMANCE CISC ARCHITECTURE – PENTIUM INTERFACE</b>	<b>9 Periods</b>
Protected mode operation – Segmentation – paging – Protection – multitasking – Exception and interrupts – Input /Output – Virtual 8086 model – Interrupt processing.	
<b>UNIT – IV HIGH PERFORMANCE RISC ARCHITECTURE: ARM</b>	<b>9 Periods</b>
ARM architecture – ARM assembly language program – ARM organization and implementation – ARM instruction set - Thumb instruction set.	
<b>UNIT – V SPECIAL PURPOSE PROCESSORS</b>	<b>9 Periods</b>
Altera Cyclone Processor – Audio codec – Video codec design – Platforms – General purpose processor –Digital signal processor – Embedded processor – Media Processor – Video signal Processor – Custom Hardware – Co-Processor.	
<b>Contact Periods:</b> <b>Lecture: 45 Periods    Tutorial: 0 Periods    Practical: 0 Periods    Total: 45 Periods</b>	

#### REFERENCES:

1	Daniel Tabak, “ <b>Advanced Microprocessors</b> ”, McGraw Hill Inc., 2011.
2	James L. Antonakos, “ <b>The Pentium Microprocessor</b> ”, Pearson Education, 1997.
3	Steve Furber, “ <b>ARM System –On –Chip architecture</b> ”, Addison Wesley, 2009.
4	Gene. H. Miller, “ <b>Micro Computer Engineering</b> ”, Pearson Education, 2003.
5	Barry. B. Brey, “ <b>The Intel Microprocessors Architecture, Programming and Interfacing</b> ”, PHI, 2008.
6	Valvano, “ <b>Embedded Microcomputer Systems</b> ” Cengage Learning India Pvt Ltd, 2011.
7	Iain E.G. Richardson, “ <b>Video codec design</b> ”, John Wiley & sons Ltd, U.K, 2002.

<b>COURSE OUTCOMES:</b> Upon completion of the course, students will be able to		<b>Bloom's Taxonomy Mapped</b>
C01	Describe the fundamentals of various processor architecture.	K2
C02	Interpret and understand the high performance features in CISC architecture.	K2
C03	Describe the concepts of Exception and interrupt processing.	K2
C04	Develop programming skill for ARM processor.	K3
C05	Explain various special purpose processor	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	3	-	2	-	-	1
C02	3	-	2	-	-	1
C03	3	-	2	-	-	1
C04	3	-	2	-	-	1
C05	3	-	2	-	-	1
<b>23AEOE27</b>	<b>3</b>	<b>-</b>	<b>2</b>	<b>-</b>	<b>-</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Rememberin g (K1) %</b>	<b>Understandin g (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzin g (K4) %</b>	<b>Evaluatin g (K5) %</b>	<b>Creatin g (K6) %</b>	<b>Total %</b>
CAT1	40%	40%	20%				100%
CAT2	40%	40%	20%				100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1		50%	50%				100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2		50%	50%				100%
ESE	30%	40%	30%				100%

<b>23VLOE28</b>	<b>HDL PROGRAMMING LANGUAGES</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	OE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To code and simulate any digital function in Verilog HDL and understand the difference between synthesizable and non-synthesizable codes.</li></ul>		
<b>UNIT – I</b>	<b>VERILOG INTRODUCTION AND MODELING</b>	<b>9 Periods</b>	
Introduction to Verilog HDL, Language Constructs and Conventions, Gate Level Modeling, Modeling at Dataflow Level, Behavioral Modeling, Switch Level Modeling, System Tasks, Functions and Compiler Directives.			
<b>UNIT – II</b>	<b>SEQUENTIAL MODELING AND TESTING</b>	<b>9 Periods</b>	
Sequential Models - Feedback Model, Capacitive Model, Implicit Model, Basic Memory Components, Functional Register, Static Machine Coding, Sequential Synthesis. Test Bench - Combinational Circuits Testing, Sequential Circuit Testing, Test Bench Techniques, Design Verification, Assertion Verification.			
<b>UNIT – III</b>	<b>SYSTEM VERILOG</b>	<b>9 Periods</b>	
Introduction, System Verilog declaration spaces, System Verilog Literal Values and Built-in Data Types, System Verilog User-Defined and Enumerated Types, system Verilog Arrays, Structures and Unions, system verilog Procedural Blocks, Tasks and Functions.			
<b>UNIT – IV</b>	<b>SYSTEM VERILOG MODELING</b>	<b>9 Periods</b>	
System Verilog Procedural Statements, Modeling Finite State Machines with System Verilog, System Verilog Design Hierarchy.			
<b>UNIT – V</b>	<b>INTERFACES AND DESIGN MODEL</b>	<b>9 Periods</b>	
System Verilog Interfaces, A Complete Design Modeled with System Verilog, Behavioral and Transaction Level Modeling.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial:0 Periods      Practical:0 Periods      Total: 45 Periods</b>			

#### REFERENCES:

1	T.R.Padmanabhan, B Bala Tripura Sundari, " <b>Design through Verilog HDL</b> ",Wiley 2009.
2	Stuart Sutherland, Simon Davidmann ,Peter Flake , Foreword by Phil Moorby, " <b>System Verilog For Design Second Edition A Guide to Using System Verilog for Hardware Design and Modelling</b> ", Springer 2006.
3	Samir Palnitkar, " <b>Verilog HDL</b> ", 2nd Edition, Pearson Education, 2009.
4	ZainalabdienNavabi, " <b>Verilog Digital System Design</b> ",TMH,2ndEdition,2005.
5	System Verilog 3.1a, Language Reference Manual, Accellera, 2004
6	Dr.SRamachandran, " <b>Digital VLSI Systems Design: A Design Manual for Implementation of Projects on FPGAs and ASICs Using Verilog</b> ", Springer, 2007.
7	Chris Spear, " <b>System verilog for verification a guide to learning the test bench Language Features</b> ", Springer 2006.
6	Stuart Sutherland, Simon Davidmann, Peter Flake, " <b>System Verilog For Design: A Guide to Using System Verilog for Hardware Design and Modeling</b> " 1st Edition, 2003

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
CO1	Explain the verilog coding and simulate any digital function using Verilog HDL	K2
CO2	Develop sequential modeling based Verilog HDL code and develop the test bench for the modeling	K3
CO3	Explain the system verilog modeling	K2
CO4	Differentiate the synthesizable and non-synthesizable code	K3
CO5	Apply good coding techniques on system verilog interfaces and complete design model	K3

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
CO1	3	3		2		2
CO2	3	3		2		2
CO3	3	3		2		2
CO4	3	3		2		2
CO5	3	3		2		2
<b>23VLOE28</b>	<b>3</b>	<b>3</b>		<b>2</b>		<b>2</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	40%	40%	20%	-	-	-	100%
CAT2	40%	40%	20%	-	-	-	100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1	-	50%	50%	-	-	-	100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2	-	50%	50%	-	-	-	100%
ESE	40%	40%	20%	-	-	-	100%

<b>23VLOE29</b>	<b>CMOS VLSI DESIGN</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
NIL	OE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To gain knowledge on CMOS Circuits with its characterization and to design CMOS logic and sub-system with low power</li></ul>		
<b>UNIT – I</b>	<b>INTRODUCTION TO MOS CIRCUITS</b>	<b>9 Periods</b>	
MOS Transistor Theory -Introduction MOS Device Design Equations -MOS Transistor as a Switches - Pass Transistor - CMOS Transmission Gate -Complementary CMOS Inverter - Static Load MOS Inverters - Inverters with NMOS loads - Differential Inverter - Tri State Inverter - BiCMOS Inverter.			
<b>UNIT – II</b>	<b>CIRCUIT CHARACTERIZATION AND PERFORMANCE ESTIMATION</b>	<b>9 Periods</b>	
Delay Estimation, Logical Effort and Transistor Sizing, Power Dissipation, Sizing Routing Conductors, Charge Sharing, Design Margin and Reliability.			
<b>UNIT – III</b>	<b>CMOS CIRCUIT AND LOGIC DESIGN</b>	<b>9 Periods</b>	
CMOS Logic Gate Design, Physical Design of CMOS Gate, Designing with Transmission Gates, CMOS Logic Structures, Clocking Strategies, I/O Structures.			
<b>UNIT – IV</b>	<b>CMOS SUBSYSTEM DESIGN</b>	<b>9 Periods</b>	
DataPath Operations-Addition/Subtraction, Parity Generators, Comparators, Zero/One Detectors, Binary Counters, ALUs, Multipliers, Shifters, Memory Elements, Control-FSM, Control Logic Implementation.			
<b>UNIT – V</b>	<b>LOW POWER CMOS VLSI DESIGN</b>	<b>9 Periods</b>	
Introduction to Low Power Design, Power Dissipation in FET Devices, Power Dissipation in CMOS, Low-Power Design through Voltage Scaling – VTCMOS Circuits, MTCMOS Circuits, Architectural Level Approach – Pipelining and Parallel Processing Approaches, Low Power Basics CMOS Gate and Adder Design.			
<b>Contact Periods:</b>			
<b>Lecture: 45 Periods      Tutorial:0 Periods      Practical:0 Periods      Total: 45 Periods</b>			

#### REFERENCES:

1	Sung Mo Kang, Yusuf Lablebici, "CMOS Digital Integrated Circuits: Analysis & Design", Tata McGraw Hill, 2011.
2	N.Weste and K.Eshraghian, "Principles of CMOS VLSI Design", Addison Wesley, 1998.
3	Neil H. E. Weste, David Harris, Ayan Banerjee, "CMOS VLSI Design: A Circuits and Systems Perspective", Pearson Education 2013.
4	Kiat-Seng Yeo, Kaushik Roy, "Low-Voltage, Low-Power VLSI Subsystems", McGraw-Hill Professional, 2004.
5	Gary K.Yeap, "Practical Low Power Digital VLSI Design", Kluwer Academic Press, 2002.
6	Jan M .Rabaey, "Digital Integrated Circuits: A Design Perspective", Pearson Education, 2003.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Explain the MOS circuits and Transmission gates	K2
C02	Illustrate the CMOS Circuits with its characterization	K2
C03	Design CMOS logic circuits	K3
C04	Design CMOS sub-system	K3
C05	Discuss low power CMOS VLSI Design	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
CO1	2	1	-	2	-	3
CO2	2	1	-	2	-	3
CO3	2	1	-	2	-	3
CO4	3	1	-	2	-	3
CO5	3	1	-	2	-	3
<b>23VLOE29</b>	<b>3</b>	<b>1</b>	<b>-</b>	<b>2</b>	<b>-</b>	<b>3</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	40%	40%	20%	-	-	-	100%
CAT2	40%	40%	20%	-	-	-	100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1	-	50%	50%	-	-	-	100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2	-	50%	50%	-	-	-	100%
ESE	40%	40%	20%	-	-	-	100%

<b>23VLOE30</b>	<b>HIGH LEVEL SYNTHESIS</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	OE	3	0	0	3

<b>Course Objective</b>	<ul style="list-style-type: none"><li>To provide students with foundations in High level synthesis, verification and CAD Tools</li></ul>	
<b>UNIT – I</b>	<b>HIGH-LEVEL SYNTHESIS (HLS) FUNDAMENTALS</b>	<b>9 Periods</b>
Overview HLS flow, Scheduling Techniques, Resource sharing and Binding Techniques, Data-path and Controller Generation Techniques.		
<b>UNIT – II</b>	<b>HIGH LEVEL SYNTHESIS</b>	<b>9 Periods</b>
Introduction to HDL, HDL to DFG, operation scheduling: constrained and unconstrained scheduling, ASAP, ALAP, List scheduling, Force directed Scheduling, operator binding, Static Timing Analysis: Delay models, setup time, hold time, cycle time, critical paths, Topological mvns. Logical timing analysis, False paths, Arrival time (AT), Required arrival Time (RAT), Slacks.		
<b>UNIT – III</b>	<b>HIGH-LEVEL SYNTHESIS VERIFICATION</b>	<b>9 Periods</b>
Simulation based verification - Formal Verification of digital systems- BDD based approaches, functional equivalence, finite state automata, $\omega$ -automata, FSM verification.		
<b>UNIT – IV</b>	<b>CAD TOOLS FOR SYNTHESIS</b>	<b>9 Periods</b>
CAD tools for synthesis, optimization, simulation and verification of design at various levels as well as for special realizations and structures such as microprogrammes, PLAs, gate arrays etc. Technology mapping for FPGAs. Low power issues in high level synthesis and logic synthesis.		
<b>UNIT – V</b>	<b>ADVANCED TOPICS</b>	<b>9 Periods</b>
Relative Scheduling, IO scheduling modes - cycle fixed scheduling modes, super-fixed scheduling modes, free-floating scheduling mode, Pipelining, Handshaking, System Design, High-Level Synthesis for FPGA.		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>		

#### REFERENCES :

1	Philippe Coussy and Adam Morawiec, " <b>High-level Synthesis from Algorithm to Digital Circuit</b> ", Springer, 2008.
2	Sherwani, N., " <b>Algorithms for VLSI Physical Design Automation</b> ", Springer, 3rd ed., 2005.
3	D. Micheli, " <b>Synthesis and optimization of digital systems</b> ", Mc Graw Hill, 2005.
4	Dutt, N. D. and Gajski, D. D., " <b>High level synthesis</b> ", Kluwer, 2000.
5	Gerez S.H., " <b>Algorithms for VLSI Design Automation</b> ", John Wiley (1998)
6	David. C. Ku and G. De Micheli, " <b>High-level Synthesis of ASICs Under Timing and Synchronization Constraints</b> ", Kluwer Academic Publishers, 1992.
7	K. Parhi, " <b>VLSI Digital Signal Processing Systems: Design and Implementation</b> ", Jan 1999, Wiley.
8	Egon Boerger and Robert Staerk " <b>Abstract State Machines: A Method for High-Level System Design and Analysis</b> ", Springer, 2006.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
CO1	Understand the fundamentals of High level synthesis	K2
CO2	Synthesis the HDL for operation scheduling	K2
CO3	Simulate and verify any digital systems	K2
CO4	Apply CAD tools for synthesis	K2
CO5	Have knowledge on various scheduling modes	K2

**COURSE ARTICULATION MATRIX :**

<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
CO1	2	2	-	2	2	-
CO2	2	2	-	2	2	-
CO3	2	2	-	2	2	-
CO4	2	2	-	2	2	-
CO5	2	2	-	2	2	-
<b>23VLOE30</b>	<b>2</b>	<b>2</b>	<b>-</b>	<b>2</b>	<b>2</b>	<b>-</b>

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	50%	50%	-	-	-	-	100%
CAT2	50%	50%	-	-	-	-	100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1	-	50%	50%	-	-	-	100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2	-	50%	50%	-	-	-	100%
ESE	50%	50%	-	-	-	-	100%



<b>23CSOE31</b>	<b>ARTIFICIAL INTELLIGENCE</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>OE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	• Identify and apply AI techniques in the design of systems that act intelligently, making automatic decisions and learn from experience.	
<b>UNIT – I</b>	<b>SEARCH STRATEGIES</b>	<b>9 Periods</b>
Uninformed Strategies – BFS, DFS, Djisktra, Informed Strategies – A* search, Heuristic functions, Hill Climbing, Adversarial Search – Min-max algorithm, Alpha-beta Pruning		
<b>UNIT – II</b>	<b>PLANNING AND REASONING</b>	<b>9 Periods</b>
State Space search, Planning Graphs, Partial order planning, Uncertain Reasoning – Probabilistic Reasoning, Bayesian Networks, Dempster Shafer Theory, Fuzzy logic		
<b>UNIT – III</b>	<b>PROBABILISTIC REASONING</b>	<b>9 Periods</b>
Probabilistic Reasoning over Time - Hidden Markov Models, Kalman Filters, Dynamic Bayesian Networks. Knowledge Representations – Ontological Engineering, Semantic Networks and description logics.		
<b>UNIT – IV</b>	<b>DECISION MAKING</b>	<b>9 Periods</b>
Utility Theory, Utility Functions, Decision Networks – Sequential Decision Problems – Partially Observable MDPs – Game Theory.		
<b>UNIT – V</b>	<b>REINFORCEMENT LEARNING</b>	<b>9 Periods</b>
Reinforcement Learning - Passive and active reinforcement learning - Generations in Reinforcement Learning - Policy Search – Deep Reinforcement Learning.		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods   Tutorial: 0 Periods   Practical: 0 Periods   Total: 45 Periods</b>		

#### REFERENCES :

1	Deepak Khemani, <i>"A First Course in Artificial Intelligence"</i> , Tata Mc Graw Hill Education 2013
2	Yang Q, <i>"Intelligent Planning: A decomposition and Abstraction based Approach"</i> , Springer, 2006
3	Russell and Norvig, <i>"Artificial Intelligence, A Modern Approach"</i> , 3rd edition, Pearson Prentice Hall, 2010.
4	Elaine Rich, Kevin Knight, Shivashankar B. Nair, <i>"Artificial Intelligence"</i> , 3rd edition, TataMcGraw Hill, 2009.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Use search techniques to solve AI problems	K2
C02	Reason facts by constructing plans and understand uncertainty efficiently.	K3
C03	Examine data using statistical codes and solve complex AI problems	K6
C04	Apply techniques to make apt decisions.	K4
C05	Use deep reinforcement learning to solve complex AI problems	K6

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/ POs</b>	<b>PO 1</b>	<b>PO2</b>	<b>PO 3</b>	<b>PO 4</b>	<b>PO5</b>	<b>PO6</b>
C01	3		2		3	3
C02	3		2		3	3
C03	3		3		3	3
C04	3		3		3	3
C05	3		3		3	3
<b>23CSOE31</b>	<b>3</b>		<b>3</b>		<b>3</b>	<b>3</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1		20	40	20	20		100
CAT2		10	20	40	10	20	100
Individual Assessment 1/ Case study 1/ Seminar 1/ Project 1					50	50	100
Individual Assessment 2/ Case study 2/ Seminar 2/ Project 2					50	50	100
<b>ESE</b>	30	30	40				100

<b>23CSOE32</b>	<b>COMPUTER NETWORK MANAGEMENT</b> (Common to all Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>OE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	<ul style="list-style-type: none"><li>After the completion of the course, the students will be able to understand the concept of layering in networks, functions of protocols of each layer of TCP/IP protocol suite, concepts related to network addressing and routing and build simple LANs, perform basic configurations for routers and switches, and implement IPv4 and IPv6 addressing schemes using Cisco Packet Tracer.</li></ul>	
<b>UNIT – I</b>	<b>INTRODUCTION AND APPLICATION LAYER</b>	<b>9 Periods</b>
Building network – Network Edge and Core – Layered Architecture – OSI Model – Internet Architecture (TCP/IP) Networking Devices: Hubs, Bridges, Switches, Routers, and Gateways – Performance Metrics - Ethernet Networking – Introduction to Sockets – Application Layer protocols – HTTP – FTP Email Protocols – DNS.		
<b>UNIT – II</b>	<b>TRANSPORT LAYER AND ROUTING</b>	<b>9 Periods</b>
Transport Layer functions –User Datagram Protocol – Transmission Control Protocol – Flow Control – Retransmission Strategies – Congestion Control - Routing Principles – Distance Vector Routing – Link State Routing – RIP – OSPF – BGP – Introduction to Quality of Service (QoS).Case Study: Configuring RIP, OSPF BGP using Packet tracer		
<b>UNIT – III</b>	<b>NETWORK LAYER</b>	<b>9 Periods</b>
Network Layer: Switching concepts – Internet Protocol – IPV4 Packet Format – IP Addressing – Subnetting – Classless Inter Domain Routing (CIDR) – Variable Length Subnet Mask (VLSM) – DHCP – ARP – Network Address Translation (NAT) – ICMP – Concept of SDN.Case Study: Configuring VLAN, DHCP, NAT using Packet tracer		
<b>UNIT – IV</b>	<b>INTERNETWORK MANAGEMENT</b>	<b>9 Periods</b>
Introduction to the Cisco IOS - Router User Interface – CLI - Router and Switch Administrative Functions - Router Interfaces - Viewing, Saving, and Erasing Configurations - Switching Services - Configuring Switches - Managing Configuration Registers - Backing Up and Restoring IOS - Backing Up and Restoring the Configuration - Using Discovery Protocol (CDP) - Checking Network Connectivity		
<b>UNIT – V</b>	<b>TRAFFIC MANAGEMENT AND WAN PROTOCOLS</b>	<b>9 Periods</b>
Managing Traffic with Access Lists: Introduction to Access Lists - Standard Access Lists - Extended Access Lists - Named Access Lists - Monitoring Access Lists - Wide Area Networking Protocols: Introduction to Wide Area Networks - Cabling the Wide Area Network - High-Level Data-Link Control (HDLC) Protocol - Point-to-Point Protocol (PPP) - Frame Relay: Frame Relay Implementation and Monitoring - Integrated Services Digital Network (ISDN) - Dial-on-Demand Routing (DDR): Configuring DDR		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 45 Periods</b>		

#### REFERENCES :

1	James F. Kurose, Keith W. Ross, <i>“Computer Networking: A Top-Down Approach”, Seventh Edition, Pearson Education, 2017.</i>
2	William Stallings, <i>“Data and Computer Communications”, Tenth Edition, Pearson Education, 2014</i>
3	Larry L. Peterson, Bruce S. Davie, <i>“Computer Networks: A Systems Approach”, Fifth Edition, Morgan Kaufmann Publishers Inc., 2011.</i>
4	Todd Lammle, <i>“CCNA™: Cisco® Certified Network Associate Study Guide”, 5th Edition, Sybex, 2003</i>
5	Ying-Dar Lin, Ren-Hung Hwang, Fred Baker, <i>“Computer Networks: An Open Source Approach”,</i>

	McGraw Hill, 2012.
6	Ron Gilster, Jeff Biennu, and Kevin Ullstad, "CCNA for Dummies", IDG Books Worldwide, 2000

COURSE OUTCOMES:		Bloom's Taxonomy Mapped
Upon completion of the course, the students will be able to:		
CO1	Highlight the significance of the functions of each layer in the network.	K1
CO2	Identify the devices and protocols to design a network and implement it.	K4
CO3	Apply addressing principles such as subnetting and VLSM for efficient routing.	K3
CO4	Build simple LANs, perform basic configurations for routers and switches	K6
CO5	Illustrate various WAN protocols	K2

COURSE ARTICULATION MATRIX						
COs/POs	P01	P02	P03	P04	P05	P06
CO1	3		3		2	1
CO2	3		3		2	2
CO3	3		3		3	2
CO4	3		3		3	3
CO5	3		3		3	3
<b>23CSOE32</b>	<b>3</b>		<b>3</b>		<b>3</b>	<b>2</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

ASSESSMENT PATTERN - THEORY							
Test / Bloom's Category*	Remembering (K1) %	Understanding (K2) %	Applying (K3) %	Analyzing (K4) %	Evaluating (K5) %	Creating (K6) %	Total %
CAT1	30	30	20	20			100
CAT2		30	20	30	10	10	100
Individual Assessment 1 / Case Study 1 / Seminar 1 / Project 1	10	30	20	20	20		100
Individual Assessment 2 / Case Study 2 / Seminar 2 / Project 2		20	20	20	20	20	100
<b>ESE</b>	20	40	40				100

<b>23CSOE33</b>	<b>BLOCKCHAIN TECHNOLOGIES</b> (Common to all Branches)
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<b>PRE REQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>OE</b>	<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

<b>Course Objective</b>	• The objective of the course is to explore basics of block chain technology and its application in various domain	
<b>UNIT – I</b>	<b>INTRODUCTION OF CRYPTOGRAPHY AND BLOCKCHAIN</b>	<b>9 Periods</b>
History of Blockchain - Types of blockchain- CAP theorem and blockchain – benefits and Limitations of Blockchain – Decentralization using blockchain – Blockchain implementations- Block chain in practical use - Legal and Governance Use Cases		
<b>UNIT – II</b>	<b>BITCOIN AND CRYPTOCURRENCY</b>	<b>9 Periods</b>
Introduction to Bitcoin, The Bitcoin Network, The Bitcoin Mining Process, Mining Developments, Bitcoin Wallets, Decentralization and Hard Forks, Ethereum Virtual Machine (EVM), Merkle Tree, Double-Spend Problem, Blockchain and Digital Currency, Transactional Blocks, Impact of Blockchain Technology on Cryptocurrency		
<b>UNIT – III</b>	<b>ETHEREUM</b>	<b>9 Periods</b>
Introduction to Ethereum, Consensus Mechanisms, Metamask Setup, Ethereum Accounts, , Transactions, Receiving Ethers, Smart Contracts		
<b>UNIT – IV</b>	<b>HYPERLEDGER AND SOLIDITY PROGRAMMING</b>	<b>9 Periods</b>
Introduction to Hyperledger, Distributed Ledger Technology & its Challenges, Hyperledger & Distributed Ledger Technology, Hyperledger Fabric, Hyperledger Composer. Solidity – Programming with solidity		
<b>UNIT – V</b>	<b>BLOCKCHAIN APPLICATIONS</b>	<b>9 Periods</b>
Ten Steps to build your Blockchain application – Application: Internet of Things, Medical Record Management System, Domain Name Service and Future of Blockchain, Alt Coins		
<b>Contact Periods:</b>		
<b>Lecture: 45 Periods</b>	<b>Tutorial: 0 Periods</b>	<b>Practical: 0 Periods      Total: 45 Periods</b>

#### REFERENCES:

1	Imran Bashir, <b>“Mastering Blockchain: Distributed Ledger Technology, Decentralization, and Smart Contracts Explained”</b> , Second Edition, Packt Publishing, 2018.
2	Joseph J. Bambara Paul R. Allen, <b>“Blockchain A Practical Guide to Developing Business, Law, and Technology Solutions”</b> , McGraw Hill Education ,2018.
3	Narayanan, J. Bonneau, E. Felten, A. Miller, S. Goldfeder, <b>“Bitcoin and Cryptocurrency Technologies: A Comprehensive Introduction”</b> Princeton University Press, 2016.
4	Manav Gupta <b>“Blockchain for Dummies”</b> , IBM Limited Edition 2017.
5	Antonopoulos and G. Wood, <b>“Mastering Ethereum: Building Smart Contracts and Dapps”</b> , O’Reilly Publishing, 2018
6	<b>NPTEL Course : Blockchain and its applications</b> <a href="https://archive.nptel.ac.in/courses/106/105/106105235/">https://archive.nptel.ac.in/courses/106/105/106105235/</a>

<b>COURSE OUTCOMES:</b> Upon completion of the course, the students will be able to:		<b>Bloom's Taxonomy Mapped</b>
CO1	Comprehend the working of Blockchain technology	K2
CO2	Narrate working principle of smart contracts and create them using solidity for given scenario.	K3
CO3	Comprehend the working of Hyperledger in an real time application	K2
CO4	Apply the learning of solidity to build de-centralized apps on Ethereum	K3
CO5	Develop applications on Blockchain	K3

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	2		3	2		3
C02	2	3	3	3	2	3
C03	3		3	2		3
C04	3	3	3	3	2	3
C05	3	3	3	3	2	3
<b>23CSOE33</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>2</b>	<b>3</b>
<b>1 – Slight, 2 – Moderate, 3 – Substantial</b>						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20	40	40				100
CAT2	20	30	50				100
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1		30	70				100
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2		40	60				100
ESE	10	60	30				100

<b>23AEACZ1</b>	<b>ENGLISH FOR RESEARCH PAPER WRITING</b> (Common to All Branches)
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<b>PREREQUISITES</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>AC</b>	<b>2</b>	<b>0</b>	<b>0</b>	<b>0</b>

<b>Course Objective</b>	• The objective of the course is to make the learners understand the format and intricacies involved in writing a research paper.	
<b>UNIT – I</b>	<b>PLANNING AND PREPARATION</b>	<b>6 Periods</b>
Need for publishing articles, Choosing the journal, Identifying a model journal paper, Creation of files for each section, Expectations of Referees, Online Resources.		
<b>UNIT – II</b>	<b>SENTENCES AND PARAGRAPHS</b>	<b>6 Periods</b>
Basic word in English, Word order in English and Vernacular, placing nouns, Verbs, Adjectives, and Adverb suitably in a sentence, Using Short Sentences, Discourse Markers and Punctuations- Structure of a Paragraph, Breaking up lengthy Paragraphs.		
<b>UNIT – III</b>	<b>ACCURACY, BREVITY AND CLARITY (ABC) OF WRITING</b>	<b>6 Periods</b>
Accuracy, Brevity and Clarity in Writing, Reducing the linking words, Avoiding redundancy, Appropriate use of Relative and Reflexive Pronouns, Monologophobia, verifying the journal style, Logical Connections between others author’s findings and yours.		
<b>UNIT – IV</b>	<b>HIGHLIGHTING FINDINGS, HEDGING AND PARAPHRASING</b>	<b>6 Periods</b>
Making your findings stand out, Using bullet points headings, Tables and Graphs- Availing non-experts opinions, Hedging, Toning Down Verbs, Adjectives, Not over hedging, Limitations of your research.		
<b>UNIT – V</b>	<b>SECTIONS OF A PAPER</b>	<b>6 Periods</b>
Titles, Abstracts, Introduction, Review of Literature, Methods, Results, Discussion, Conclusions, References.		
<b>Contact Periods:</b>		
<b>Lecture: 30 Periods    Tutorial: 0 Periods    Practical: 0 Periods    Total: 30 Periods</b>		

#### REFERENCES :

1	Port R, "Writing for Science", Yale University Press (available on GoogleBooks), 2006
2	Day R, <b>How to Write and Publish a Scientific Paper</b> , Cambridge University Press, 2006.
3	Highman N, "Handbook of Writing for the Mathematical Sciences", SIAM. Highman's book, 1998.
4	Adrian Wallwork, "English for Writing Research Papers", Springer New York Dordrecht Heidelberg London, 2011.

<b>COURSE OUTCOMES :</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of this course the learners will be able to		
C01	Understand the need for writing good research paper.	K2
C02	Practice the appropriate word order, sentence structure and paragraph writing.	K4
C03	Practice unambiguous writing.	K3
C04	Avoid wordiness in writing.	K2
C05	Exercise the elements involved in writing journal paper.	K3

<b>COURSE ARTICULATION MATRIX :</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
C01	3	3	1	1	1	1
C02	3	3	1	1	1	1
C03	3	3	1	1	1	1
C04	3	3	1	1	1	1
C05	3	3	1	1	1	1
<b>23AEACZ1</b>	<b>3</b>	<b>3</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	40	40	20	-	-	-	100
CAT2	40	40	20	-	-	-	100
Individual Assessment 1/ Case Study 1/ Seminar 1/ Project 1	-	50	50	-	-	-	100
Individual Assessment 2/ Case Study 2/ Seminar 2/ Project 2	-	50	50	-	-	-	100
ESE	30	30	40	-	-	-	100



<b>23AEACZ2</b>	<b>DISASTER MANAGEMENT</b> (Common to all branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	AC	2	0	0	0

<b>Course Objectives</b>	<ul style="list-style-type: none"><li>• To become familiar in key concepts and consequences about hazards, disaster and area of occurrence.</li><li>• To know the various steps in disaster planning.</li><li>• To create awareness on disaster preparedness and management.</li></ul>		
<b>UNIT – I</b>	<b>INTRODUCTION</b>	<b>6 Periods</b>	
Disaster: Definition, Factors and Significance; Difference between Hazard and Disaster; Natural and Manmade Disasters: Difference, Nature, Types and Magnitude. Areas prone to ,EarthquakesFloods ,Droughts, Landslides , Avalanches ,Cyclone and Coastal Hazards with Special Reference to Tsunami.			
<b>UNIT – II</b>	<b>REPERCUSSIONS OF DISASTERS AND HAZARDS</b>	<b>6 Periods</b>	
Economic Damage, Loss of Human and Animal Life, Destruction of Ecosystem. Natural Disasters: Earthquakes, Volcanisms, Cyclones, Tsunamis, Floods, Droughts and Famines, Landslides and Avalanches, Man-made disaster: Nuclear Reactor Meltdown, Industrial Accidents, Oil Slicks and Spills, Outbreaks of Disease and Epidemics, War and Conflicts.			
<b>UNIT – III</b>	<b>DISASTER PLANNING</b>	<b>6 Periods</b>	
Disaster Planning-Disaster Response Personnel roles and duties, Community MitigationGoals, Pre-Disaster Mitigation Plan, Personnel Training, Comprehensive Emergency Management, Early Warning Systems.			
<b>UNIT – IV</b>	<b>DISASTER PREPAREDNESS AND MANAGEMENT</b>	<b>6 Periods</b>	
Preparedness: Monitoring of Phenomena Triggering a Disaster or Hazard; Evaluation of Risk: Application of Remote Sensing, Data from Meteorological and other Agencies, Media Reports: Governmental and Community Preparedness.			
<b>UNIT – V</b>	<b>RISK ASSESSMENT</b>	<b>6 Periods</b>	
Disaster Risk: Concept and Elements, Disaster Risk Reduction, Global and National Disaster Risk Situation. Techniques of Risk Assessment, Global Co-Operation in Risk Assessment and Warning, People’s Participation in Risk Assessment, Strategies for Survival.			
<b>Contact Periods:</b>			
<b>Lecture: 30 Periods    Tutorial: 0 Periods    Practical: 0 Periods    Total: 30 Periods</b>			

#### REFERENCES:

1	R. Nishith, Singh AK, <i>"Disaster Management In India: Perspectives, Issues And Strategies"</i> , New Royal book Company, 2007.
2	Sahni, Pardeep Et.Al. (Eds.), <i>"Disaster Mitigation Experiences And Reflections"</i> , Prentice Hall Of India, New Delhi, 2010
3	Goel S. L, <i>"Disaster Administration And Management Text And Case Studies"</i> , Deep &Deep Publication Pvt. Ltd., New Delhi, 2008.
4	Jagbir Singh, <i>"Disaster Management: Future Challenges And Opportunities"</i> , I.K. International Publishing House Pvt. Ltd., New Delhi, 2007.
5	Damon Coppola <i>"Introduction To International Disaster Management"</i> , Butterworth-Heinemann, 2015
6	Ryan Lanclos <i>"Dealing With Disasters: Gis For Emergency Management"</i> , ESRI Press 2021.

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Differentiate hazard and disaster with their significance.	K4
C02	Analyse the causes and impact of natural and manmade disaster.	K4
C03	Execute the steps involved in disaster planning.	K4
C04	Predict vulnerability of disaster and to prevent, mitigate their impact.	K4
C05	Prepare risk assessment strategy for national and global disaster.	K4

<b>COURSE ARTICULATION MATRIX</b>					
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>
C01	2	1	1	2	2
C02	1	2	1	1	1
C03	1	1	1	2	2
C04	1	1	1	2	2
C05	2	1	1	2	2
<b>23AEACZ2</b>	1	1	1	2	2
1 – Slight, 2 – Moderate, 3 – Substantial					

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	50	50					100
CAT2			100				100
Individual Assessment 1/Case Study 1/Seminar 1/Project 1	50	50					100
Individual Assessment 2/Case Study 2/Seminar 2/Project 2			100				100
ESE	25	25	50				100

<b>23AEACZ3</b>	<b>VALUE EDUCATION</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	AC	2	0	0	0

<b>Course Objectives</b>	<ul style="list-style-type: none"><li>• Value of education and self- development</li><li>• Requirements of good values in students</li><li>• Importance of character</li></ul>	
<b>UNIT – I</b>	<b>ETHICS AND SELF-DEVELOPMENT</b>	<b>6 Periods</b>
Social values and individual attitudes. Work ethics, Indian vision of humanism. Moral and non-moral valuation. Standards and principles. Value judgements.		
<b>UNIT – II</b>	<b>PERSONALITY AND BEHAVIOR DEVELOPMENT</b>	<b>6 Periods</b>
Soul and Scientific attitude. Positive Thinking. Integrity and discipline. Punctuality, Love and Kindness.Avoid fault Thinking. Free from anger, Dignity of labour. Universal brotherhood and religious tolerance.		
<b>UNIT – III</b>	<b>VALUES IN HUMAN LIFE</b>	<b>6 Periods</b>
Importance of cultivation of values, Sense of duty. Devotion, Self-reliance. Confidence, Concentration. Truthfulness, Cleanliness. Honesty, Humanity. Power of faith, National Unity. Patriotism. Love for nature,Discipline.		
<b>UNIT – IV</b>	<b>VALUES IN SOCIETY</b>	<b>6 Periods</b>
True friendship. Happiness Vs suffering, love for truth. Aware of self-destructive habits. Association andCooperation. Doing best for saving nature.		
<b>UNIT – V</b>	<b>POSITIVE VALUES</b>	<b>6 Periods</b>
Character and Competence –Holy books vs Blind faith. Self-management and Good health. Science of reincarnation. Equality, Nonviolence, Humility, Role of Women. All religions and same message. Mind your Mind, Self-control. Honesty, Studying effectively.		
<b>Contact Periods:</b>		
<b>Lecture: 30 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 30 Periods</b>		

#### REFERENCES :

1	Chakroborty, S.K. <i>“Values and Ethics for organizations Theory and practice”</i> , Oxford University Press, New Delhi, 1998
2	Dr. Yogesh Kumar Singh, <i>“Value Education”</i> , A.P.H Publishing Corporation, New Delhi, 2010
3	R.P Shukla, <i>“Value Education and Human Rights”</i> , Sarup and Sons, New Delhi, 2004
4	<a href="https://nptel.ac.in/courses/109104068/36">https://nptel.ac.in/courses/109104068/36</a>

<b>COURSE OUTCOMES :</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Know the values and work ethics.	K3
C02	Enhance personality and behavior development.	K3
C03	Apply the values in human life.	K3
C04	Gain Knowledge of values in society.	K3
C05	Learn the importance of positive values in human life.	K3

<b>COURSE ARTICULATION MATRIX</b>						
<b>Cos/Pos</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	-	-	3	-	-	1
C02	-	-	3	-	-	1
C03	-	-	3	-	-	1
C04	-	-	3	-	-	1
C05	-	-	3	-	-	1
<b>23AEACZ3</b>	-	-	<b>3</b>	-	-	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN - THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20%	50%	30%	-	-	-	100%
CAT2	20%	50%	30%	-	-	-	100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1	20%	50%	30%	-	-	-	100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2	20%	50%	30%	-	-	-	100%
ESE	20%	50%	30%	-	-	-	100%

<b>23VLACZ4</b>	<b>CONSTITUTION OF INDIA</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	AC	2	0	0	0

<b>Course Objectives</b>	<ul style="list-style-type: none"><li>• To address the importance of constitutional rights and duties</li><li>• To familiarize about Indian governance and local administration.</li><li>• To know about the functions of election commission.</li></ul>	
<b>UNIT – I</b>	<b>INDIAN CONSTITUTION</b>	<b>6 Periods</b>
History of Making of the Indian Constitution: History Drafting Committee, (Composition & Working) - Philosophy of the Indian Constitution: Preamble Salient Features.		
<b>UNIT – II</b>	<b>CONSTITUTIONAL RIGHTS &amp; DUTIES</b>	<b>6 Periods</b>
Contours of Constitutional Rights & Duties: Fundamental Rights , Right to Equality, Right to Freedom, Right against Exploitation, Right to Freedom of Religion, Cultural and Educational Rights, Right to Constitutional Remedies, Directive Principles of State Policy, Fundamental Duties.		
<b>UNIT – III</b>	<b>ORGANS OF GOVERNANCE</b>	<b>6 Periods</b>
Organs of Governance: Parliament, Composition, Qualifications and Disqualifications, Powers and Functions, Executive, President, Governor, Council of Ministers, Judiciary, Appointment and Transfer of Judges, Qualifications, Powers and Functions.		
<b>UNIT – IV</b>	<b>LOCAL ADMINISTRATION</b>	<b>6 Periods</b>
Local Administration: District's Administration head: Role and Importance, Municipalities: Introduction, Mayor and role of Elected Representative, CEO of Municipal Corporation. Panchayat raj: Introduction, PRI: Zila Panchayat. Elected officials and their roles, CEO Zila Panchayat: Position and role. Block level: Organizational Hierarchy (Different departments), Village level: Role of Elected and Appointed officials, Importance of grass root democracy.		
<b>UNIT – V</b>	<b>ELECTION COMMISSION</b>	<b>6 Periods</b>
Election Commission: Election Commission: Role and Functioning. Chief Election Commissioner and Election Commissioners. State Election Commission: Role and Functioning. Institute and Bodies for the welfare of SC/ST/OBC and women.		
<b>Contact Periods:</b>		
<b>Lecture: 30 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 30 Periods</b>		

#### REFERENCES:

1	<i>"The Constitution of India", 1950 (Bare Act), Government Publication.</i>
2	<i>Dr. S. N. Busi, Dr. B. R. Ambedkar "Framing of Indian Constitution", 1st Edition, 2015.</i>
3	<i>M. P. Jain, "Indian Constitution Law", 7th Edn., Lexis Nexis, 2014.</i>
4	<i>D.D. Basu, "Introduction to the Constitution of India", Lexis Nexis, 2015.</i>

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Discuss the growth of the demand for civil rights in India.	K2
C02	Discuss the intellectual origins of the framework of argument that informed the conceptualization of social reforms leading to revolution in India.	K2
C03	Understand the various organs of Indian governance.	K2
C04	Familiarize with the various levels of local administration.	K2
C05	Gain knowledge on election commission of india.	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	-	-	1	1	1	1
C02	-	-	1	1	1	2
C03	-	-	1	1	2	1
C04	-	-	1	1	1	1
C05	-	-	1	1	1	1
<b>23AEACZ4</b>	-	-	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20%	50%	30%	-	-	-	100%
CAT2	20%	50%	30%	-	-	-	100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1	20%	50%	30%	-	-	-	100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2	20%	50%	30%	-	-	-	100%
ESE	20%	50%	30%	-	-	-	100%

23VLACZ5	<b>PEDAGOGY STUDIES</b> (Common to all Branches)
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	AC	2	0	0	0

<b>Course Objectives</b>	<ul style="list-style-type: none"><li>• To understand of various theories of learning, prevailing pedagogical practices and design of curriculum in engineering studies.</li><li>• Application of knowledge in modification of curriculum, its assessment and introduction of innovation in teaching methodology.</li></ul>		
<b>UNIT – I</b>	<b>INTRODUCTION</b>	<b>6 Periods</b>	
Introduction and Methodology: Aims and rationale, Policy background, Conceptual framework and terminology Theories of learning, Curriculum, Teacher education. Conceptual framework, Research questions. Overview of methodology and Searching.			
<b>UNIT – II</b>	<b>PEDAGOGICAL PRACTICES</b>	<b>6 Periods</b>	
Thematic overview: Pedagogical practices are being used by teachers in formal and informal classrooms in developing countries. Curriculum, Teacher education. Evidence on the effectiveness of pedagogical practices Methodology for the in depth stage: quality assessment of included studies.			
<b>UNIT – III</b>	<b>PEDAGOGICAL APPROACHES</b>	<b>6 Periods</b>	
How can teacher education (curriculum and practicum) and the school curriculum and guidance materials best support effective pedagogy? Theory of change. Strength and nature of the body of evidence for effective pedagogical practices. Pedagogic theory and pedagogical approaches. Teacher’s attitudes and beliefs and Pedagogic strategies.			
<b>UNIT – IV</b>	<b>PROFESSIONAL DEVELOPMENT</b>	<b>6 Periods</b>	
Professional development: alignment with classroom practices and follow-up support. Peer support Support from the head teacher and the community. Curriculum and assessment Barriers to learning: limited resources and large class sizes.			
<b>UNIT – V</b>	<b>CURRICULUM AND ASSESSMENT</b>	<b>6 Periods</b>	
Research gaps and future directions Research design Contexts Pedagogy Teacher education Curriculum and assessment Dissemination and research impact.			
<b>Contact Periods:</b>			
<b>Lecture: 30 Periods      Tutorial: 0 Periods      Practical: 0 Periods      Total: 30 Periods</b>			

#### REFERENCES:

1	Ackers J, Hardman F , <i>Classroom interaction in Kenyan primary schools,Compare</i> , 31 (2): 245-261, 2001.
2	Alexander RJ , <i>Culture and pedagogy: International comparisons in primary education</i> . Oxford and Boston: Blackwell, 2001
3	Akyeampong K, Lussier K, Pryor J, Westbrook J, <i>Improving teaching and learning of basic maths and reading in Africa: Does teacher preparation count? International Journal Educational Development</i> , 33 (3): 272-282, 2013.
4	Agrawal M , <i>Curricular reform in schools: The importance of evaluation</i> , <i>Journal of Curriculum Studies</i> , 36 (3): 361-379, 2004

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Explain the concept of curriculum, formal and informal education systems and teacher education.	K3
C02	Explain the present pedagogical practices and the changes occurring in pedagogical approaches	K3
C03	Understand the relation between teacher and community, support from various levels of teachers to students and limitation in resources and size of the class.	K3
C04	Perform research in design a problem in pedagogy and curriculum development.	K3

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	-	-	1	1	2	1
C02	-	-	1	1	1	2
C03	-	-	1	1	2	1
C04	-	-	1	1	2	1
<b>23AEACZ5</b>	-	-	<b>1</b>	<b>1</b>	<b>2</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category *</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20%	50%	30%	-	-	-	100%
CAT2	20%	50%	30%	-	-	-	100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1	20%	50%	30%	-	-	-	100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2	20%	50%	30%	-	-	-	100%
ESE	20%	50%	30%	-	-	-	100%



23AEACZ6	<b>STRESS MANAGEMENT BY YOGA (Common to all Branches)</b>
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PREREQUISITES	CATEGORY	L	T	P	C
NIL	AC	2	0	0	0

<b>Course Objectives</b>	<ul style="list-style-type: none"><li>• To create awareness on the benefits of yoga and meditation.</li><li>• To understand the significance of Asana and Pranayama.</li></ul>		
<b>UNIT – I</b>	<b>PHYSICAL STRUCTURE AND ITS FUNCTIONS</b>	<b>6 Periods</b>	
Yoga - Physical structure, Importance of physical exercise, Rules and regulation of simplified physical exercises, hand exercise, leg exercise, breathing exercise, eye exercise, kapalapathy, maharasana, body massage, acupressure, body relaxation.			
<b>UNIT – II</b>	<b>YOGA TERMINOLOGIES</b>	<b>6 Periods</b>	
Yamas - Ahimsa, satya, astheya, bramhacharya, aparigraha Niyamas- Saucha, santosha, tapas, svadhyaya, Ishvara pranidhana.			
<b>UNIT – III</b>	<b>ASANA</b>	<b>6 Periods</b>	
Asana - Rules & Regulations – Types & Benefits			
<b>UNIT – IV</b>	<b>PRANAYAMA</b>	<b>6 Periods</b>	
Regularization of breathing techniques and its effects-Types of pranayama			
<b>UNIT – V</b>	<b>MIND</b>	<b>6 Periods</b>	
Bio magnetism& mind - imprinting & magnifying – eight essential factors of living beings, Mental frequency and ten stages of mind, benefits of meditation, such as perspicacity, magnanimity, receptivity, adaptability, creativity.			
<b>Contact Periods:</b>			
<b>Lecture: 30 Periods</b>		<b>Tutorial: 0 Periods</b>	<b>Practical: 0 Periods</b>
		<b>Total: 30 Periods</b>	

#### REFERENCES :

1	Janardan Swami Yogabhyasi Mandal , “Yogic Asanas for Group Training-Part-I”, Nagpur.
2	Swami Vivekananda, “Rajayoga or conquering the Internal Nature”, Advaita Ashrama (Publication Department), Kolkata.
3	Pandit Shambu Nath, “Speaking of Stress Management Through Yoga and Meditation”, New Dawn Press, New Delhi, 2016.
4	K. N. Udupa, “Stress and its management by Yoga”, Motilal Banarsidass Publishers, New Delhi, 2007.

<b>COURSE OUTCOMES:</b>		Bloom's Taxonomy Mapped
Upon completion of the course, the students will be able to:		
CO1	Practice physical exercises and maintain good health.	K3
CO2	Attain knowledge on the various concepts of Yoga.	K2
CO3	Perform various asanas with an understanding on their benefits.	K3
CO4	Practice breathing techniques in a precise manner.	K3
CO5	Attain emotional stability and higher level of consciousness.	K2

<b>COURSE ARTICULATION MATRIX</b>					
COs/POs	PO1	PO2	PO3	PO4	PO5
CO1	-	-	-	-	2
CO2	-	-	-	-	3
CO3	-	-	-	-	2
CO4	-	-	-	-	1
CO5	-	-	-	-	1
<b>23AEACZ6</b>	-	-	-	-	<b>2</b>
1 – Slight, 2 – Moderate, 3 – Substantial					

<b>ASSESSMENT PATTERN – THEORY</b>							
Test / Bloom's Category*	Remembering (K1) %	Understanding (K2) %	Applying (K3) %	Analyzing (K4) %	Evaluating (K5) %	Creating (K6) %	Total %
CAT1	40%	30%	30%	-	-	-	100%
CAT2	30%	40%	30%	-	-	-	100%
Individual Assessment1/ Case study1/ Seminar 1/Project1	40%	40%	20%	-	-	-	100%
Individual Assessment2/ Case study2/ Seminar 2 /Project2	30%	30%	40%	-	-	-	100%
ESE	30%	30%	40%	-	-	-	100%

<b>23AEACZ7</b>	<b>PERSONALITY DEVELOPMENT THROUGH LIFE ENLIGHTENMENT SKILLS</b> (Common to all Branches)
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<b>PREREQUISITES :</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
NIL	AC	2	0	0	0

<b>Course Objectives</b>	<ul style="list-style-type: none"> <li>To familiar with Techniques to achieve the highest goal in life.</li> <li>To become a person with stable mind, pleasing personality and determination.</li> </ul>
<b>UNIT – I</b>	<b>6 Periods</b>
Neetisatakam-Holistic development of personality-Verses- 19,20,21,22 (wisdom)-Verses29,31,32 (pride & heroism)-Verses- 26,28,6.	
<b>UNIT – II</b>	<b>6 Periods</b>
Verses- 52,53,59 (don't's)-Verses- 71,73,75,78 (do's). - Approach to day to day work and duties.- Shrimad BhagwadGeeta - Chapter 2-Verses 41, 47,48,	
<b>UNIT – III</b>	<b>6 Periods</b>
Shrimad BhagwadGeeta -Chapter 3-Verses 13, 21, 27, 35, Chapter 6-Verses 5,13,17, 23, 35,- Chapter 18-Verses 45, 46, 48.	
<b>UNIT – IV</b>	<b>6 Periods</b>
Statements of basic knowledge.-Shrimad BhagwadGeeta: -Chapter2-Verses 56, 62, 68 -Chapter 12 - Verses 13, 14, 15, 16,17, 18-Personality of Role model.	
<b>UNIT – V</b>	<b>6 Periods</b>
Shrimad BhagwadGeeta: Chapter2-Verses 17, Chapter 3-Verses 36,37,42, Chapter 4-Verses 18, 38,39- Chapter18 – Verses 37,38,63.	
<b>Contact Periods:</b> <b>Lecture: 30 Periods    Tutorial: 0 Periods    Practical: 0 Periods    Total: 30 Periods</b>	

#### REFERENCES :

1	Swami SwarupanandaAdvaita Ashram " <b>Srimad Bhagavad Gita</b> ",AdvaitaAshrama, Kolkata,2016
2	P.Gopinath, Rashtriya Sanskrit Sansthanam " <b>Bhartrihari's Three Satakam</b> " (Niti-sringar-vairagya), New Delhi, 1986.
3	Swami Mukundananda, JagadguruKripalujiYog " <b>Bhagavad Gita: The Song Of God</b> ", USA,2019
4	A.C. Bhaktivedanta Swami Prabhupada " <b>Bhagavad-Gita As It Is</b> ",Bhaktivedanta Book Trust Publications,2001

<b>COURSE OUTCOMES:</b>		<b>Bloom's Taxonomy Mapped</b>
Upon completion of the course, the students will be able to:		
C01	Apply the Holistic development in life	K4
C02	Effective Planning of day to day work and duties	K4
C03	Identify mankind to peace and prosperity	K4
C04	Develop versatile personality.	K4
C05	Awakening wisdom in life	K4

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>P01</b>	<b>P02</b>	<b>P03</b>	<b>P04</b>	<b>P05</b>	<b>P06</b>
C01	-	-	1	-	-	-
C02	-	-	1	-	-	-
C03	-	-	1	-	-	-
C04	-	-	1	-	-	-
C05	-	-	1	-	-	-
<b>23AEACZ7</b>	-	-	<b>1</b>	-	-	-
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Remembering (K1) %</b>	<b>Understanding (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluating (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20%	50%	30%	-	-	-	100%
CAT2	20%	50%	30%	-	-	-	100%
Individual Assessment 1 /Case Study 1/ Seminar 1 / Project1	20%	50%	30%	-	-	-	100%
Individual Assessment 2 /Case Study 2/ Seminar 2 / Project 2	20%	50%	30%	-	-	-	100%
ESE	20%	50%	30%	-	-	-	100%

<b>23VLACZ8</b>	<b>SANSKRIT FOR TECHNICAL KNOWLEDGE</b> (Common to all Branches)
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<b>PREREQUISITES:</b>	<b>CATEGORY</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>NIL</b>	<b>AC</b>	<b>2</b>	<b>0</b>	<b>0</b>	<b>0</b>

<b>Course Objectives</b>	<ul style="list-style-type: none"><li>• To get a working knowledge in illustrious Sanskrit, the scientific language in the world.</li><li>• Learning of Sanskrit to improve brain functioning.</li><li>• Enhancing the memory power.</li><li>• Learning of Sanskrit to develop the logic in mathematics, science &amp; other subjects.</li></ul>		
<b>UNIT – I</b>	<b>BASICS OF SANSKRIT</b>	<b>6 Periods</b>	
Alphabets in Sanskrit, Past/Present/Future Tense.			
<b>UNIT – II</b>	<b>SENTENCES AND ROOTS</b>	<b>6 Periods</b>	
Simple Sentences - Order, Introduction of roots			
<b>UNIT – III</b>	<b>SANSKRIT LITERATURE</b>	<b>6 Periods</b>	
Technical information about Sanskrit Literature			
<b>UNIT – IV</b>	<b>TECHNICAL CONCEPTS -1</b>	<b>6 Periods</b>	
Technical concepts of Engineering-Electrical, Mechanical			
<b>UNIT – V</b>	<b>TECHNICAL CONCEPTS -2</b>	<b>6 Periods</b>	
Technical concepts of Engineering-Architecture, Mathematics			
<b>Contact Periods:</b>			
<b>Lecture: 30 Periods      Tutorial: 0 Periods      Practical: 30 Periods      Total: 30 Periods</b>			

#### REFERENCES:

1	Dr.Vishwas, " <b>Abhyaspustakam</b> ", Samskrita -Bharti Publication, New Delhi, 2020.
2	Prathama Deeksha Vempati Kutumbshastri, " <b>Teach Yourself Sanskrit</b> ", Rashtriya Sanskrit Sansthanam, New Delhi, Publication, 2009.
3	Suresh Soni, " <b>India's Glorious Scientific Tradition</b> ", Ocean books (P) Ltd., New Delhi,2006.

<b>COURSE OUTCOMES:</b> Upon completion of the course, the students will be able to:		<b>Bloom's Taxonomy Mapped</b>
C01	Recognize ancient literature and their basics	K3
C02	Formulate the sentences with order and understand the roots of Sanskrit	K2
C03	Acquire familiarity of the major traditions of literatures written in Sanskrit	K3
C04	Distinguish the Technical concepts of Electrical & Mechanical Engineering	K2
C05	Categorize the Technical concepts of Architecture & Mathematics	K2

<b>COURSE ARTICULATION MATRIX</b>						
<b>COs/POs</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>
C01	-	-	-	1	2	1
C02	-	-	-	1	2	-
C03	-	-	-	1	1	1
C04	-	-	-	2	1	1
C05	-	-	-	1	2	1
<b>23AEACZ8</b>	-	-	-	<b>1</b>	<b>2</b>	<b>1</b>
1 – Slight, 2 – Moderate, 3 – Substantial						

<b>ASSESSMENT PATTERN – THEORY</b>							
<b>Test / Bloom's Category*</b>	<b>Rememberin g (K1) %</b>	<b>Understandin g (K2) %</b>	<b>Applying (K3) %</b>	<b>Analyzing (K4) %</b>	<b>Evaluatin g (K5) %</b>	<b>Creating (K6) %</b>	<b>Total %</b>
CAT1	20%	50%	30%	-	-	-	100%
CAT2	20%	50%	30%	-	-	-	100%
Individual Assessme nt 1 /Case Study 1/ Seminar 1 / Project1	20%	50%	30%	-	-	-	100%
Individual Assessme nt 2 /Case Study 2/ Seminar 2 / Project 2	20%	50%	30%	-	-	-	100%
ESE	20%	50%	30%	-	-	-	100%